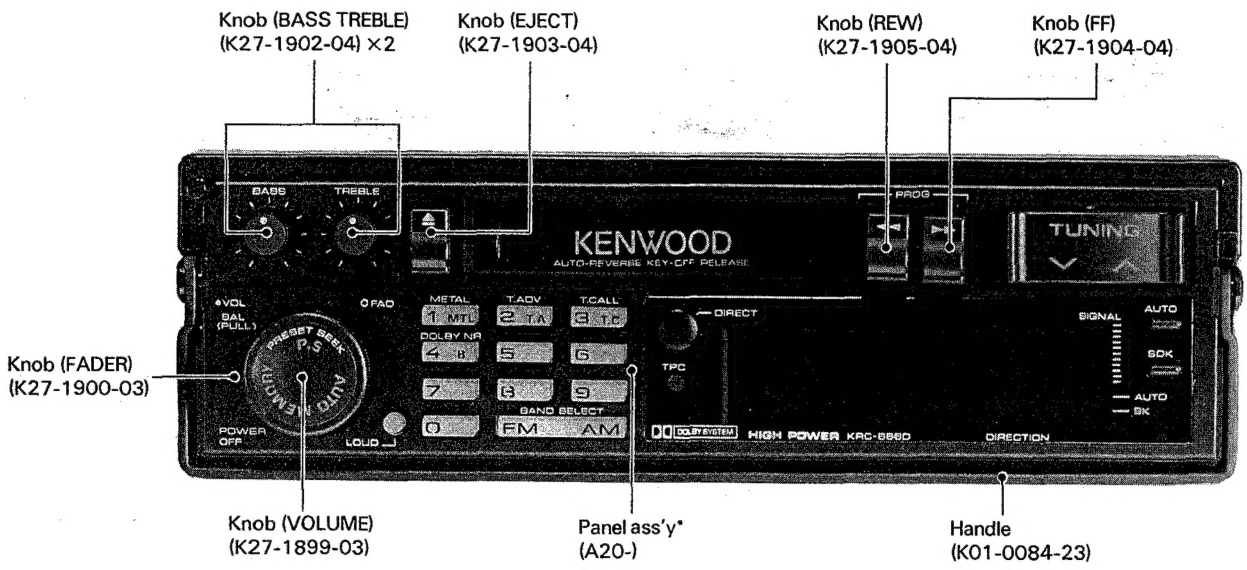
CASSETTE-RECEIVER

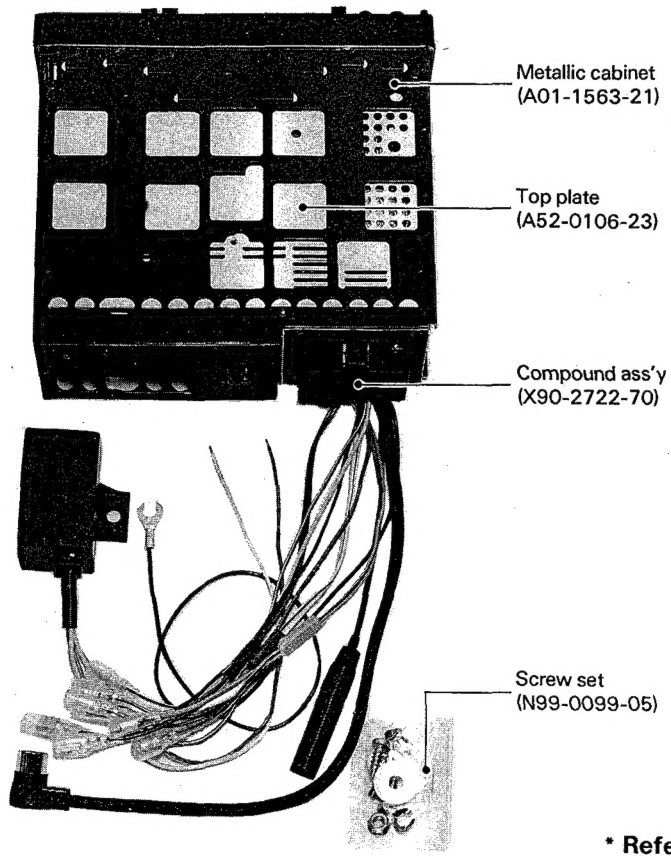
KRC-666D/L

SERVICE MANUAL

KENWOOD

B51-3550-00(B)1490





* Refer to parts list on page 57.

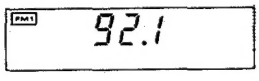
CONTENTS

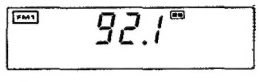
OPERATION

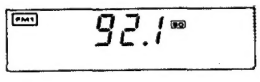
■ Frequency display

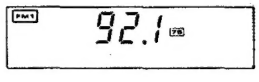
- For FM, final frequencies 25 kHz, 50 kHz and 75 kHz are represented respectively by [25], [50], and [75] on the frequency display.

For example:

92,100 MHz ⇨ 

92,125 MHz ⇨ 

92,150 MHz ⇨ 

92,175 MHz ⇨ 

■ Automatic lighting illumination

- When the unit is inserted into the detachable case, the illumination around the POWER switch lights up for about 30 seconds, even when the power is set to OFF, to indicate the location.
- When the engine is started with the unit inserted into the detachable case, the illumination around the POWER switch light up for about 30 seconds, even when the power is set to OFF, to indicate the location.

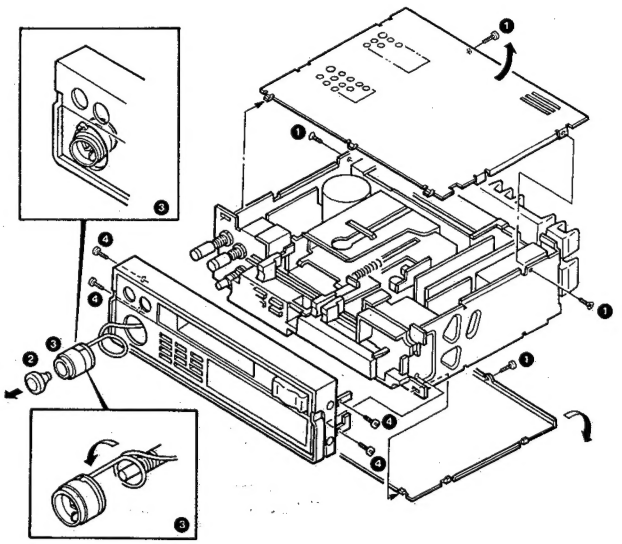
CAUTION:
This function lights the illumination by detecting variation in the battery voltage. Therefore, the illumination could light up also when another electrical car accessory is switched ON or OFF.

■ This unit generates beeps in the following cases.

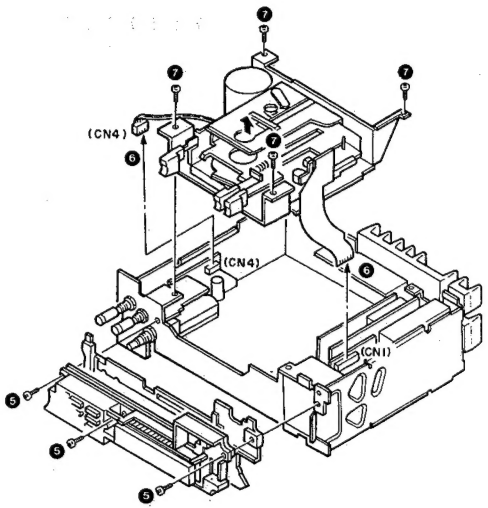
Operation	Number of Beeps
When a cassette tape is loaded	Once
When a cassette tape is ejected	Once
When storing into the 10-key memory	Once
When direct tuning is finished	Once
When Auto Memory starts	Once
When a program is stored with Auto Memory function	Once
When Auto Memory finishes	Twice
When Preset Scan finishes	Twice

DISASSEMBLY FOR REPAIR

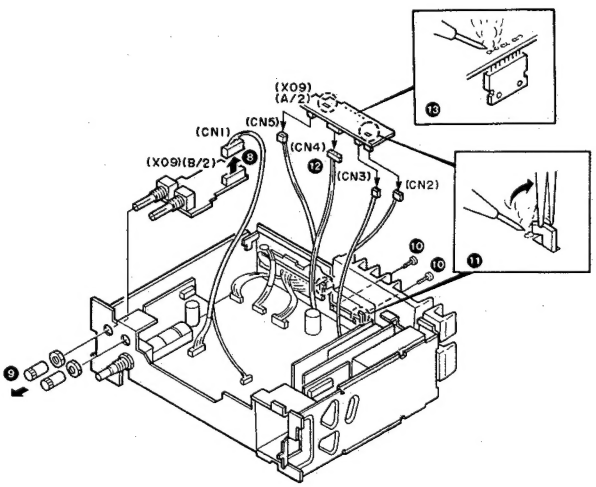
1. Remove the 4 screws ❶ and then take off the top cover and bottom cover.
2. Remove the volume knob ❷.
3. Remove the FADER knob ❸.
   Note: Be careful not to cut the wires connected to the FADER knob.
   How to attach the FADER knob
   (1) Align the FADER knob shaft with the center.
   (2) Insert the FADER knob by turning it once counter-clockwise (be sure to insert the knob with the lamp downward).
4. Remove the 4 screws ❹, and then take off the front panel while paying attention to the FADER knob.
   Note: When attaching the front panel, also attach the FADER knob through the hole provided on the front panel. Also be careful so that the TPC knob on the front panel is not caught when the front panel is attached.



5. Remove the 3 screws ❺, and then take off the Switch Unit.
6. Disconnect the connector (CN4) and flexible cable from the Mechanism Ass'y ❻.
7. Remove the 4 screws ❼, and then take off the Mechanism Ass'y.



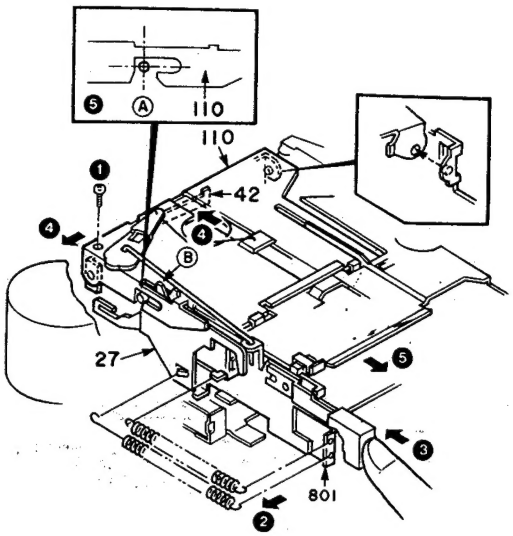
8. Disconnect the connector (CN1) from the Audio Unit (X09-, B/2) ❽.
9. Remove the 2 knobs and 2 nuts ❾, and then take off the Audio Unit.
10. Remove the 2 screws holding the Power IC ❿.
11. Remove the solder from the tongue of the Audio Unit (X09-, A/2), extend the tongue using pliers, and then take off the Audio Unit ⓫.
12. Disconnect the connectors (CN2, CN3, CN4, CN5) from the Audio Unit ⓬.
13. Remove the solder from the Power IC pins, and then take off the Power IC ⓭.



DISASSEMBLY FOR REPAIR

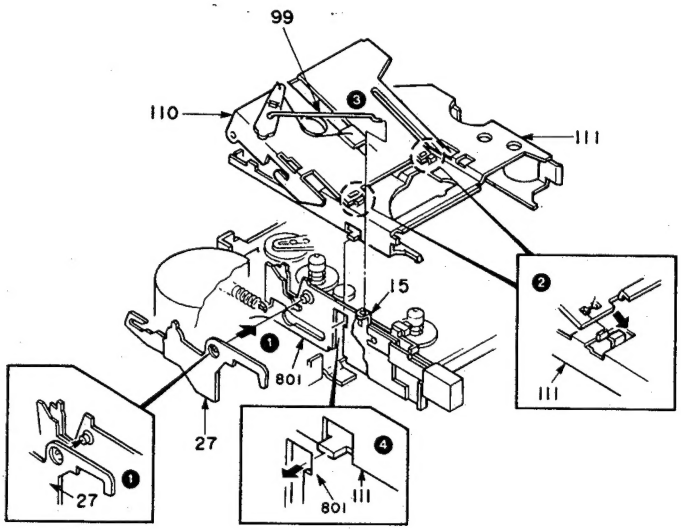
DISASSEMBLY OF HOLDER (ACTION PLATE)

1. Remove the screw (❶) from the holder (action plate [110]).
2. Remove the two springs (❷) from mechanism chassis (801).
3. Hold down the EJECT button (❸) with one hand.
4. Press the lever (42) with your other hand, move the holder (action plate [110]) toward the motor, and remove the lever (42) from projection Ⓑ of the mechanism chassis (801) (❹).
5. Push the holder (action plate [110]) forward. When the projection of the arm (action [27]) reaches point Ⓐ, release the EJECT button (❺).

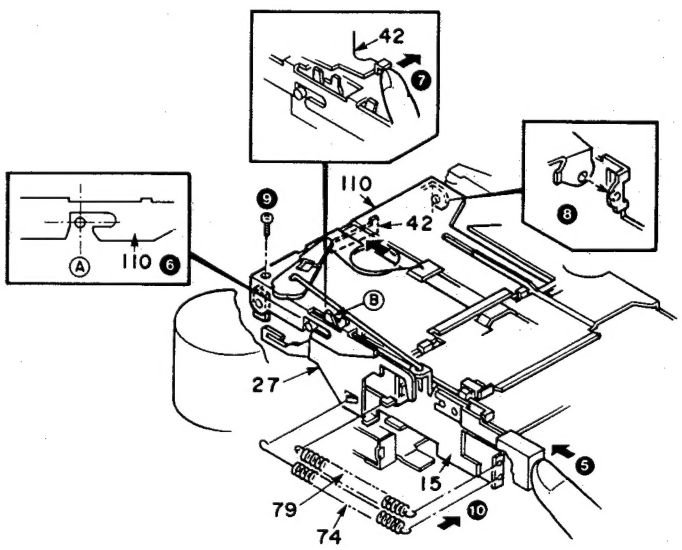


REASSEMBLY OF HOLDER (ACTION PLATE)

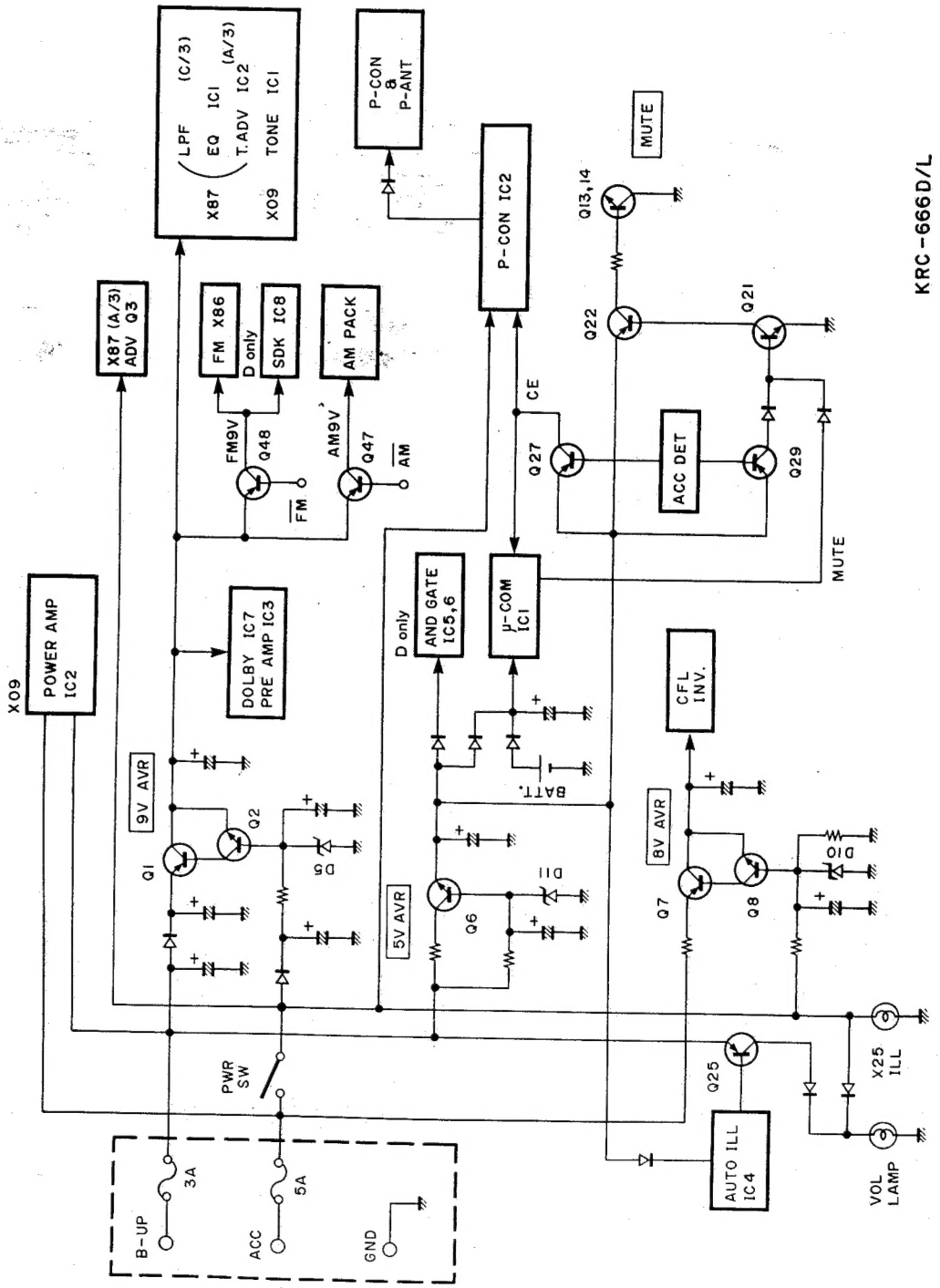
1. Align the projection of the mechanism chassis (801) with the hole in the arm (action [27]) (❶).
2. Align the two tips (❷) of the holder (action plate [110]) with the claws of the holder (cassette case [111]).
3. Insert the formed wire (99) (❸) into the hole of the lever ass'y (EJECT) (15).
4. Align the left claw of the holder (cassette case [111]) with the mechanism chassis (❹).



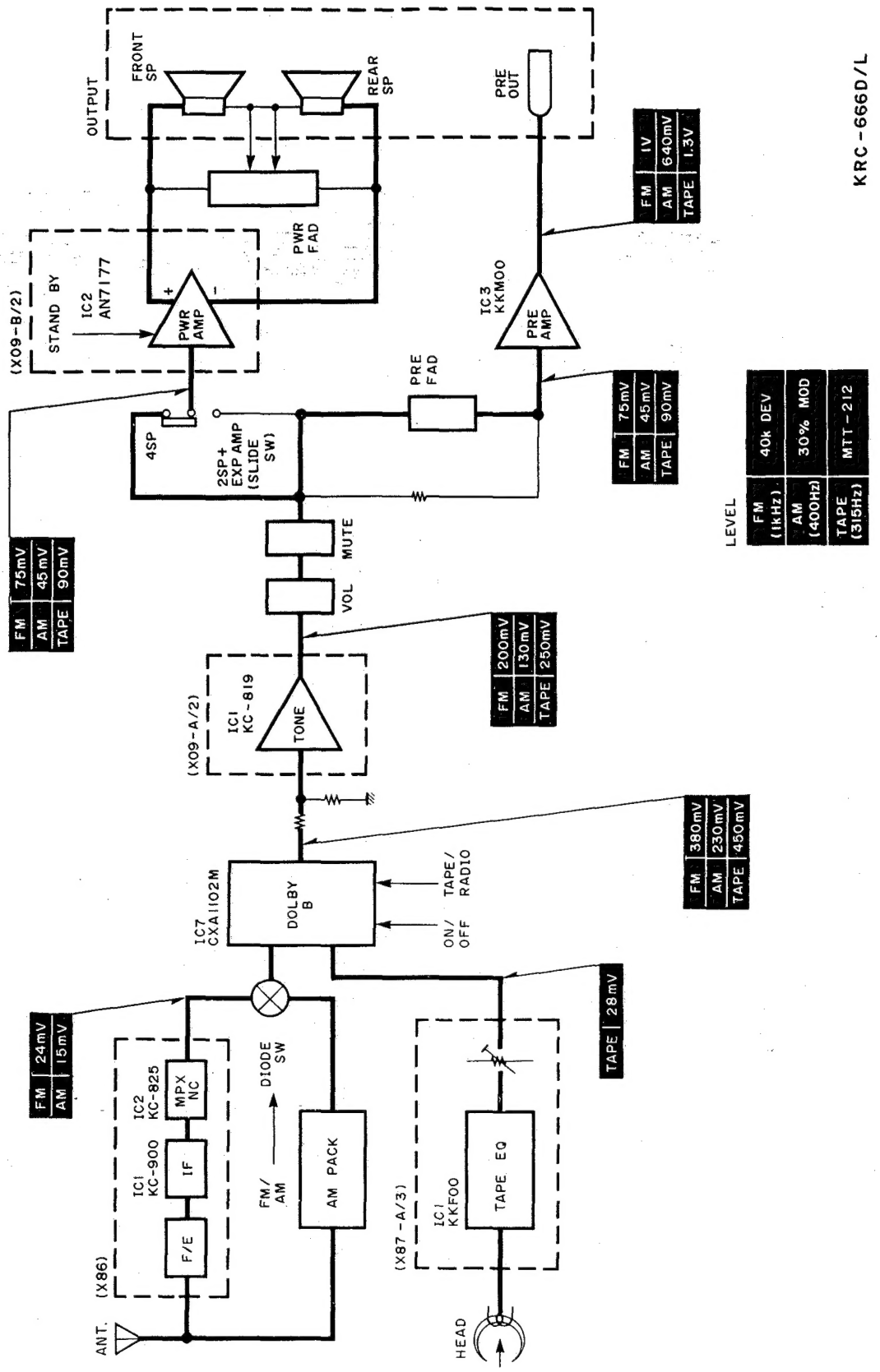
5. While holding the arm (action [27]) with one hand, push the EJECT button (❺).
6. Insent the projection of the arm (action [27]) into point Ⓐ in the side of the holder (action plate [110]) (❻).
7. Push the lever (42) outward (❼) take out the projection of the mechanism chassis (801) from the hole in the holder (action plate [110]).Release the EJECT button.
8. Align the projection (❽) of the mechanism chassis (801) with the hole of holder (action plate [110]) with the screw.
9. Secure the holder (action plate [110]) with the screw (❾).
10. Mount the tention springs (79) and (74) on the mechanism chassis (801) (❿).



BLOCK DIAGRAM



BLOCK LEVEL DIAGRAM



CIRCUIT DESCRIPTION

Audio Unit X09-2672-70

Ref. No.	Parts No.	Use and Function	Operation
IC1	KC-819	Tone (Bass, Treble) HIC	Tone controls.
IC2	AN-7177	Power IC	18 W + 18 W (BTL × 2-ch)
Q11	DTC144EK	STAND-BY CONTROL	Turns OFF when the unit is in operation.

Synthesizer Unit X14-3342-70

Ref. No.	Parts No.	Use and Function	Operation
IC1	μPD1719G-551-11	Master microcomputer	Refer to the attached sheet.
IC2	KKQ00	P-Com output protection	Refer to the attached sheet.
IC3	KKM00	DIN amp	Refer to the attached sheet.
IC4	μPD4001BG-T1	Power fluctuation detection +1 AND circuit	Refer to the attached sheet.
IC5	μPD4081BG-T1	4 AND circuits (exclusively for SDK)	Alternate switch **(D only)**
IC6	μPD4081BG-T1	4 AND circuits (TAPE, DIR, ST, FF/REW)	Alternate switch
IC7	CXA1102M	Dolby B-type NR decoder	Refer to the attached sheet.
IC8	KKC00	SDK HIC (hi-Cut)	Refer to the attached sheet. **(D only)**
Q1	2SB1015	9 V (power supply for audio circuit) AVR, Darlington circuit	Outputs 8.5 V constant voltage when power is ON.
Q2	2SC2412K		
Q3	DTC124EK	DK OUT inversion	Outputs low level signal to PRE, OUT DK pin on DK interruption. **(D only)**
Q4	DTC124EK	RST IN inversion	Inverts RST signal from outside.
Q5	2SA1037K	For power supply fluctuation	Refer to the attached sheet.
Q6	2SD1055F	5 V (for logic circuits) AVR	
Q7	2SB1015	8 V (for CFL INV) AVR Darlington circuit	Outputs 8.2 V constant voltage when power is ON.
Q8	2SC2412K		
Q9, 10	2SD1757K	DK VOL UP inhibition	Normally ON. Turns OFF to increase volume when DK is engaged. **(D only)**
Q11, 12	2SD1757K	LOUD ON/OFF	Q11 and 12 are turned OFF when LOUD is ON.
Q13, 14	2SD1757K	MUTE	Turns ON when MUTE is activated.
Q15	DTA124EK	Pack In signal inversion	Outputs high level signal when Pack In is engaged.
Q16	DTA124EK	Tape Mute signal inhibition in Radio mode	In Radio mode, Q16 and 17 are turned OFF to inhibits Tape Mute signal.
Q17	DTC124EK		
Q18	DTC124EK	SD inhibition	Inhibits SD signal when FM MUTE is activated (when detuned, etc.).
Q19	DTC124EK	AGC time contstant select switching	Turns OFF when AGC Cut is engaged. Normally turned ON to supply 9 V.
Q20	DTA124EK		
Q21	DTC124EK	MUTE driver	When MUTE is activated, Q21 and 22 are turned ON to drive Mute transistor in 5 V.
Q22	2SA1037K		
Q23	2SA1037K	LOUD driver	Turns ON when LOUD is ON, to drive Q11 and 12.
Q24	DTC124EK	DK VOL UP inversion	Turns ON when DK is ON, to turn Q9 and 10 OFF. **(D only)**
Q25	2SB822F	Lamp illumination driver on power fluctuation	These turn ON by detecting the power fluctuation, to supply lamp +B power.
Q26	DTC124EK		
Q27	2SB822F	CE (chip enable) output	Normally, both turn ON to output CE (5 V) when power is ON.
Q28	2SC2412K		
Q29	DTA144EK	KEY OFF MUTE output	Outputs high level signal when power switch is OFF or on power failure.
Q30	DTC143TK	Buffer beep tone	Output pulse signal when Beep tone is activated.
Q31	DTA144EK	MUTE output inversion	Outputs pre-out mute signal (high level) when tuning, etc.
Q32	2SC2412K	AM SD switching	
Q33	DTC144WK	FM SD switching	

CIRCUIT DESCRIPTION

Ref. No.	Parts No.	Use and Function	Operation
Q34	DTC124EK	Dolby ON/OFF	Turns ON to output low level signal when Dolby B-type NR is engaged.
Q35	DTC144EK	MONO/ST select	Turns ON to output low level signal when MONO is ON. **(L only)**
Q36	DTC644EK	MONO/ST select	Turns ON to supply +B power when ST is ON. **(L only)**
Q37	DTC144WK	ST indicator switch	Turns OFF to output high level signal when ST is ON.
Q38	2SC2412K	DIRECTION inversion	Turns ON to output low level signal when REV is ON.
Q42	DTC124EK	AGC CUT	Turns ON when AGC CUT is engaged.
Q43	DTA124EK	AGC CUT (AM)	Turns ON when AGC CUT is engaged.
Q44	2SA1037K	SD buffer (AM)	
Q45	DTA124EK	Band select switch	Turns ON to supply 9 V in MW mode.
Q46	DTC124EK	SK output inhibition	Turns ON to inhibit SK output when FM MUTE is engaged. **(D only)**
Q47	2SB822F	AM +B power supply	Supplies AM +B power in AM mode.
Q48	2SB822F	FM +B power supply	Supplies FM +B power in FM mode.

Front-end Unit X86-1092-70

Ref. No.	Parts No.	Use and Function	Operation
IC1	KC-900	FM IF amp, DET	Refer to the attached sheet (New Parts).
IC2	KC-825	FM MPX, NC	Decoder, Noise canceller HIC
Q2	2SC2413K	IF amp	IF amplifier
Q3	2SC2412K	Buffer	For high-cut, separator
Q4	2SC2412K	CRSC switching	
Q5	2SA1037K	Buffer	For signal meter output voltage

Playback Amplifier Unit X87-1232-70

Ref. No.	Parts No.	Use and Function	Operation
IC1	KKF00	Head amp (EQ) HIC	Refer to the attached sheet (New Parts).
IC2	AN6263N	T. ADV. (Tape Advance)	Gap detection, Plunger driver
Q1	DTC144EK	T. ADV. ON/OFF	Turns ON when the "T. ADV." key is activated in FF/REW mode.
Q2	2SC2412K (S)	Plunger driver	Gap detection. Both turn ON to drive plunger in STOP mode.
Q3	2SA1428		
Q4	2SC2412K (S)	Tape Mute signal inversion	Turns OFF to output high level signal in FF, REW, PRG mode.
Q21	2SA1428	Motor +B power supply	Both turn ON to supply Motor +B power (14 V) in Tape (Pack) IN mode.
Q22	2SC2412K (S)		
Q23	DTC144EK	DK, RST release	Stops motor on DK interruption or RST mode.
Q24	DTC144EK	Pack IN	Turns ON to output low level signal in Pack In mode.
Q31	DTC144EK	AM +B siwtching	Turns ON to output low level signal in MW/LW mode.
Q32	DTC144EK	Band switch	Turns ON to output low level signal in MW mode.
Q33	DTC144EK	FM +B siwtching	Turns ON to output low level signal in FM mode.
Q34	DTC144EK	FM & MW LPF inhibition	Turns ON to inhibit FM & MW LPF output in LW mode.
Q35	2SC2412K (S)	LPF (low pass filter)	Consists MW & FM LPF (low pass filter)
Q36	2SC2412K (S)		
Q37	DTC144EK	LW LPF inhibition	Turns ON to inhibit LW LPF output in FM, LW mode.
Q38	2SC2412K (S)	LPF (low pass filter)	Consists LW LPF (low pass filter)
Q39	2SC2412K (S)		

CIRCUIT DESCRIPTION

Automatic Illumination Circuit

1. Function

Even when the power switch is turned OFF, starting the engine or inserting the detaching/attaching set into the detaching/attaching case lights up the illumination of the power switch section.

2. Operation

a. When the engine is started:

When the engine is started, the battery voltage is lower to 2—3 V generally.

* Backup voltage V_{BU}
* D7 Vf
* C10 charge voltage Va
* Q5 base current Ib
* Q5 V_{BE}
* R9 Rs ohms

In here,

$$Va - V_{BU} > V_{BE} + Vf + Ib \times Rs \quad \ldots\ldots\ldots\ldots\ldots (1)$$

If equation (1) is satisfied, Q5 turns ON and the high level signal is generated by R10 and C12.

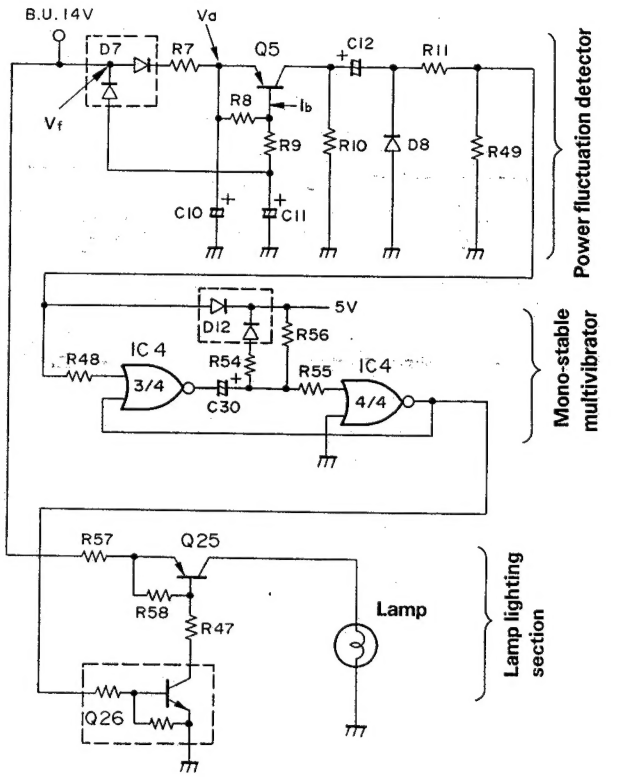
This pulse is applied to the mono-stable multivibrator, and the high level signal is output for the period set by C30 and R56 to turn ON Q26 and Q25 so that the lamp lights up for a fixed period.

b. When detaching/attaching set is inserted:

When V_{BU} is applied, Q5 is turned ON while C11 is being charged by the time constant of R9 and C11. As a result, the pulse is generated to light up the lamp in the same way as a.

3. Cautions

With the characteristics of this circuit, when the backup voltage is dropped by turning on the wiper or other electrical equipment such as light or air-conditioner, if equation (1) is satisfied, lamp may be lit mistakenly even when the engine is not started.



KEY-ON, KEY-OFF, Power Failure Mute

1. Object

This is the MUTE signal generator circuit, which activate muting function for a fixed period when the power is turned ON (ACC or POWER switch), and activates muting function instantly when the power is turned OFF (ACC or POWER switch).

2. Operation

(1) $V_1 = V_{+B} - V_{D1}$ (same amount as Zener effect)

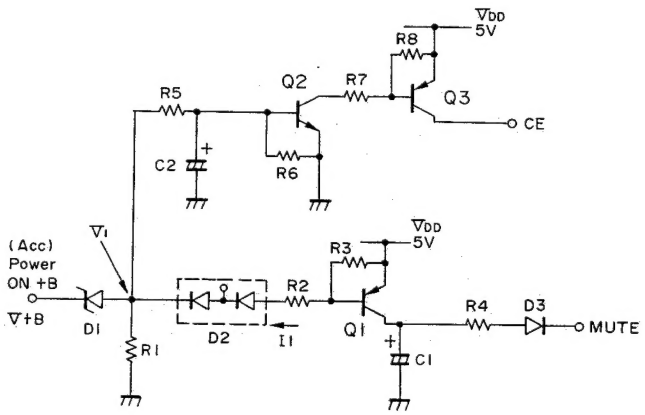
$\ldots\ldots\ldots\ldots\ldots\ldots\ldots\ldots$ (1) (V_{DD}: 5 V for backup)

$V_1 + V_{D2} + I1 \cdot R2 < V_{DD} - V_{BE}$ $\ldots\ldots\ldots\ldots\ldots$ (2)

When the above equations are satisfied, Q1 is turned ON to output the MUTE signal via D3.

That is;

* When the power is OFF:
  Q1 turns ON, and MUTE signal is output continuously.
* When the power is ON (rising up):
  Q1 immediately turns OFF, however, MUTE signal is still output for the period as time constant of C1 and R4.
* When the power is OFF (falling down):
  Equation (2) is satisfied, and MUTE signal is output immediately.



* On power failure:
  MUTE signal is output when V+B (about 8 V) satisfying equation (2).
* The same operation will be performed on momentary power failure.

(2) Relationship with CE (chip enable)

* When the voltage (V+B) is lowered, CE is cut off after the MUTE signal has been output. (When the voltage is raised, MUTE is released after CE signal goes high.)
* For timing, CE is cut off when the fixed period has elapsed after MUTE has been output be the time constant of R5 and C2.

CIRCUIT DESCRIPTION

2. Microprocessor μPD1719G-551 (X14-3342-70: IC1)

The μPD1719G-551 includes the prescaler which operates up to 150 MHz, PLL frequency synthesizer and LCD driver, developed for the FM/MW/LW radio to be used in U.S., Europe, Middle East and Japan.

2-1. Features

(1) 4-bit microcomputer for digital tuning
(2) Prescaler (two modulus prescaler: 150 MHz max.) and PLL circuits incorporated
(3) LCD driver (1/2 duty, 1/2 bias, frame frequency: 100 Hz, 56 segment max.)
(4) 5 V ±10% single power supply
(5) Low power consumption CMOS
(6) Easy to backup data memory (RAM) (with CE pin)
(7) FM1/FM2/MW/LW 4-band tuner (by initial set diode switch)
(8) Auto/manual up/down tuning possible (sawtooth wave sweeping)
(9) Preset memory of up to 10 stations for each band (10 stations for MW + LW combination) (by initial set diode switch)
(10) Preset scanning, auto memory function
(11) Direct station select (by initial set diode)
(12) Compatible with SDK (by initial set diode)
(13) Accommodate to optional output for radio, such as NR, Load and OP
(14) Tuner call possible
(15) Accommodate to optional output for tape such as NR, Load, Metal and Dolby-B/C type, T. ADV.
(16) Accommodate to clock function (by initial set diode switch)

2-2. Outline of Functions

(a) Band plan

Area	Receiving Band	Receiving Frequency Range	Channel Spacing		Reference Frequency	Intermediate Frequency	Local
			Auto	Manual			
U.S.A.	FM	87.9 ~ 107.9 MHz	200 kHz	←	25 kHz	10.7 MHz	Upper
	MW	530 ~ 1620 kHz	10 kHz	←	10 kHz	450 kHz	Upper
Europe	FM	87.5 ~ 108.0 MHz	50 kHz	25 kHz	25 kHz	10.7 MHz	Upper
	MW	531 ~ 1611 kHz	9 kHz	←	9 kHz	450 kHz	Upper
	LW	153 ~ 281 kHz	9 kHz	1 kHz	1 kHz	450 kHz	Upper
Middle East	FM	87.5 ~ 108.0 MHz	50 kHz	25 kHz	25 kHz	10.7 MHz	Upper
	MW	531 ~ 1611 kHz	9 kHz	←	9 kHz	450 kHz	Upper
Japan	FM	76.1 ~ 89.9 MHz	100 kHz	←	25 kHz	10.7 MHz	Lower
	MW	522 ~ 1629 kHz	9 kHz	←	9 kHz	450 kHz	Upper

(b) Initial Setting

(1) Preset memory initialization:

Area	Receiving Band	1	2	3	4	5	6	7	8	9	0
U.S.A.	FM	87.9 MHz	←	←	←	←	←	←	←	←	←
	MW	530 kHz	←	←	←	←	←	←	←	←	←
Europe	FM	87.5 MHz	←	←	←	←	←	←	←	←	←
	MW	531 kHz	←	←	←	←	←	←	←	←	←
	LW		←	←	←	←	←	←	←	←	←
Middle East	FM	87.5 MHz	←	←	←	←	←	←	←	←	←
	MW	531 kHz	←	←	←	←	←	←	←	←	←
Japan	FM	76.1 MHz	←	←	←	←	←	←	←	←	←
	MW	522 kHz	←	←	←	←	←	←	←	←	←

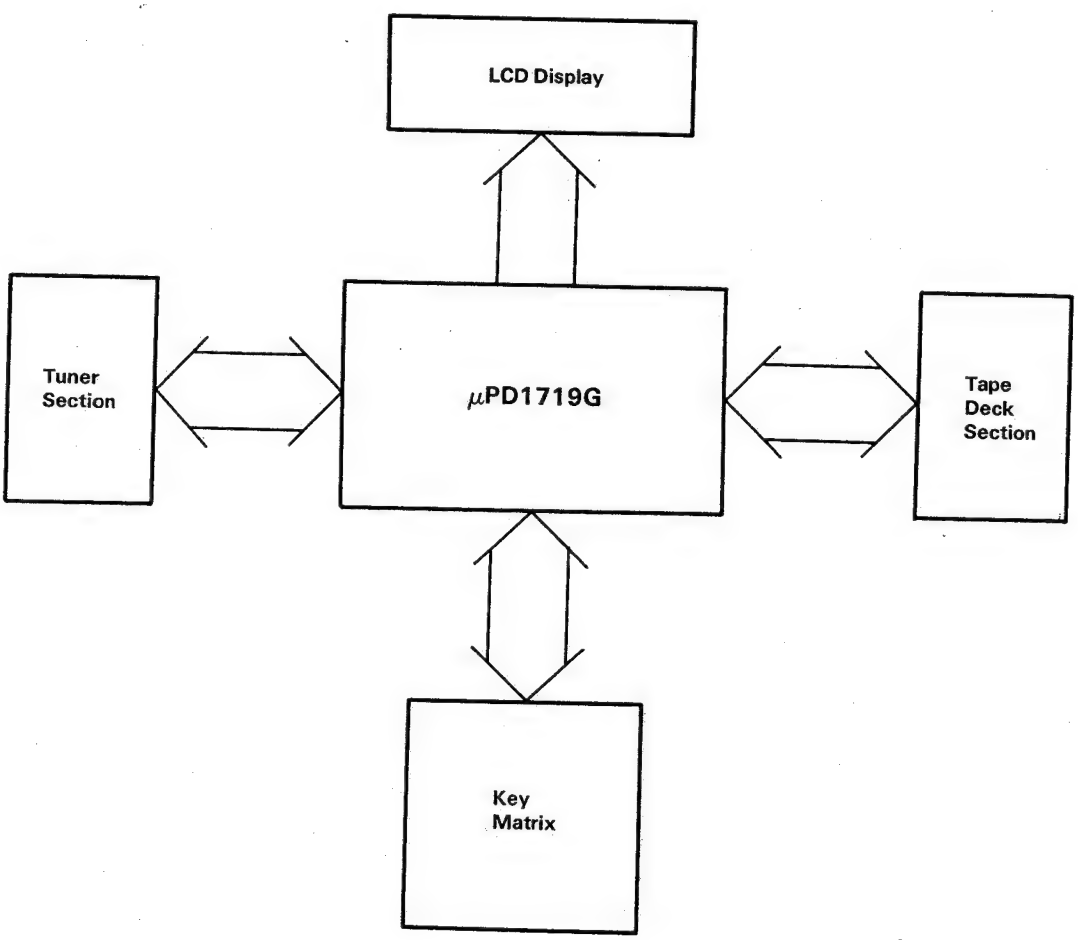
* The lowest frequency is initially stored for all preset memories as shown in the table above.
But no frequency is stored in memory for LW band in Europe version.
* Also for FM2 band, the lowest frequency is stored in memory for all preset memories.

CIRCUIT DESCRIPTION

(2) Other initialization:

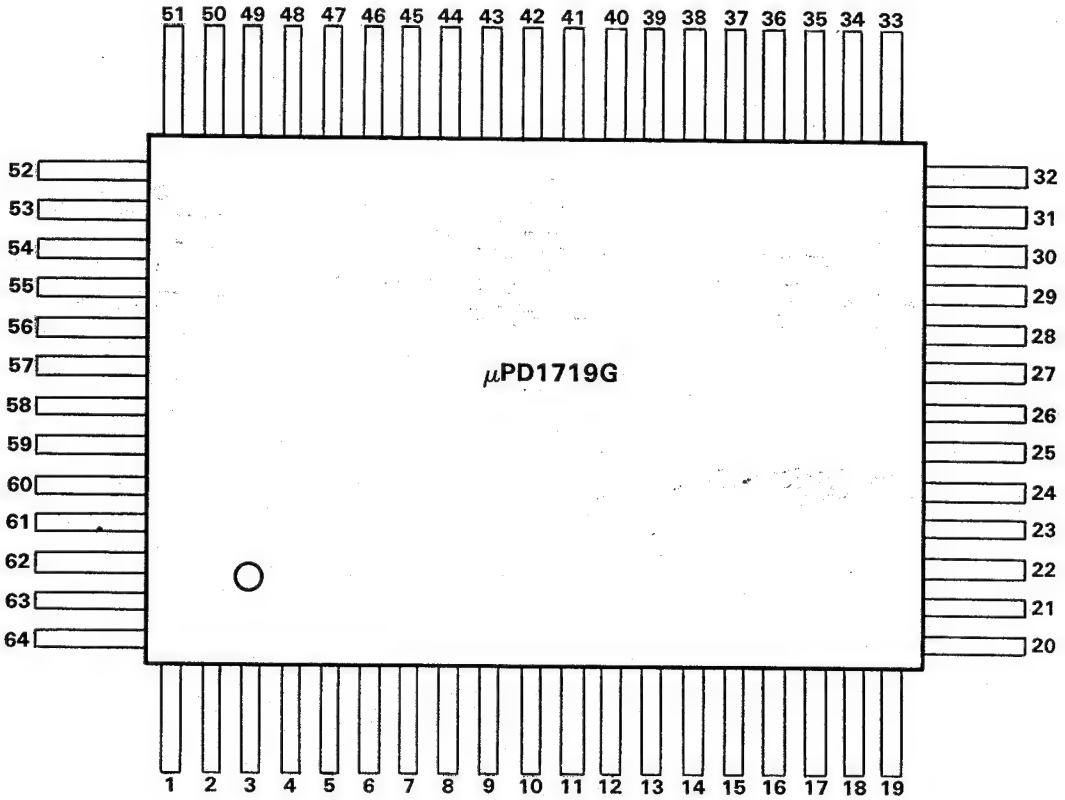
Frequency	Lowest Frequency	op	off	MTL	off
Channel Indication	Blank	NR	Off (for both Radio and Tape)	T-ADV	off
Receiving Band	FM (FM1)	SDK	off	T-call	off
Power	off	Auto	By diode switch	Clock	12:00
Loudress	on	Dolby	Off (for both B and C)		

3-1. Block Diagram



CIRCUIT DESCRIPTION

(a) Pin Connection Diagram (Top View)



Pin No.	Pin Name	Pin No.	Pin Name	Pin No.	Pin Name	Pin No.	Pin Name
1	NC	17	PA1/FMIF	33	LCD22	49	LCD6/KS6
2	E01	18	PA2/SI	34	LCD21	50	LCD5/KS5
3	E02	19	PA3/$\overline{SCK}$	35	LCD20	51	LCD4/KS4
4	GND	20	PBO/SO	36	LCD19	52	LCD3/KS3
5	VCOL (AM)	21	PB1	37	LCD18	53	LCD2/KS2
6	VCOH (FM)	22	PB2	38	LCD17	54	LCD1/KS1
7	CE	23	PB3	39	LCD16	55	LCD0/KS0
8	GND	24	XO	40	LCD15/KS15	56	COM1
9	PD1	25	XI	41	LCD14/KS14	57	COM2
10	PD2	26	VDD	42	LCD13/KS13	58	VDD*
11	PD3	27	CGP	43	LCD12/KS12	59	K3
12	PC0	28	LCD27/PL3	44	LCD11/KS11	60	K2
13	PC1	29	LCD26/PL2	45	LCD10/KS10	61	K1
14	PC2	30	LCD25/PL1	46	LCD9/KS9	62	K0
15	PC3	31	LCD24/PL0	47	LCD8/KS8	63	AD
16	PA0/AMIF	32	LCD23	48	LCD7/KS7	64	$\overline{INT}$

(* Internally connected to pin 26)
(NC: No Connection)

(b) Pin Description

Pin No.	Symbol	Pin Name	Functions	Remarks
1	NC	No. Connection	Although this pin is not connected to the internal chip, it can be used for connection of Open, GND or V_{DD}, etc.	
2 3	EO_1 EO_2	Error Outputs	Error output pin for PLL. When the divided local oscillator frequency is higher than the reference frequency, high level signal is output, and when it is lower than the reference frequency, low level signal is output. When they match with each other, they are floated. This output signal is applied to the external low pass filter (LPF), then applied to the varactor diode. Since the same waveforms are output from E01 and E02, either of these pins can be used.	Three-state CMOS
4, 8	V_{DD}	Power Supply	Power supply pin for device. This supplies 5 V ±10% voltage when the device is in operation. To maintain the internal data memory (RAM) (when the clock stops), the voltage can be lowered to 2.5 V.	
5	AM	AM Local Oscillation Signal Input	Inputs the local oscillator output from 0.6 to 15 MHz (0.3 Vp-p min). This pin operates when the direct dividing system is selected. Since the AC amp is incorporated, it is necessary to cut.	Input
6	FM	FM Local Oscillation Signal Input	Inputs the local oscillator output from 15 to 150 MHz (0.5 Vp-p min). This pin operates when the pulse swallow system is selected. Since the AC amp is incorporated, it is necessary to cut the AC current before input.	Input
7	CE	Chip Enable	Device signal input pin. When operating the device normally, this pin is turned to high. When the device is not used, it is turned to low. While this pin is low level, PLL is inhibited. However, the signal less than 134 μs cannot be accepted. While the clock is not used, when this pin is turned to low, the internal clock generator and CPU stop operation so that the memory is maintained with low-power consumption status (less than 10 μA). When the CE pin is turned high from low, the device is reset and the program restarts from the address 0.	Input
9 ~ 11	PD3 ~ PD1	Port D	3-bit output port. For use in this specifications, refer to the item 3-2 (c) I/O port.	CMOS push-pull
12 ~ 15	PC3 ~ PC0	Port C	4-bit I/O port. When the output command is executed for port C, this port functions as output port, and when the input command is executed, it functions as input port. For use in this specifications, refer to the item 3-2 (c) I/O port.	CMOS push-pull
16 17 18 19	PA0 (AM-IF) PA1 (FM-IF) PA2 (SI) PA3 ($\overline{SCK}$)	Port A	4-bit I/O port. Input/output function can be specified for each bit to this port. These ports can be used as serial interface. At this time, PA3 pin operates as $\overline{SCK}$ (shift clock) pin, and PA2 pin operates as SI (serial input) pin. When PA3 pin is used as $\overline{SCK}$, it is necessary to pull-up PA3 pin using resistor. And PA0, PA1 pins function as the frequency measurement pins. PA0 operates as AM-IF while PA1 operates as FM-IF. The higher limit input frequency is 1 MHz for AM-IF pin, and 12 MHz for FM-IF pin. When the AM-IF pin is selected, the input signal is applied directly to the IF counter. However, when the FM-IF pin is selected, the input signal is applied to the 1/2 divider before inputting to the IF counter. When the reset signal is applied to the device (V_{DD} goes high from low, or CE goes high from low), or when the internal clock is stopped, the input mode is engaged. For use in this specifications, refer to item 3-2 (c) I/O port.	CMOS push-pull
20 ~ 23	PB3 (SO) ~ PB0	Port B	4-bit output port. PB0 pin can be used as SO (serial output) pin for serial interfacing. For use in this specifications, refer to item 3-2 (c) I/O port.	CMOS push-pull
24	XO		Crystal oscillator connect pin. Connect the crystal having 4.5 MHz oscillating frequency to these pins. When adjusting the oscillating frequency (4.5 MHz), perform while observing XO pin output.	CMOS input
25	XI			
26, 58	GND	Ground	Ground (GND) pin for device.	

CIRCUIT DESCRIPTION

Pin No.	Symbol	Pin Name	Functions	Remarks
27	CGP	Clock Generator Port	CGP (Clock Generator Port), or 1-bit output port (PG2). When this pin is used as CGP, either of two modes can be selected depending on the program between VDP (Variable Duty Pulse) function with which the 2.69 kHz pulses are output continuously and the pulse can be varied in 64 steps, and SG (Signal Generator) function with which 180 kHz and 18 kHz reference frequencies are divided into 64 steps (50% duty) are output respectively. When the reset signal is applied to the device (V_{DD} goes high from low, or CE goes high from low), or when the internal clock is stopped, the CGP pin goes low.	CMOS push-pull
28 ~ 55	LCD27 ~ LCD0	LCD Segment Outputs	Segment signal output pins for LCD panel. Up to 56 dots indications can be displayed by the matrix of COM1 and COM2. When these pins are not used as LCD segment signal output pins, pins from LCD27/PL3 to LCD24/PL0 can be used as 4-bit output port. For use in this specifications, refer to item 3-2 (c) I/O port. The display data and the key source signals are output from the LCD15/KS15 to LCD0/KS0 pins in time division system. When the power is turned ON (V_{DD} goes high from low), or when the internal clock is stopped, the low level signal (display off mode) is output automatically.	CMOS push-pull
56 57	COM1 COM2	LCD Common Outputs	Common signal output pin to LCD panel. Up to 56 dots indications are possible with the matrix by LCD0 to 27. Three values of GND, 1/2 Vp-p and V_{DD} are output with an interval of 5 ms in synchronization of 100 Hz. When the potential difference of $\pm V_{DD}$ is generated between LCD 0—27 and these pins, the corresponding segments light up. When the power is turned ON (V_{DD} goes high from low), or when the internal clock is stopped, the low level signal (display off mode) is output automatically.	CMOS push-pull
59 ~ 62	K3 ~ K0	Key Return Signal Outputs	4-bit input port. Normally used for key matrix inputs. For use in this specifications, refer to item 3-2 (c) I/O port.	Input
63	AD	Analog Digital input	A/D (analog to digital) converter input pin. As an A/D converter, the 6-bit successive approximation A/D converter is incorporated with the program. The reference voltage for A/D conversion is V_{DD} (5 V ±10%). For use in this specifications, refer to item 3-2 (c) I/O port.	Input
64	$\overline{INT}$	Interrupt	Interruption request signal input pin. The interruption request signal is issued at the rising edge of the signal applied to this pin. For use in this specifications, refer to item 3-2 (c) I/O port.	Input

(c) I/O Port

Port	Allocation	I/O	Functions
PA0	Power	O	● Power supply control output port. Power ON → High Power OFF → Low
PA1	$\overline{Mute}$	O	● Audio muting output port. Used to cancel the shock noise when the PLL lock is detuned, or the pop noise when switching between Tape and Radio. Mute ON → Low Mute OFF → High
PA2	Tape/$\overline{Radio}$	O	● Audio switching output port for Tape and Radio. In radio mode → Low In tape mode → High However, while in the tape mode, when the radio sound is output such as "tuner call" or "DK interruption", it goes low.
PA3	Loud	O	● Loudness control output port Loudness ON → High Loudness OFF → Low

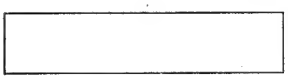
CIRCUIT DESCRIPTION

<table>
<tr><th>Port</th><th>Allocation</th><th>I/O</th><th>Functions</th></tr>
<tr><td>PB0</td><td>Dolby</td><td>O</td><td>• When NR = 0:
This functions as Dolby NR control output port together with B/C (PB1) in tape mode.
<table>
<tr><th>Dolby</th><th>B/C</th><th>Function</th></tr>
<tr><td>0</td><td>0</td><td>off</td></tr>
<tr><td>0</td><td>1</td><td>—</td></tr>
<tr><td>1</td><td>0</td><td>Dolby-B</td></tr>
<tr><td>1</td><td>1</td><td>Dolby-C</td></tr>
</table>
• When NR = 1:
This functions as Noise Reduction control output port.
Noise Reduction ON → High
Noise Reduction OFF → Low</td></tr>
<tr><td>PB1</td><td>B/C</td><td>O</td><td>• When NR = 0:
This functions as Dolby NR control output port together with DOLBY (PB0) in tape mode.
<table>
<tr><th>Dolby</th><th>B/C</th><th>Function</th></tr>
<tr><td>0</td><td>0</td><td>off</td></tr>
<tr><td>0</td><td>1</td><td>—</td></tr>
<tr><td>1</td><td>0</td><td>Dolby-B</td></tr>
<tr><td>1</td><td>1</td><td>Dolby-C</td></tr>
</table>
• When NR = 1:
Not used.</td></tr>
<tr><td>PB2</td><td>MTL</td><td>O</td><td>• Metal tape function control output port.
Metal ON → High Metal OFF → Low</td></tr>
<tr><td>PB3</td><td>T-ADV</td><td>O</td><td>• Tape Advance function control output port.
Tape Advance ON → High
Tape Advance OFF → Low</td></tr>
<tr><td>PC0</td><td>SDK</td><td>O</td><td>• SDK mode select output port.
SDK mode ON → High SDK mode OFF → Low</td></tr>
<tr><td>PC1</td><td>OP</td><td>O</td><td>• Optional functions control output port.
Option ON → High Option OFF → Low</td></tr>
<tr><td>PC2</td><td>DK OUT</td><td>O</td><td>• SDK mode DK interruption function control output port.
During DK interruption → High
Other than above → Low</td></tr>
<tr><td>PC3</td><td>AGC OUT</td><td>O</td><td>• AGC control output port. Outputs high level signal when setting the value N for PLL.
When setting the value N for PLL → High
Other than above → Low</td></tr>
<tr><td>PD1
PD2
PD3</td><td>FM
MW
LW</td><td>O
O
O</td><td>• Tuner band select output port.
<table>
<tr><th>Receiving Band \ Port</th><th>FM</th><th>MW</th><th>LW</th></tr>
<tr><td>FM band</td><td>H</td><td>L</td><td>L</td></tr>
<tr><td>MW band</td><td>L</td><td>H</td><td>L</td></tr>
<tr><td>LW band</td><td>L</td><td>L</td><td>H</td></tr>
</table></td></tr>
<tr><td>PL0 ~ PL3</td><td></td><td></td><td>• Not used.
Used for LCD segment output signal.</td></tr>
<tr><td>K0 ~ K3</td><td></td><td>I</td><td>• Used as the key matrix input.</td></tr>
<tr><td>AD</td><td>SM</td><td>I</td><td>• Signal meter lighting voltage detection input port.</td></tr>
<tr><td>$\overline{INT}$</td><td>SD</td><td>I</td><td>• Station detector input port for tuning.
Station is detected → Low
Other than above → High</td></tr>
<tr><td>CGP</td><td>BZ</td><td></td><td>• Beep tone pulse output port.
Outputs the 2.7273 kHz pulses for 60 ms.</td></tr>
</table>

CIRCUIT DESCRIPTION

(d) Key Structure
(1)Key Matrix

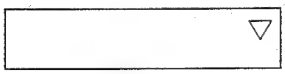
	K3	K2	K1	K0
KS0	NR	Loud	Auto	Power
KS1	SDK	Band	FM	AM
KS2	OP(R)	OP(FM)	SA	P. Scan
KS3	CLK	Seek	Down	UP
KS4	1 / Metal	2 / T-ADV	3 / T-Call	4 / Dolby-B
KS5	5 / Dolby-C	6	7	8
KS6	9	0	Direct	
KS8	Tape	Dir	FF/Rew	
KS9	DK	SK-Start	SK-Stop	ST
KS10	Band A ▽	Band B ▽		
KS11	POWSCT ▽	FM2 Band ▽	SDKSCT ▽	T-ADVSCT ▽
KS12	CLKSCT ▽	AUTOSCT ▽	Dolby ▽	NRSCT ▽



Momentary key

Alternate switch

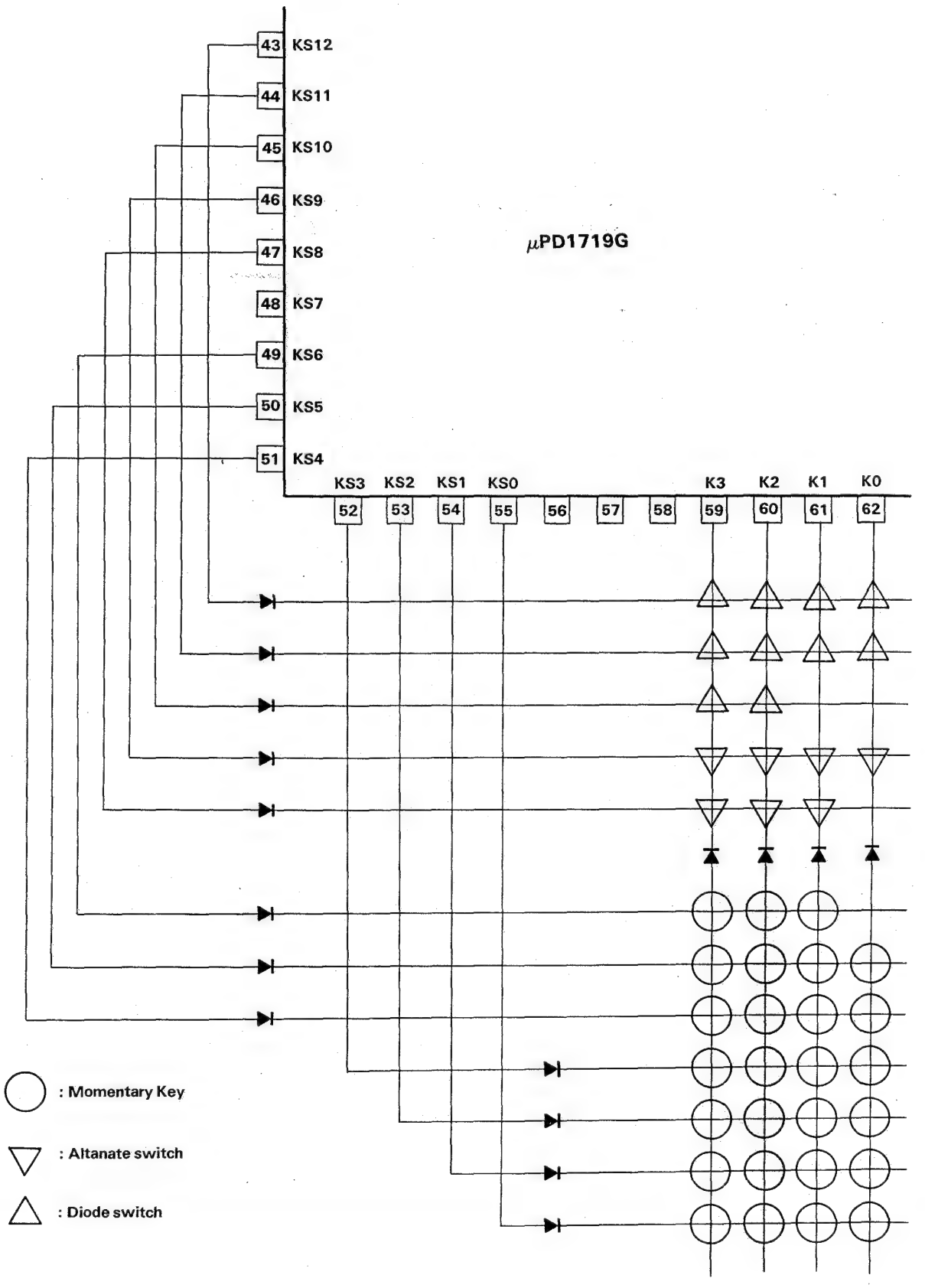
Momentary key (dual function)



Initial set diode switch

CIRCUIT DESCRIPTION

(2) Key Matrix Connection and Type of Switch



CIRCUIT DESCRIPTION

(e) Key Description

(1) Initial set diode:

There are nine types for initial set diodes. These are read out only when the reset signal is applied to the device (V_{DD} goes high from low, or CE goes high from low).

1) Area specifying switch
   Band A, Band B
3) Power ON/OFF method select switch
   POWSCT
4) Switch for setting the number of FM Band
   FM2 Band
5) Switch to select the presence of SDK mode
   SDKSCT
6) Switch to select the presence of Tape Advance function
   T-ADVSCT
7) Switch to select the presence of clock function
   CLKSCT
8) Auto mode initial set switch.
   AUTOSCT
9) Switch to set the presence of Dolby-B/C and NR functions.
   Dolby, NRSCT

These setting should be made by short-circuiting the crossing point on the matrix using diode, or leaving it open.

In the table below, "0" shows open status, while "1" should that it is short-circuited by diode.

<table>
<tr><th>Name</th><th>Functions</th></tr>
<tr><td>Band A
Band B</td><td>These switches are used to set the area (version of the model). By combination of tow diodes, the model can be specified for each area (version).
<table>
<tr><th>Bnad A</th><th>Band B</th><th>Area</th></tr>
<tr><td>0</td><td>0</td><td>U.S.A.</td></tr>
<tr><td>0</td><td>1</td><td>Japan</td></tr>
<tr><td>1</td><td>0</td><td>Europe</td></tr>
<tr><td>1</td><td>1</td><td>Middle East</td></tr>
</table>
When the U.S.A. or Middle East is selected, the area designation can be exchanged each other by operating the momentary switch. For its operation, refer to the description about "1" to "0" in the item 3-2 (e)-(3). For each band plan, refer to item 2-2 (a).</td></tr>
</table>

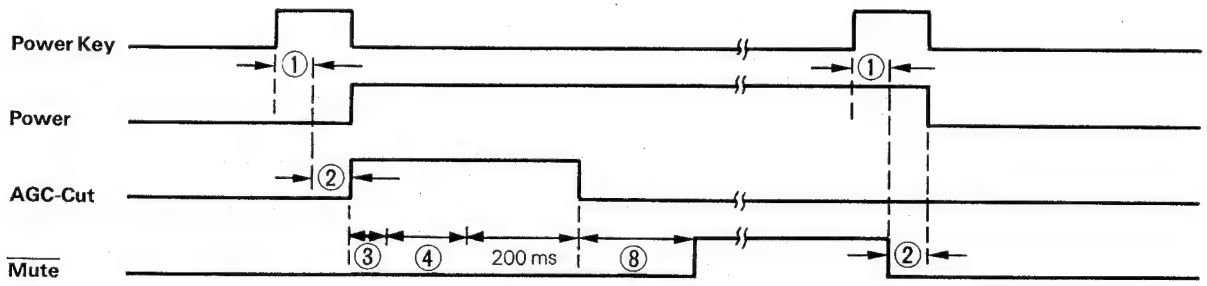
CIRCUIT DESCRIPTION

3-5. MUTE TIMING CHART

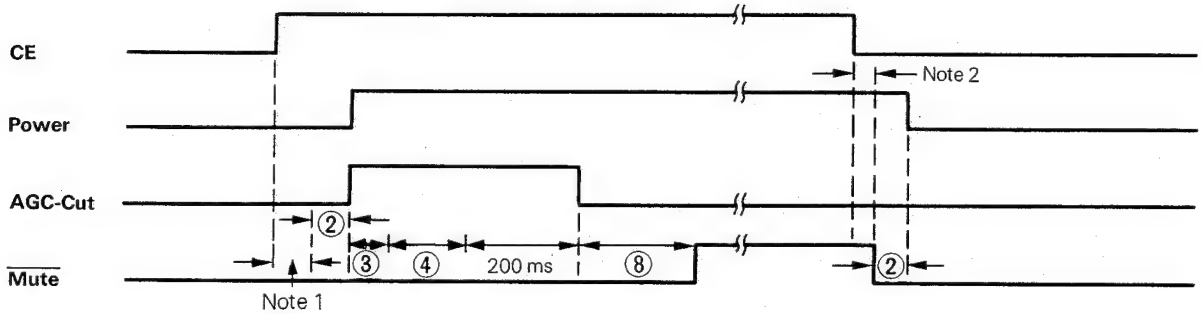
① Key-ON chattering prevention period: About 50 ms
② Pre-output Mute period: About 50 ms
③ PLL data set, output and display renewal time:
④ PLL lock-up time:
⑤ Time till SD detection after PLL lock-up
FM for Europe, Middle East version (25, 50 kHz step): About 25 ms
FM for other areas (100, 200 kHz step): About 50 ms
MW, LW: About 125 ms
Band edge (for all bands): About 200 ms
⑥ Time till second SD detection: About 50 ms
⑦ Time till SK detection: 250—375 ms
⑧ Pose-output Mute period: 250—375 ms

(1) Power ON/OFF:

Power ON/OFF by key operation



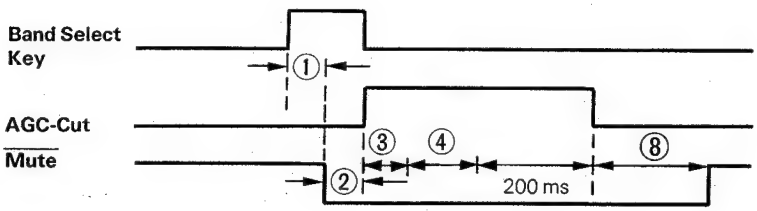
Power ON/OFF by CE pin



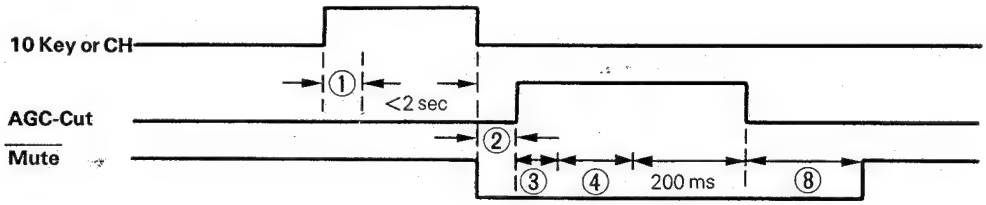
Note 1: Program does not start for 125 ms max. by rising time timing. CE rising processing will be performed.
Note 2: Low level CE signal of less than 134 us will not be accepted.

CIRCUIT DESCRIPTION

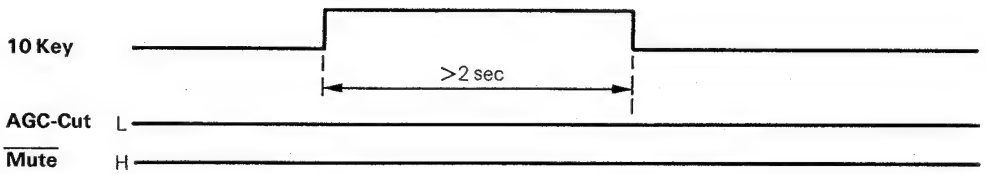
(2) Band Select:



(3) Preset Channel Recall

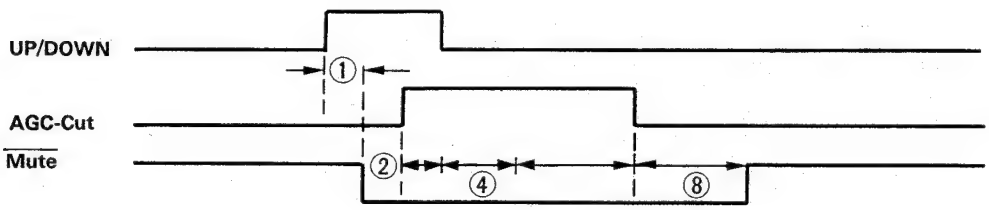


(4) Preset Memory Write

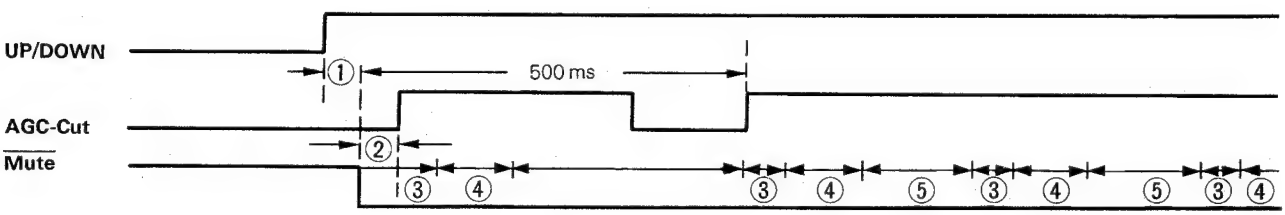


(5) Manual UP/DOWN

Depressing (ON) of more than 500 ms

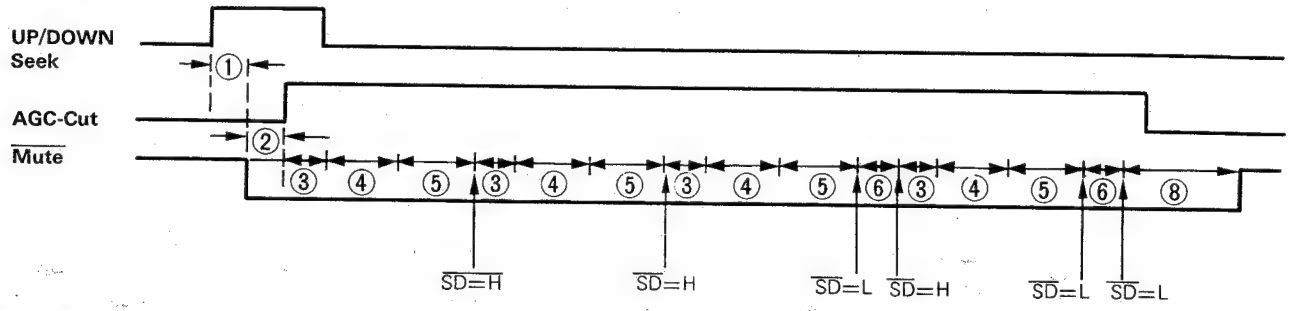


Depressing (ON) of less than 500 ms ③



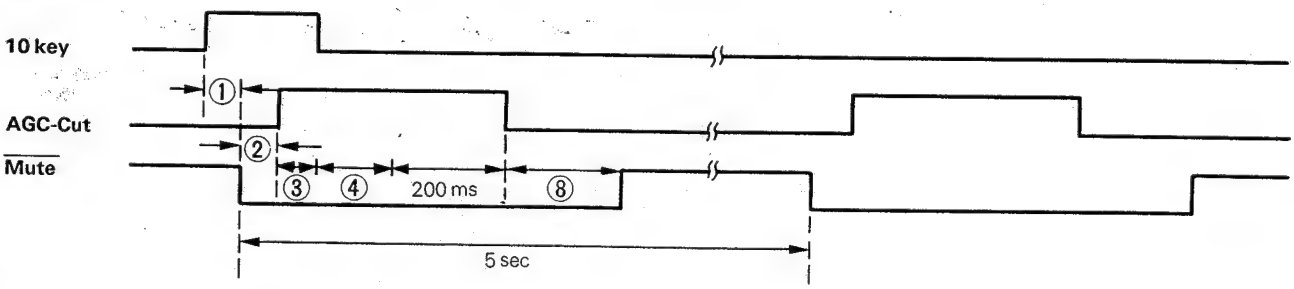
CIRCUIT DESCRIPTION

(6) Auto UP/DOWN

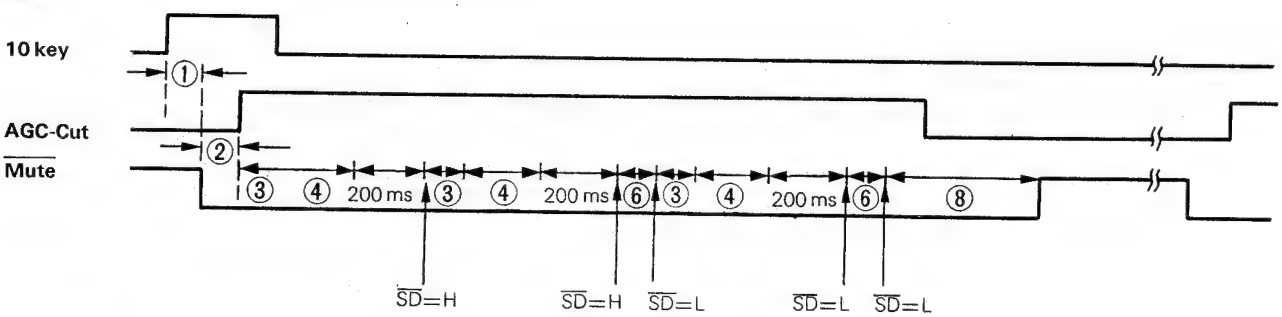


(7) Preset Scan

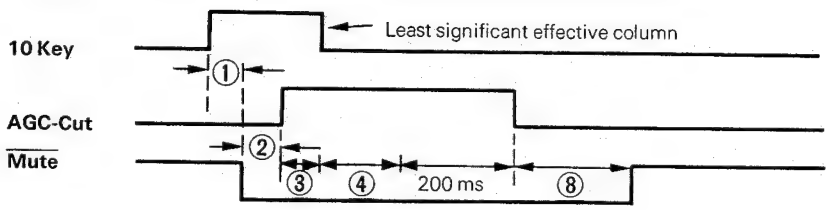
Manual mode



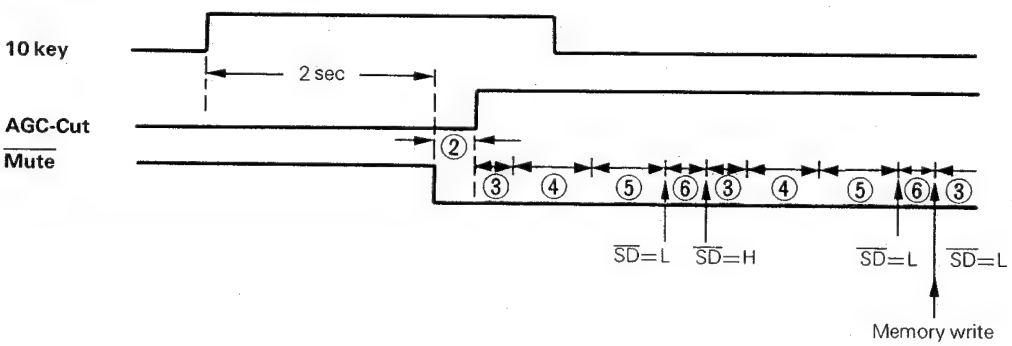
Auto mode



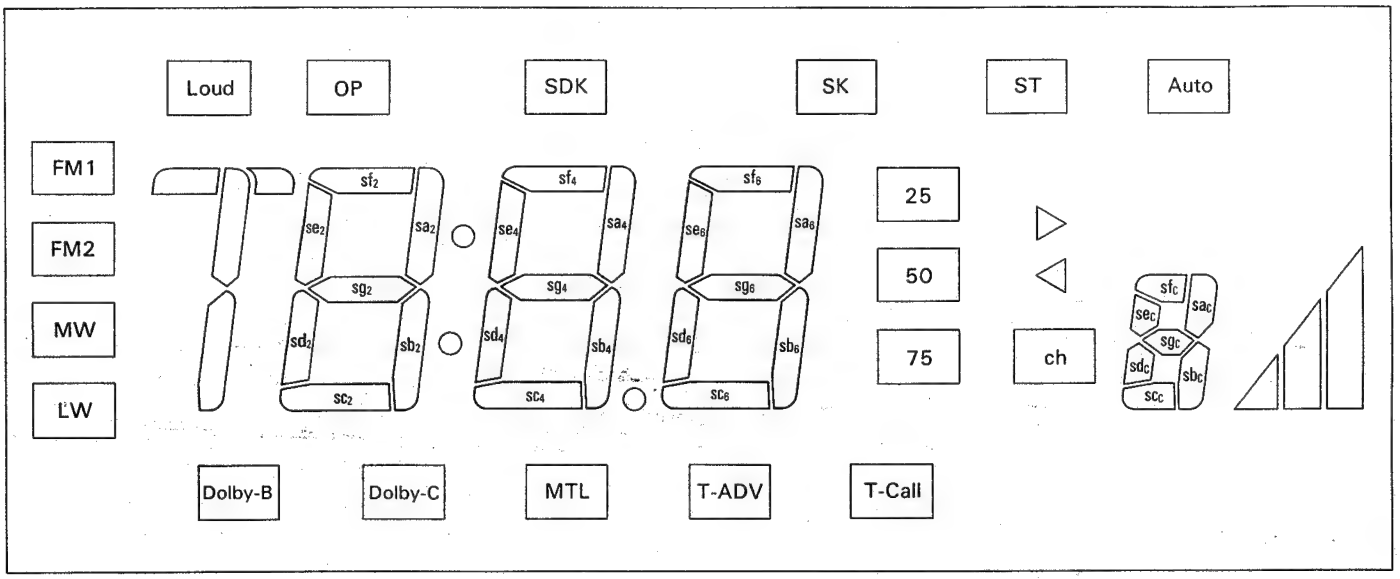
(8) Direct Access



(9) Auto Memory



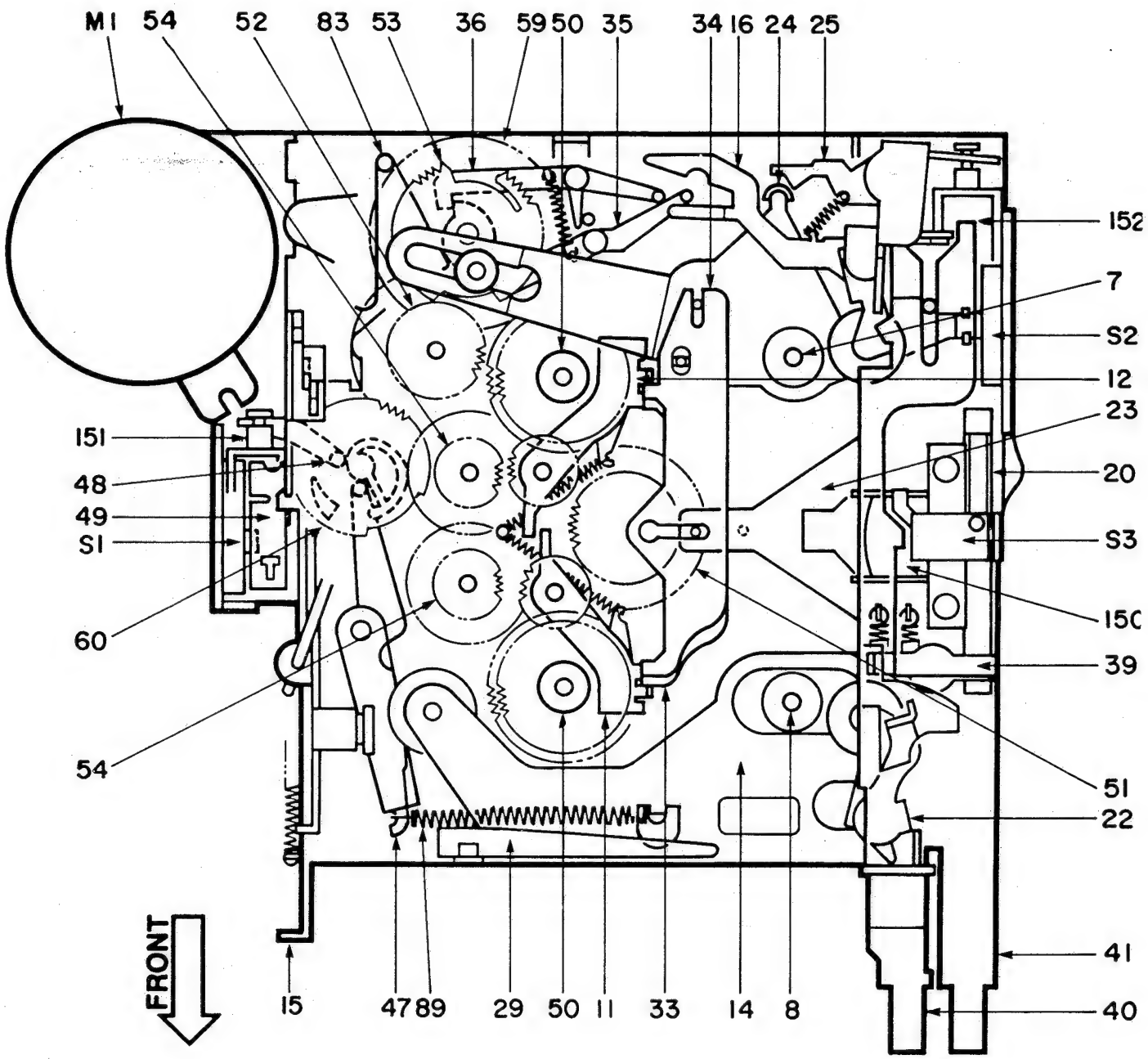
CIRCUIT DESCRIPTION



LCD Matrix

Terminal Name	COM2 Layout	COM1 Layout	Terminal Name	COM2 Layout	COM1 Layout
LCD 0	FM2	FM1	LCD 14	sd_6	sc_6
1	LW	MW	15	sa_6	SDK
2	Loud	cp	16	25	50
3			17	75	ST
4	sf_2	sb_2	18	SK	Auto
5	es_2	sg_2	19	▶	◀
6	sd_2	sc_2	20	sa_c	ch
7	sa_2	: (colon)	21	sf_c	sb_c
8	sf_4	sb_4	22	se_c	sg_c
9	se_4	sg_4	23	sd_c	sc_c
10	sd_4	sc_4	24	Dolby-C	Dolby-B/NR
11	sa_4	sa_4	25	T-ADV	T-Call
12	sf_6	sb_6	26	SM Min.	Metal
13	se_6	sg_6	27	SM Max.	SM Mid.

MECHANISM OPERATION DESCRIPTION

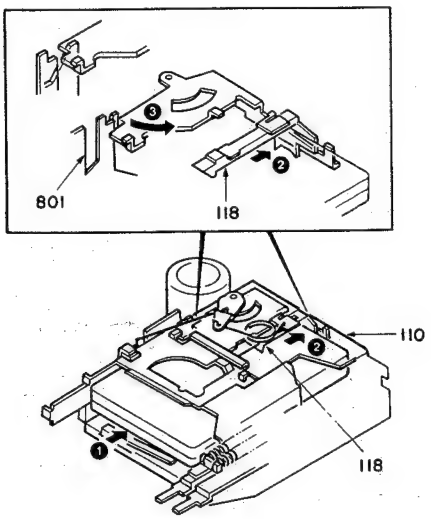


Parts Description (Front perspective view)

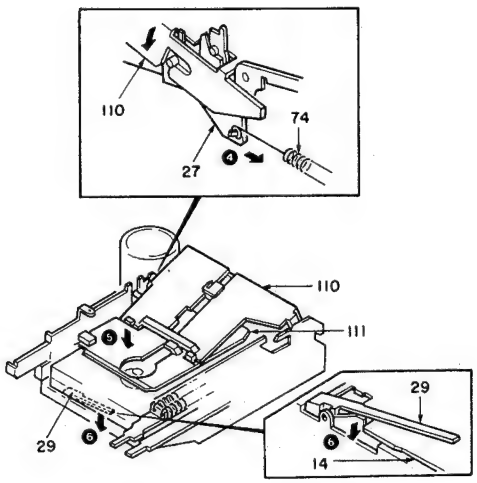
MECHANISM OPERATION DESCRIPTION

LOADING

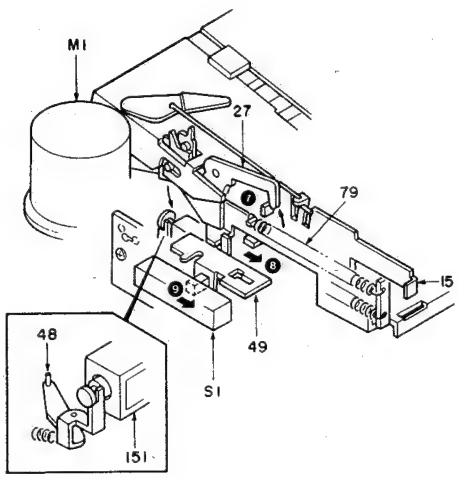
1. Insert a cassette tape (❶).
2. The cassette guide (118) pushes the lever (reverse) (❷).
3. The lever (reverse) turns in the direction of the arrow and releases the lock of the holder (action plate [110]) (❸).



4. Through the lock release of the lever (reverse), the arm (action [27]) is pulled by the tension spring (74), which turns the holder (action plate [110]). The holder (action plate) descends (❹).
5. Through the descent of the holder (action plate [110]), the holder (cassette case [111]) also descends (❺).
6. As the holder (cassette case [111]) descends, the cassette tape pushes the lever (lock plate [29]). The lever (lock plate [29]) then releases the lock of the lever assembly (head plate [14]) (❻).

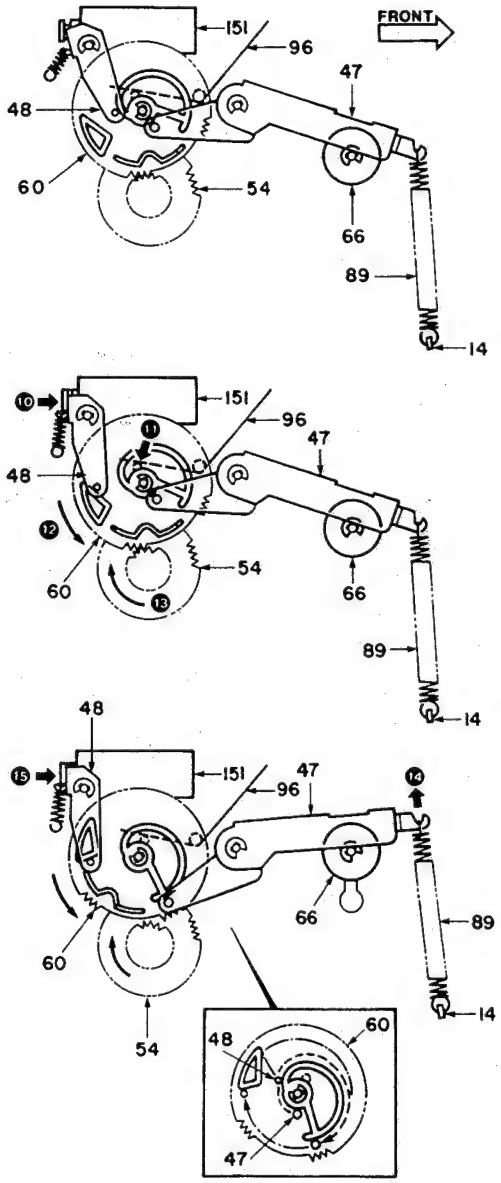


7. As the arm (action [27]) turns, the lock of the lever assembly (eject [15]) is released (❼).
8. The lever assembly (eject [15]) is pulled by the tension spring (79) and moves forward (❽).
9. Through the movement of the lever assembly (eject [15]), the lever (49) also moves forward and turns on the slide switch S1. As the slide switch S1 is turned on, electricity is supplied to the motor assembly (M1) and to the solenoid (151) (❾).

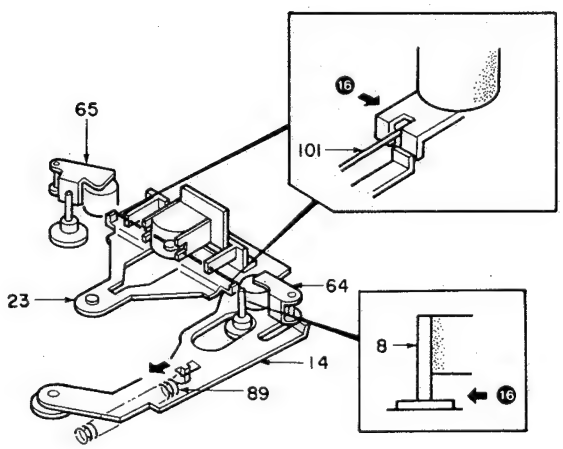


MECHANISM OPERATION DESCRIPTION

10. The solenoid (151) pulls the lever assembly (48) (⑩).
11. The gear (setting [60]) is pushed in the direction of the arrow by the torsion coil spring (96) (⑪).
12. The gear (setting [60]) turns in the direction of the arrow (⑫).
13. In turning, the gear (setting [60]) engages with the gear (take-up [54]) (⑬).
14. The rotation of the motor assembly (M1) is transmitted to each gear, and makes the gear (setting [60]) turned. Through the contact of the gear (setting [60]) with the arm assembly (47) and torsion coil spring (89) pull the lever assembly (head plate [14]), and the lever assembly (head plate [14]) moves forward (⑭).
15. The lever assembly (48) is locked by the solenoid (151) (⑮).

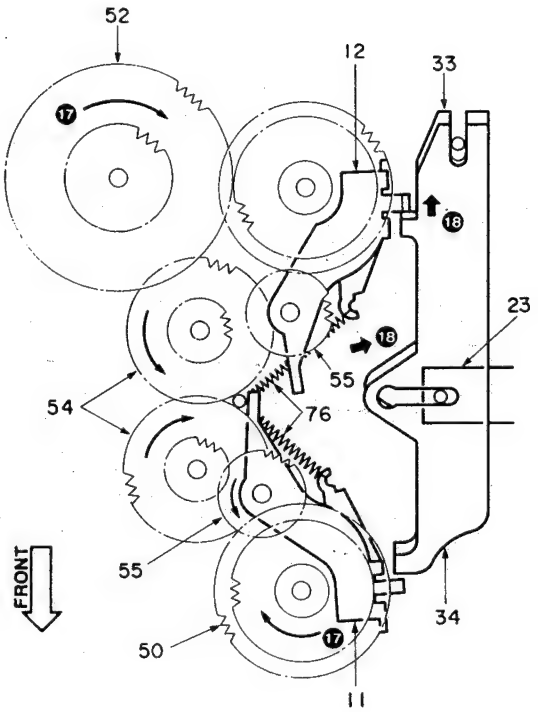


16. Through the forward movement of the lever assembly (head plate [14]), pinch roller assemblies (R & F [64, 65]) make close contact with the shaft of the flywheel assembly (R[8]) through the formed wire (101) (⑯).



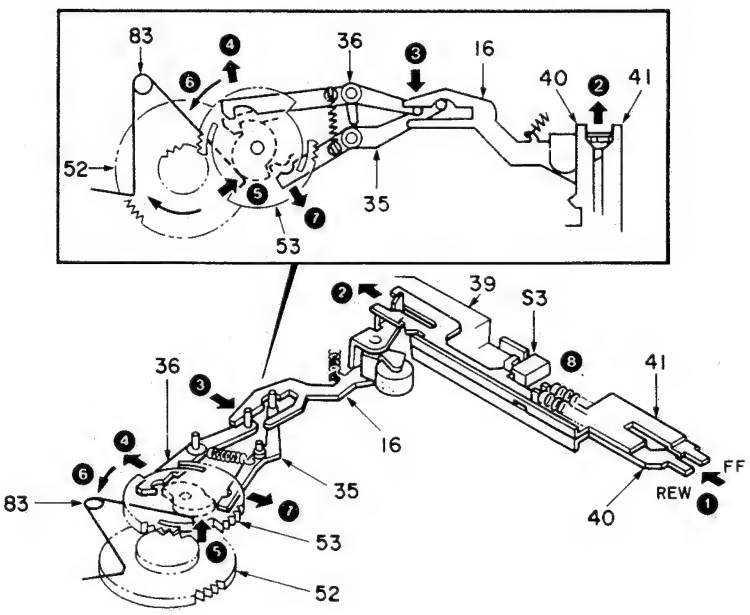
MECHANISM OPERATION DESCRIPTION

17. The rotation is transmitted from each gear (52 → 54 → 55) to the gear assembly (reel base [50]) of the take-up side (⓱).
18. The gear assembly (reel base [50]) of the pay-out side is pushed toward the slider assembly (12) by the lever (33) and the gear (take-up [55]) is disengaged in the direction of the arrow (⓲).



PROGRAM

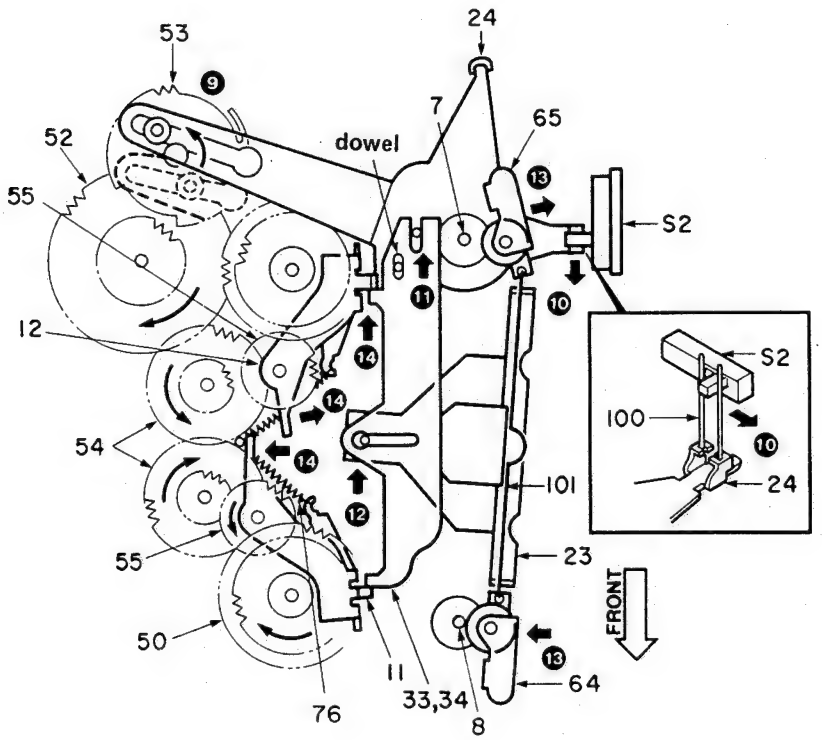
1. Push both levers (FR [40] and FR [41]) by hand at the same time (❶).
2. The calking of the lever assembly (reverse [16]) is sandwiched between the lever (FR [40]) and the lever (FR [41]), and pushes the lever assembly (reverse [16]) (❷).
3. The lever assembly (reverse [16]) moves the arm (36) (❸).
4. The arm (36) releases the lock of the gear assembly (switch [53]) (❹).
5. The torsion coil spring (83) pushes the cam of the gear assembly (switch [53]) in the direction of the arrow (❺).
6. The gear assembly (switch [53]) is pushed by the torsion coil spring (83), turns in the direction of the arrow, engages in the gear assembly (take-up [52]), and makes a half-turn (❻).
7. The arm (35) functions as a stop temporarily at this time; the stop is released when the reverse lever returns (❼).
8. The muting during the program is done by the leaf switch S3 mounted on the lever assembly (side panel [39]) (❽).



MECHANISM OPERATION DESCRIPTION

FROM FWD PLAY TO RVS PLAY

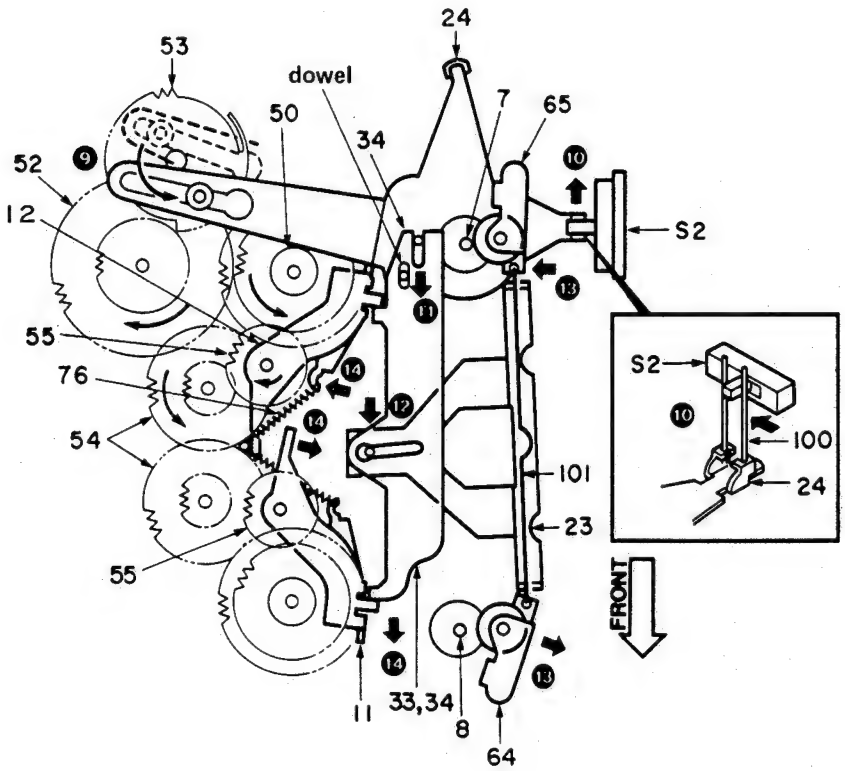
9. the gear assembly (switch [53]) moves the arm (24) from the FWD PLAY position to the RVS PLAY position through the movement of its boss (❾).
10. Through the movement of the arm (24), the slide switch S2 is switched by the formed wire spring (100) (❿).
11. The arm (24) moves the lever (33). The lever on it (34) moves at the same time through the dowel on the arm (24) (⓫).
12. The lever (33) moves the arm (23) (⓬).
13. Through the formed wire (PR [101]) of the arm (23), the pinchroller assembly (R [64]) contacts the shaft of the flywheel assembly (R [8]), and the pinch roller assembly (F [65]) is detached from the shaft of the flywheel assembly (F [7]) (⓭).
14. Through the movement of the lever (33) in the direction of the arrow (⓫), the gear (take-up [55]) attached to the slider assembly (11) is pushed by the lever (33), and the rotation is removed from the gear (take-up [54]).
    Through the movement of the lever (33) in the direction of the arrow (⓫), the gear (take-up [55]) attached to the slider assembly (11) is pulled by the tension spring (76), engages with the gear (take-up [54]), and the rotation is transmitted from the gear assembly (52→54→55→50) (⓮).



MECHANISM OPERATION DESCRIPTION

FROM RVS PLAY TO FWD PLAY

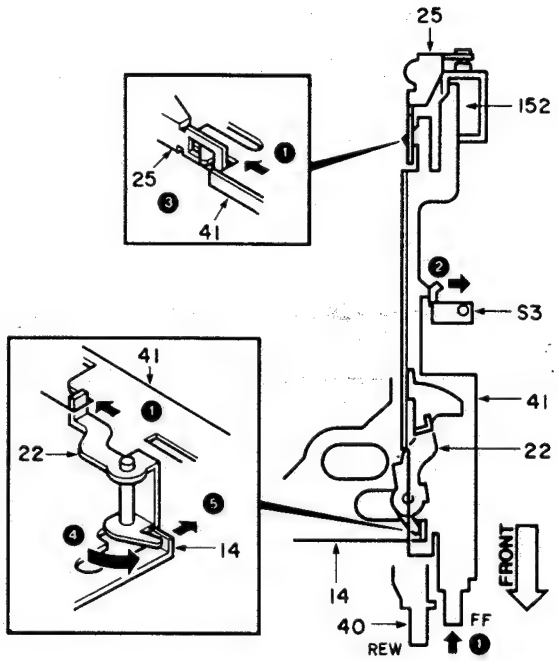
9. The gear assembly (switch [53]) moves the arm (24) from the FWD PLAY position to the RVS PLAY position through the movement of its boss (⑨).
10. Through the movement of the arm (24), the slide switch S2 is switched by the formed wire spring (100) (⑩).
11. The arm (24) moves the lever (34). The lever under it (33) moves at the same time through the dowel or the arm (24) (⑪).
12. The lever (34) moves the arm (23) (⑫).
13. Through the formed wire spring (PR [101]) of the arm (23), the pinchroller assembly (F [65]) contacts the shaft of the flywheel assembly (F [7]), and the pinch roller assembly (R [64]) is detached from the shaft of the flywheel assembly (R [8]) (⑬).
14. Through the movement of the lever (33) in the direction of the arrow (⑪), the gear (take up [55]) attached to the slider assembly (11) is pushed by the lever (33), and the rotation is removed from the gear (take-up [54]).
    Through the movement of the lever (33) in the direction of the arrow (⑫), the gear (take-up [55]) attached to the slider assembly (11) is pulled by the tension spring (76), engages with the gear (take up [54]), and the rotation is transmitted from the gear assembly (52→54→55→50) (⑭).



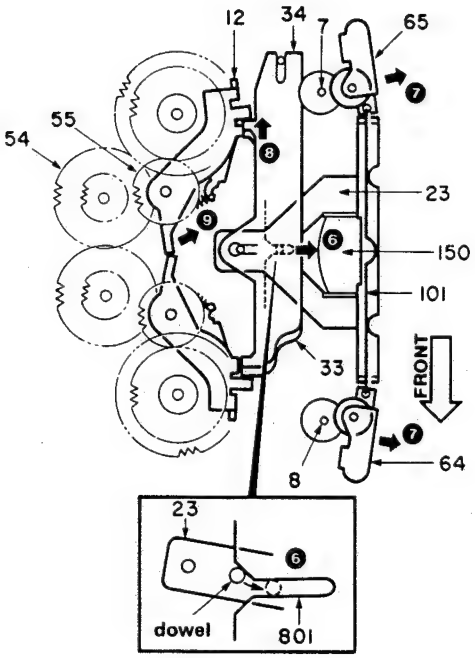
MECHANISM OPERATION DESCRIPTION

FF

1. Push the lever (FR [41]) (❶).
2. Pushing the lever (FR [41]) makes the leaf switch S3 turn on and muting is applied (❷).
3. The lever (FR [41]) is locked by the arm (FR release [25]) (❸).
4. By pushing the lever (FR [41]), the lever (FR cam [22]) is pushed in the direction of the arrow (❹).
5. Through being pushed, the lever (FR cam [22]) moves the lever assembly (head plate [14]) backward a little. Through the backward movement of the lever assembly (head plate [14]), the playback head (150) also moves backward a little (❺).

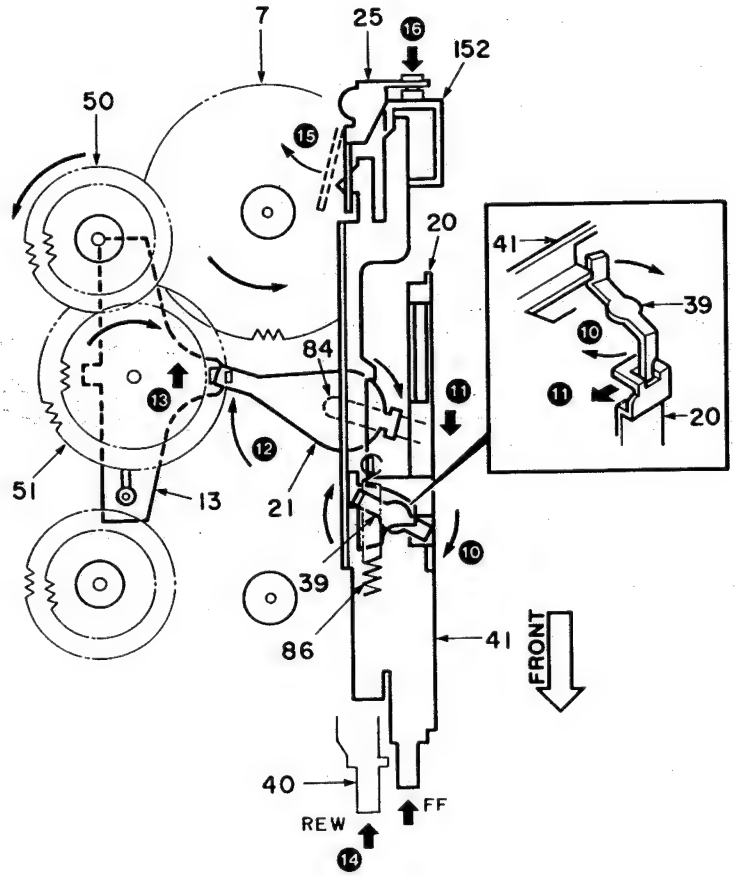


6. The arm (23) is slightly at an angle to the lever (34); however, through the backward movement of the lever assembly (head plate [14]), the arm (23) moves backward, its dowel being guided by the slot in the mechanism chassis (801) (❻).
7. By moving the arm (23) backward, the pinch roller assembly (R [64]) and the pinch roller assembly (F [65]) move backward from the shafts on the flywheel assembly (F [7]) and the flywheel assembly (R [8]) through a formed wire spring (101) (❼).
8. Through the backward movement of the dowel on the arm (23), the lever (34) moves in the direction of the arrow (❽).
9. The gear (take-up [55]) attached to the slider assembly (B [12]) disengages from the gear (take-up [54]), and the take-up torque is removed (❾).



MECHANISM OPERATION DESCRIPTION

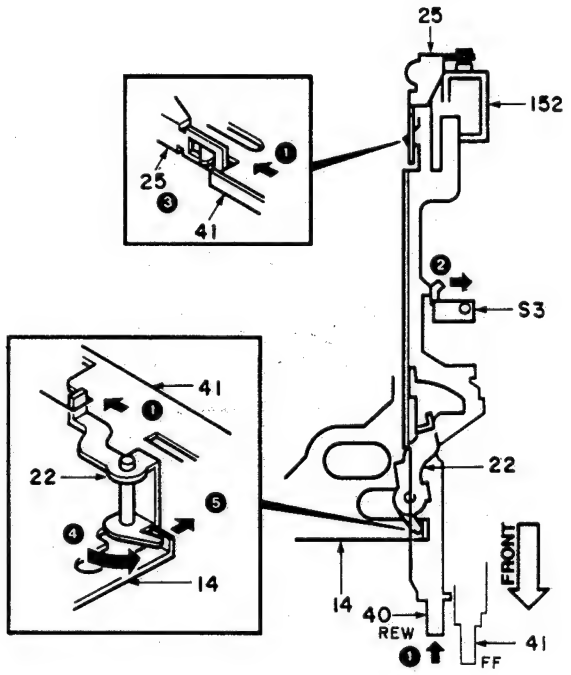
10. Meanwhile, through pushing the lever (FR [41]), the calking lever attached to the lever assembly (side panel [39]) is pushed by the lever (FR [41]) (⑩).
11. Through pushing the calking lever, the lever (FR cam [20]) moves forward (⑪).
12. Through the forward movement of the lever (FR cam [20]) the torsion coil spring (84) and the lever (FR cam [21])turn in the direction of the arrow (⑫).
13. Through the turning of the lever (FR cam [21]), the gear assembly (FR gear [51]) attached to the lever assembly (FR [13]) engages with the gear of the flywheel assembly (F [7]) and turns the gear of the gear assembly (FR gear) in the direction of the arrow (⑬).
14. To release FF, slightly depress the lever (FR [40]) (⑭).
15. By depressin the lever (FR [40]), the arm (FR release [25]) moves, and the lever (FR [41]) returns by the tension of the tension spring (86) (⑮).
16. In the operation of T.ADV, electricity is supplied to the solenoid (152), which attracts the arm (FR release [25]). The lock on the arm (FR release [25]) is released, FF is released and FWD PLAY is engaged (⑯).



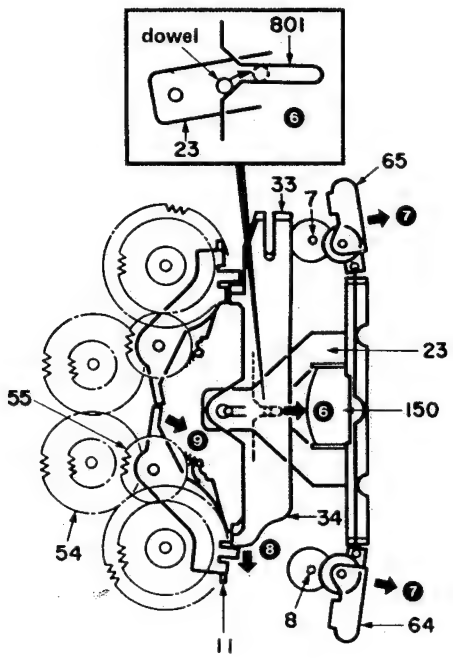
MECHANISM OPERATION DESCRIPTION

REW

1. Push the lever (FR [40]) (❶).
2. Pushing the lever (FR [40]) closes the leaf switch S3 and muting is applied (❷).
3. The lever (FR [40]) is locke by the arm (FR release [25]) (❸).
4. By pushing the lever (FR [40]), the lever (FR cam [22]) is pushed in the direction of the arrow (❹).
5. Through being pushed, the lever (FR cam [22]) moves the lever assembly (head plate [14]) backward a little. Through the backward movement of the lever assembly (head plate [14]), the playback head (150) also moves backward a little (❺).

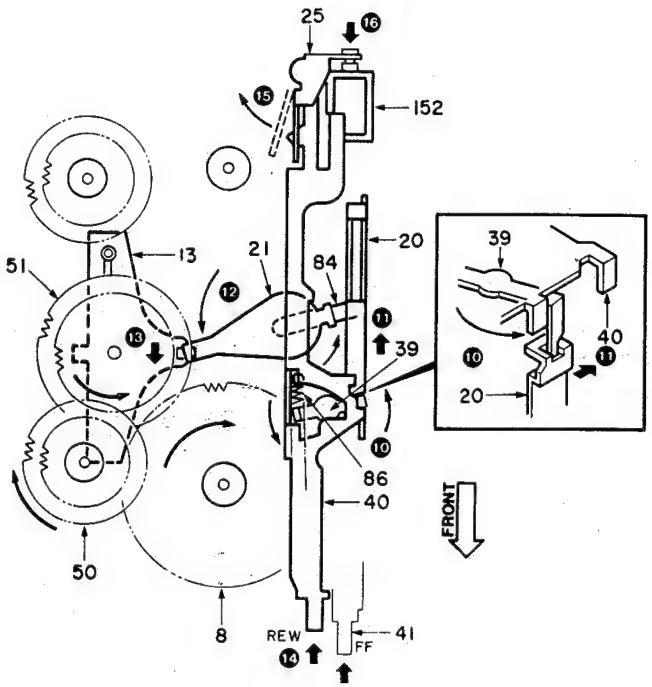


6. The arm (23) is slightly at an angle to the lever (34); however, through the backward movement of the lever assembly (head plate [14]), the arm (23) moves backward, its dowel being guided by the slot in the mechanism chassis (801) (❻).
7. Through the backward movement of the arm (23), the pinch roller assembly (F [7]) and the pinch roller assembly (R [8]) move backward from the shafts of the flywheel assembly (F [7]) and the flywheel assembly (R [8]) (❼).
8. Through the backward movement of the dowel on the arm (23), the lever (34) moves in the direction of the arrow (❽).
9. The gear (take-up [55]) attached to the slider assembly (A [11]) disengages from the gear (take-up [54]), and the take-up torque is removed (❾).



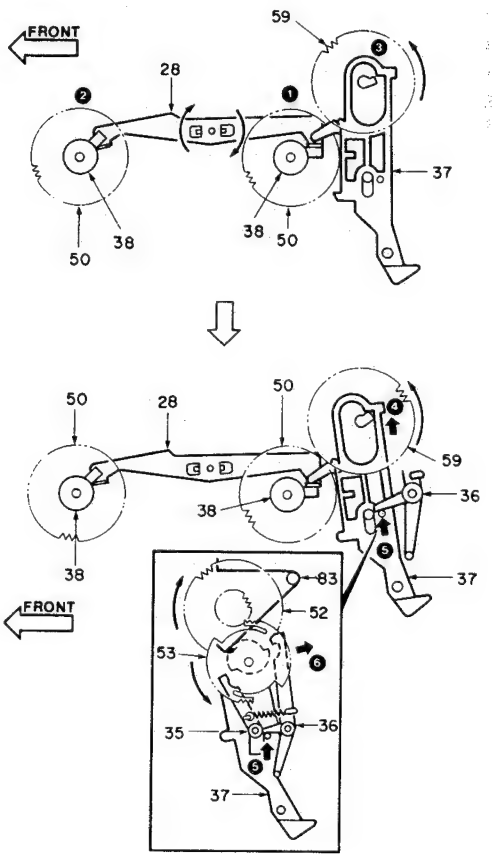
MECHANISM OPERATION DESCRIPTION

10. Meanwhile, through pushing the lever (FR [40]), the calking lever attached to the lever assembly (side panel [39]) is pushed by the lever (FR [40]) (⑩).
11. By pushing the calking lever, the lever (FR cam [20]) moves backward (⑪).
12. By the backward movement of the lever (FR cam [20]), the torsion coil spring (84) and the lever (FR cam [21]) turn in the direction of the arrow (⑫).
13. Through the turning of the lever (FR cam [21]), the gear assembly (FR gear [51]) attached to the lever assembly (FR [13]) engages with the gear of the fly wheel assembly (R [8]) and turns the gear of the gear assembly (FR gear [51]) in the direction of the arrow (⑬).
14. To release REW, slightly depress the lever (FR [41]) (⑭).
15. By depressing the lever (FR [41]), the arm (FR release [25]) moves, and the lever (FR [40]) returns by the tension of the tension spring (86) (⑮).
16. In the operation of T.ADV, electricity is supplied to the solenoid (152), which attracts the arm (FR release [25]). The lock on the arm (FR release [25]) is released, REW is released, and RVS PLAY is engaged (⑯).



AUTO REVERSE

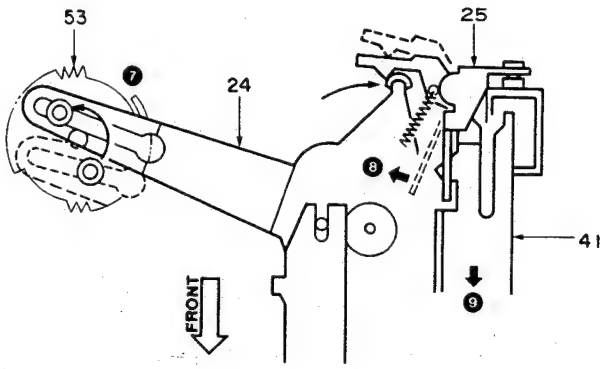
1. During FWD PLAY, when the rotation of the gear assembly (reel base [50]) of the take-up side stops at the end of the tape, the lower lever (sensor [38]) stops pushing the lever (sensor [28]) (❶).
2. The operation for RVS PLAY is the same as that for FWD PLAY (❷).
3. These end sensors on the take-up side stop pushing the end sensor lever (❸).
4. The lever (sensor [37]) moves forward, riding on the cam of the gear (switch [59]) (❹).
5. Through the forward movement of the lever (sensor [37]), its boss pushes the arm (36) (❺).
6. The arm (36) releases the lock of the gear (switch [53]), the gear assembly (switch [53]) is pushed by the torsion coil spring (83), and engages with gear assembly (take-up [52]) (❻).



MECHANISM OPERATION DESCRIPTION

7. The gear (switch [53]) makes a half-turn, and operates the program (❼).
8. At the tape end during the operation of FF or FWD, the end sensor is activated, and the arm (24) moves the lever (reverse [25]) during the program operation (❽).
9. The level (FR [41]) and the lever (FR [40]) are released (❾).

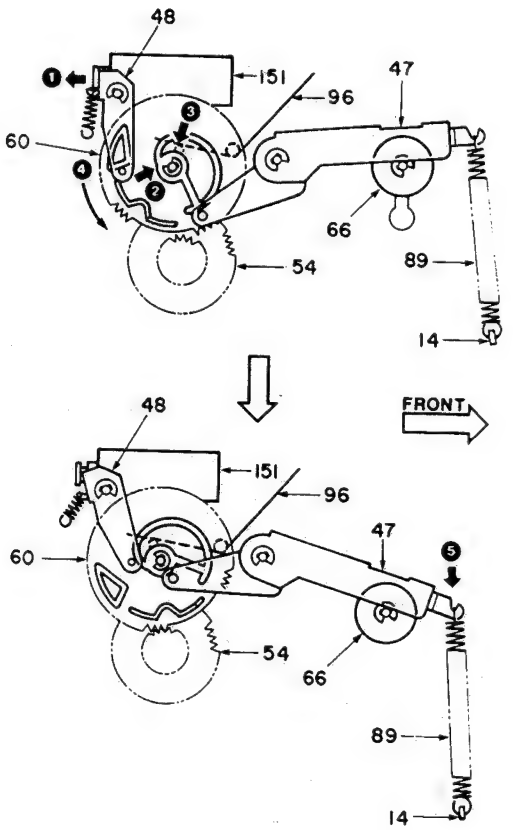
* The rotation of the gear assembly (reel base [50]) resets the lever (sensor [37]). The cam of the gear (switch [59]) pushes the lever (sensor [37]) to set it. After a half-turn of the cam of the gear assembly (switch [59]), the lever (sensor [37]) moves forward.



KEY OFF RELEASE

1. When ACC is turned off, the solenoid (151) is turned off (❶).
2. The solenoid (151) release the lever assembly (48) (❷).
3. The cam of the gear (setting [60]) is pushed by the torsion coil spring (96) (❸).
4. In being pushed by the torsion coil spring (96), the gear (setting [60]) turns in the direction of the arrow, and the lever assembly (48) releases the lock of the gear (setting [60]) (❹).
5. When the arm assembly (47) is released, the tension spring (89) moves the lever assembly (head plate [14]) backward (❺).

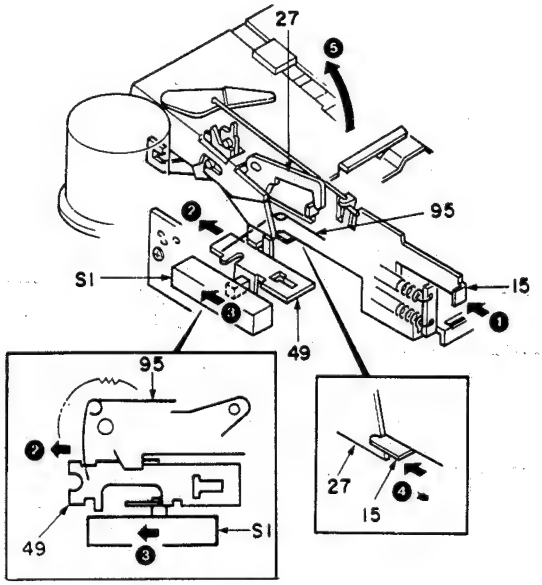
* At the time of KEY OFF, the pinch roller, tape, and head are separated. (KEY OFF EJECT does not take place.) When ACC is on in this condition, the gear (setting [60]) rotates, and FWD PLAY takes place.



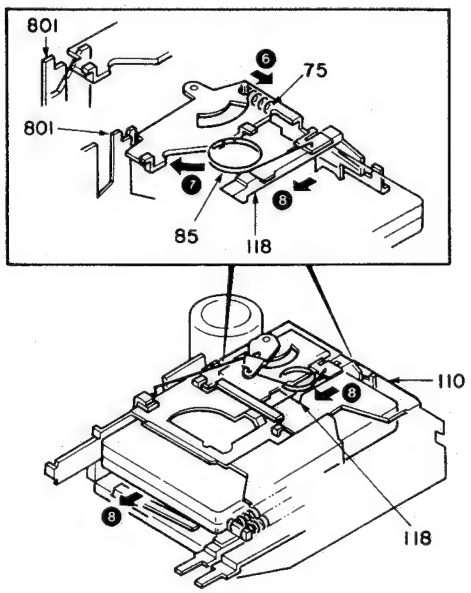
MECHANISM OPERATION DESCRIPTION

EJECT

1. Push the lever assembly (eject [15]) (❶).
2. By pushing the lever assembly (eject [15]), the torsion coil spring (95) pushes the lever (49) (❷).
3. Through pushing the lever (49), the slide switch S1 is turned off, and the lever assembly (head plate [14]) moves backward in the KEY OFF operation (❸).
4. The lever assembly (eject [15]) pushes and turns the arm (action [27]) (❹).
5. By turning, the arm (action) pushes up the holder (action plate [110]) (❺).



6. When the holder (action plate [110]) is pushed up, the lever (reverse) is pulled by the tension spring (75) and turns (❻).
7. In turning, the lever (reverse) is put on the lever of the mechanism chassis (801) (❼).
8. The cassette guide (118) is pushed forward by the torsion coil spring (85), and the cassette tape is ejected (❽).



ADJUSTMENT

Set the controls and switches as follows.

BALANCE	:center position	LOUD	:OFF	AUTO	:OFF
FADER	:center position	T・ADV	:OFF		
BASS	:center position	METAL	:OFF		
TREBLE	:center position	DOLBY NR	:OFF		

No.	ITEM	INPUT SETTINGS	OUTPUT SETTINGS	TUNER(RECEIVER) SETTINGS	ALIGNMENT POINTS	ALIGN FOR	FIG.
FM SECTION							
1	DISCRIMINATOR	(A) 98.1MHz 0 dev 60dBμ(ANT input)	Connect the DC voltmeter to TP2 (X86)	FM 98.1MHz	T1 (X86)	0V	(a)
2	SEPARATION	(C) 98.1MHz 1kHz,±40kHz dev Pilot:6±kHz dev Selector:L or R 60dBμ(ANT input)	(B)	FM 98.1MHz	VR4 (X86)	Adjust it so that the crosstalk from L to R and R to L become minimum.	
3	ANRC	(C) 98.1MHz 1kHz,±40kHz dev Pilot:±6kHz dev Selector:L or R 35dBμ(ANT input)	(B)	FM 98.1MHz	VR1 (X86)	Separation 10dB	
4	SEEK STOP LEVEL	(A) 98.1MHz 1kHz,±40kHz dev 20dBμ(ANT input)	Connect the DC voltmeter to TP1 (X86)	FM SEEK:ON 98.1MHz	VR2 (X86)	" H " level with input of 20dBμ or more	(b)
5	SOFT MUTE LEVEL	(A) 98.1MHz 1kHz,±40kHz dev 60dBμ→No input	(B)	FM 98.1kHz	VR3 (X86)	Output Noise level -25dBμ (When not add any signal to ANT terminal)	
6	S-METER	(A) 98.1MHz 1kHz,±40kHz dev 20dBμ(ANT input)	–	FM 98.1kHz	VR2 (X14)	One of them lights up with 20dBμ input.	
SDK SECTION							
7	DK LEVEL	(E) 98.1MHz 1kHz,±40kHz dev SK 5.33% DK 30% BK 60% 60dBμ(ANT input)	Connect the AC voltmeter to TP1 (X14)	FM 98.1MHz SDK:ON	L1 VR4 (X14)	Maximum	(c)
AM SECTION							
(1)	SEEK STOP LEVEL	(D) 999kHz 400Hz,30% mod 35dBμ(ANT input)	Connect the DC voltmeter to TP2 (X14)	AM 999kHz	VR3 (X14)	" L " level with input of 35dBμ or more	(d)
CASSETTE DECK SECTION							
[1]	AZIMUTH	MTT-114 10kHz	(B)	TAPE PLAY	Head Azimuth Screw	Adjust the azimuth for each L CH/R CH or FWD/RVS becomes maximum.	(e)
[2]	PLAYBACK LEVEL	MTT-150	Connect the AC voltmeter to TP1 (X09)	TAPE PLAY	VR1(L) VR2(R) (X87)	388mV	(f)

REGLAGE

Régler les controles et les boutons comme suit.

BALANCE	:position centre	LOUD	:OFF	AUTO	:OFF
FADER	:position centre	T・ADV	:OFF		
BASS	:position centre	METAL	:OFF		
TREBLE	:position centre	DOLBY NR	:OFF		

N°	ITEM	REGLAGE DE L'ENTREE	REGLAGE DE LA SORTIE	REGLAGE DU TUNER (AMPLI TUNER)	POINTS DE L'ALIGNEMENT	ALIGNER POUR	FIG.
SECTION MF							
1	DISCRIMINATEUR	(A) 98,1MHz 0 dév 60dBμ(Entrée ANT)	Connecter le voltmètre CC à TP2 (X86)	FM 98,1MHz	T1 (X86)	0V	(a)
2	SEPARATION	(C) 98,1MHz 1kHz.±40kHz dév Pilote:±6kHz dév Selecteur:G ou D 60dBμ(Entrée ANT)	(B)	FM 98,1MHz	VR4 (X86)	Le régler de manière à ce que la diaphonie de L à R et de R à L devienne minimum.	
3	ANRC	(C) 98,1kHz 1kHz.±40kHz dév Pilote:±6kHz dév Selecteur:G ou D 35dBμ(Entrée ANT)	(B)	FM 98,1MHz	VR1 (X86)	Séparation 10dB	
4	NIVEAU DE CHERCHER D'ARRET	(A) 98,1MHz 1kHz.±40kHz dév 20dBμ(Entrée ANT)	Connecter le voltmètre CC à TP1 (X86)	FM CHERCHER:ON 98,1MHz	VR2 (X86)	Niveau " H " avec entrée de 20dBμ ou plus.	(b)
5	NIVEAU DE SOFT MUTE	(A) 98,1MHz 1kHz.±40kHz dév 60dBμ→Entrée NO	(B)	FM 98,1MHz	VR3 (X86)	Bruit de niveau de sortie -25dBμ (Sous non correspondance d'antenne.)	
6	INDICATEUR DE CHAMP	(A) 98,1MHz 1kHz.±40kHz dév 20dBμ(Entrée ANT)		FM 98,1MHz	VR2 (X14)	L'un d'eux s'allume avec une entrée de 20dBμ.	
SECTION SDK							
7	NIVEAU DE DK	(E) 98,1MHz 1kHz.±40kHz dév SK 5,33% DK 30% BK 60% 60dBμ(Entrée ANT)	Connecter le voltmètre CA à TP1 (X14)	FM 98,1MHz SDK:ON	L1 VR4 (X14)	Maximale	(c)
SECTION MA							
(1)	NIVEAU DE CHERCHER D'ARRET	(D) 999kHz 400Hz, 30% mod 35dBμ(Entrée ANT)	Connecter le voltmètre CC à TP2 (X14)	AM 999kHz	VR3 (X14)	Niveau " L " avec entrée de 35dBμ ou plus.	(d)
SECTION DU MAGNETPHONE							
[1]	AZIMUTH	MTT-114 10kHz	(B)	Lecture bande	Vis d'azimut de tête	Ajuster l'azimut pour que chaque L-CH/R-CH ou FWD/RVS devienne maximum.	(e)
[2]	NIVEAU DE LECTURE	MTT-150	Connecter le voltmèter CA à TP1. (X09)	Lecture bande	VR1(G) VR2(D) (X87)	388mV	(f)

ABGLEICH

Die Regler und Knöpfe wire folgt einstellen.

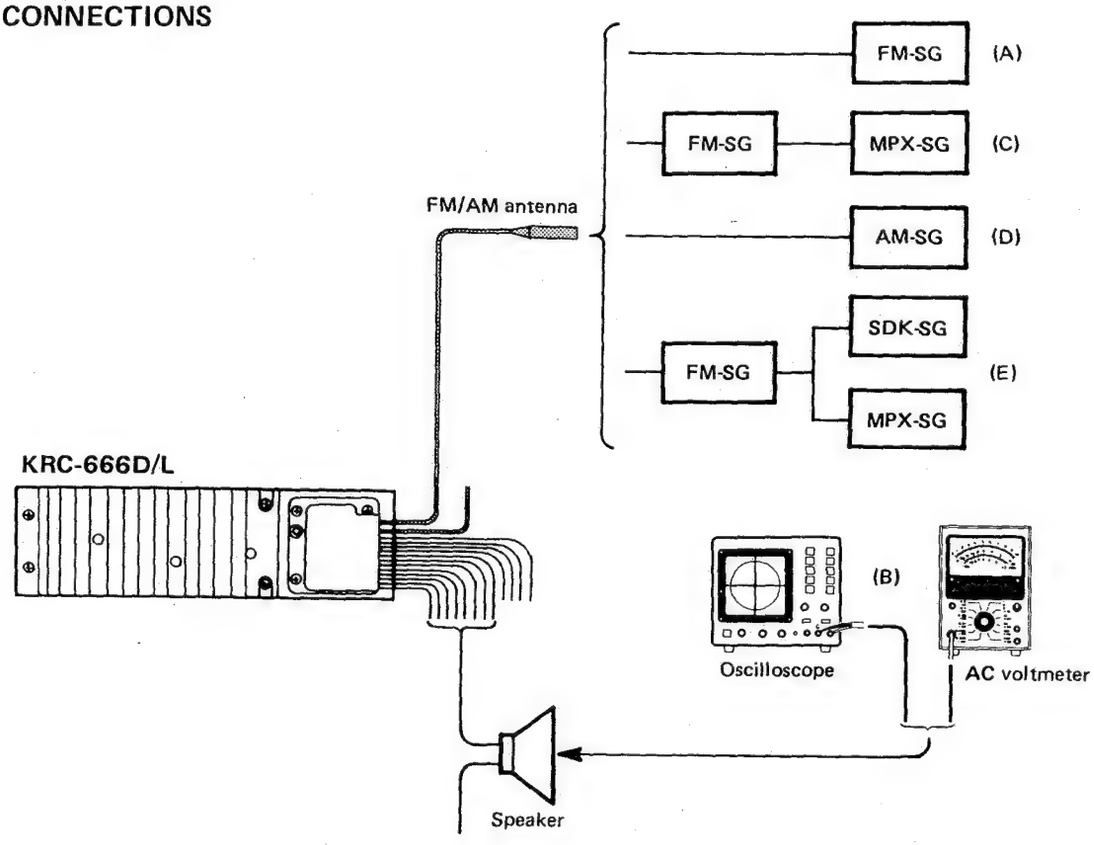
BALANCE	:Mittelage	LOUD	:OFF	AUTO	:OFF
FADER	:Mittelage	T・ADV	:OFF		
BASS	:Mittelage	METAL	:OFF		
TREBLE	:Mittelage	DOLBY NR	:OFF		

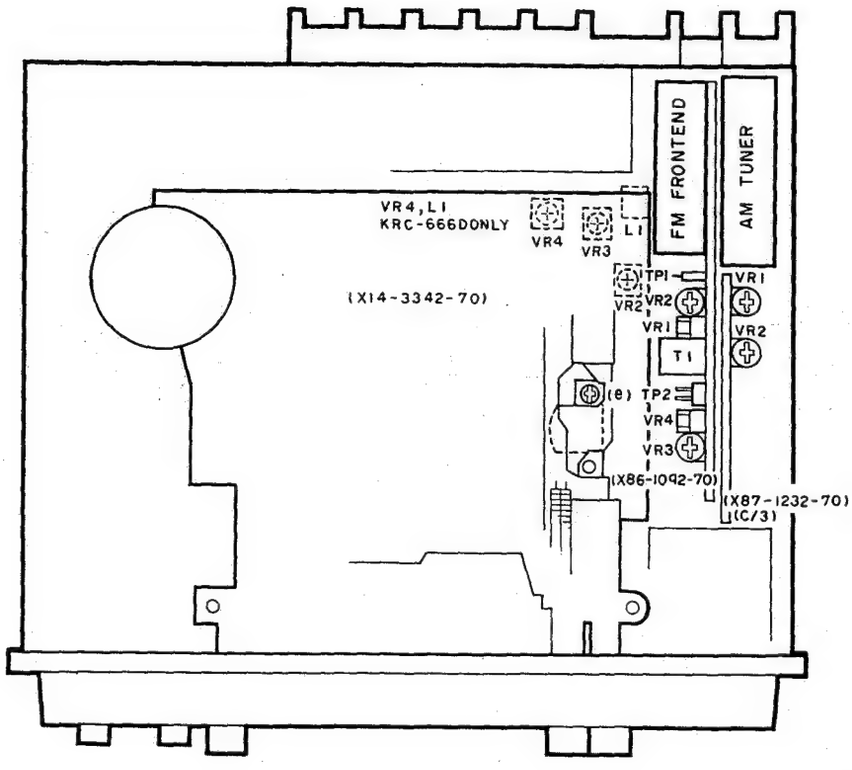
NR.	GEGENSTAND	EINGANGS-EINSTELLUNG	AUSGANGS-EINSTELLUNG	TUNER(RECEIVER)-EINSTELLUNG	ABGLEICH PUNKTE	ABGLEICHEN FÜR	ABB.
UKW-ABTEILUNG							
1	DISKRIMINATOR	(A) 98,1MHz 0 Hub 60dBμ(ANT-Eingang)	Das Gleichstrom-Voltmeter an TP2 anschließen. (X86)	FM 98,1MHz	T1 (X86)	0V	(a)
2	STEREO KANAL TRENNUNG	(C) 98,1MHz 1kHz.±40kHz Hub Pilot:±6kHz Hub Wahler:L or R 60dBμ(ANT-Eingang)	(B)	FM 98,1MHz	VR4 (X86)	So einstellen, daß das Übersprechen von L auf R und von R auf L minimal wird.	
3	ANRC	(C) 98,1MHz 1kHz.±40kHz Hub Pilot:±6kHz Hub Wahler:L or R 35dBμ(ANT-Eingang)	(B)	FM 98,1MHz	VR1 (X86)	Trennung 10dB	
4	SUCHEN HALT PEGEL	(A) 98,1MHz 1kHz.±40kHz Hub 20dBμ(ANT-Eingang)	Das Gleichstrom-Voltmeter an TP1 anschließen. (X86)	FM SUCHEN:ON 98,1MHz	VR2 (X86)	" H "-Pegel mit Eingang von 20dBμ oder mehr	(b)
5	SOFT MUTE PEGEL	(A) 98,1MHz 1kHz.±40kHz Hub 60dBμ→No Eingang	(B)	FM 98,1MHz	VR3 (X86)	Ausgang Geräusch pegel -25dBμ (Wenn Autenna Stecker Nicht anschließen.)	
6	ANZEIGE INSTRUMENT	(A) 98,1MHz 1kHz.±40kHz Hub 20dBμ(ANT-Eingang)	—	FM 98,1MHz	VR2 (X14)	Eine davon leuchtet bei 20dBμ Eingang.	
SDK-ABTEILUNG							
7	DK PEGEL	(E) 98,1MHz 1kHz.±40kHz Hub SK 5,33% DK 30% BK 60% 60dBμ(ANT-Eingang)	Das Wechsel-spannungmesser an TP1 anschließen. (X14)	FM 98,1MHz SDK:ON	L1 VR4 (X14)	Maximale	(c)
MW-ABTEILUNG							
(1)	SUCHEN HALT PEGEL	(D) 999kHz 400Hz, 30% mod 35dBμ(ANT-Eingang)	Das Gleichstrom-Voltmeter an TP2 anschließen. (X14)	MW 999kHz	VR3 (X14)	" L "-Pegel mit Eingang von 35dBμ oder mehr	(d)
CASSETTEN-DECK-ABTEILUNG							
[1]	AZIMUTH	MTT-114 10kHz	(B)	Bandwiedergabe	Kopfazimutschraube	So einstellen, daß das Azimuth für jeweils L-CH/R-CH oder FWD/RVS maximal wird.	(e)
[2]	WIDERGABE PEGEL	MTT-150	Das Wechsel-spannungsmesser an TP1 anschließen. (X09)	Bandwiedergabe	VR1(L) VR2(R) (X87)	388mV	(f)

KRC-666D/L

ADJUSTMENT/REGLAGE/ABGLEICH

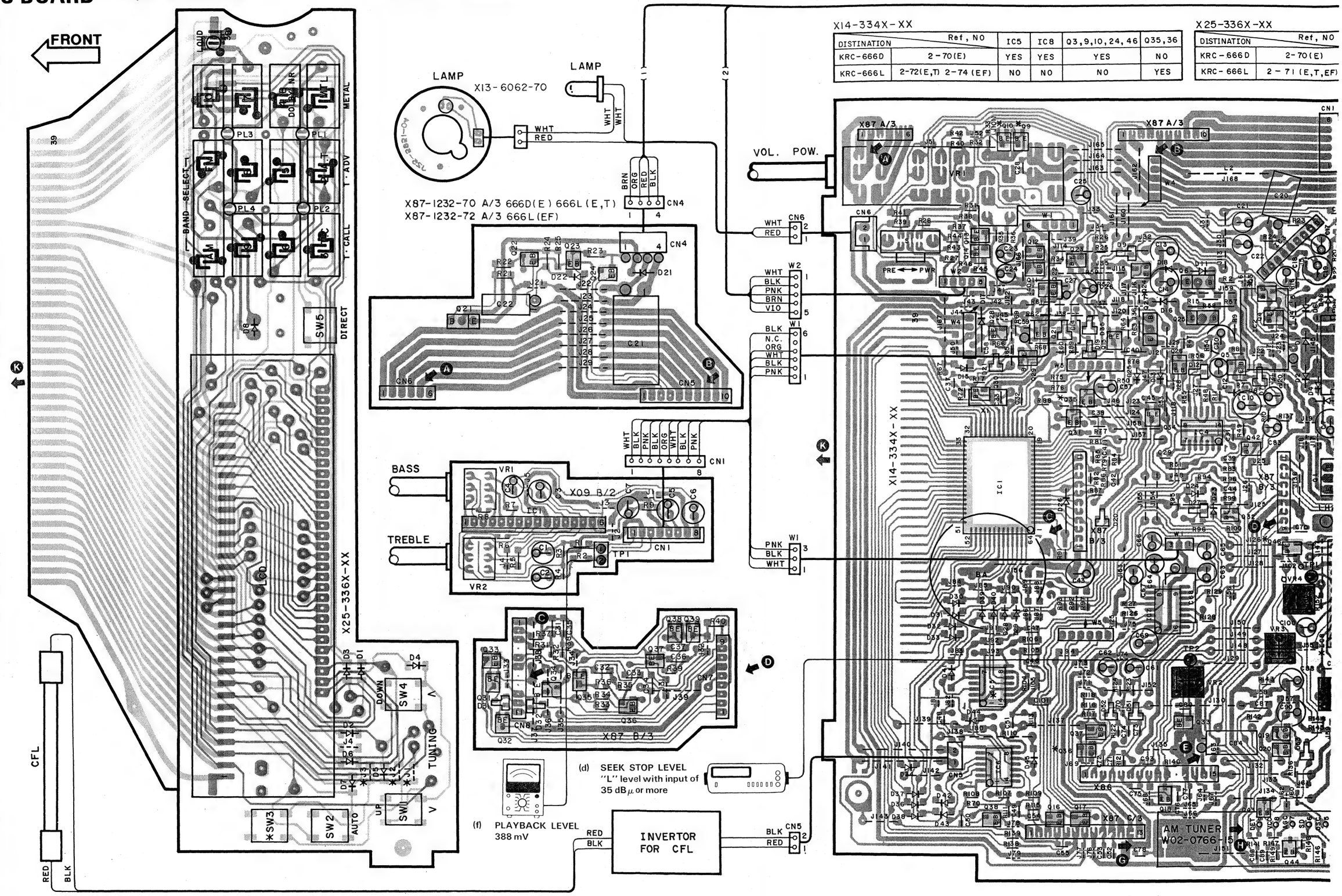
SYSTEM CONNECTIONS





KRC-666D/L

PC BOARD (Component Side View)



X14-334X-XX

DISTINATION	Ref, NO	IC5	IC8	Q3,9,10,24,46	Q35,36
KRC-666D	2-70(E)	YES	YES	YES	NO
KRC-666L	2-72(E,T) 2-74(EF)	NO	NO	NO	YES

X25-336X-XX

DISTINATION	Ref, NO
KRC-666D	2-70(E)
KRC-666L	2-71(E,T,EF)

8	Q3,9,10,24,46	Q35,36
6	YES	NO
	NO	YES

X25-336X-XX

DISTINATION / Ref, NO	J1	J2,3	SW3
KRC-666D 2-70(E)	NO	YES	SDK
KRC-666L 2-71(E,T,EF)	YES	NO	MONO

X87 A/3

X87 B/3

X87 C/3

X86

AM TUNER W02-0766-15

(c) DK LEVEL Maximum

KRC-666D(E) ONLY
X14-3342-70
TP1,VR4,L1

CN1 GRN GRY BLU RED GRY ORG

CN2 BLK RED BLU BRN GRN ORG

CN3 ORG RED RED BLK BLK YEL

W3 BLK N.C. YEL RED BLK

CN4 ANT BLK

SOL. TAPE ↔ RADIO

MECHANISM ASS'Y
D40-0816-05

MOTOR BLK RED

SOL. BLU BLU

MUTE

PLAY BACK HEAD

RVS ↔ FWD

TPC.

(a) DISCRIMINATOR 0 V

X09-2672-70 A/2 666D(E) 666L(E,T)
X09-2670-72 A/2 666L(EF)

CN5 BLU N.C. GRN

CN4 WHT BLK PNK BRN VIO

CN3 ORG RED

CN2 BLK RED YEL BLK

X86-1092-70 666D(E) 666L(E,T)
X86-1092-71 666L(EF)

FM FRONT END W02-0926-05

(b) SEEK STOP LEVEL "H" level with input of 20 dBμ or more

AM TUNER W02-0766-15

DET VCC AGC SD OSC BS Vt ANT. LO/DX

FM FRONT END W02-0926-05

OSC OUT IF OUT VT +B AGC ANT

BLK GND ANT. BLK BLK RED BLU BRN GRN ORG YEL BLK BLK ORG RED RED BLU BLU GRY GRY GRY GRN GRN ORG ORG

GND Rch Lch DK RST P-CONT YEL/W RED/W BLK GND VIO/B VIO GRY/B GRY

PRE OUT

FUSE

BACK UP

ACC RED

BACK UP YEL

BLU

FRONT-L GRN

FRONT-R GRY

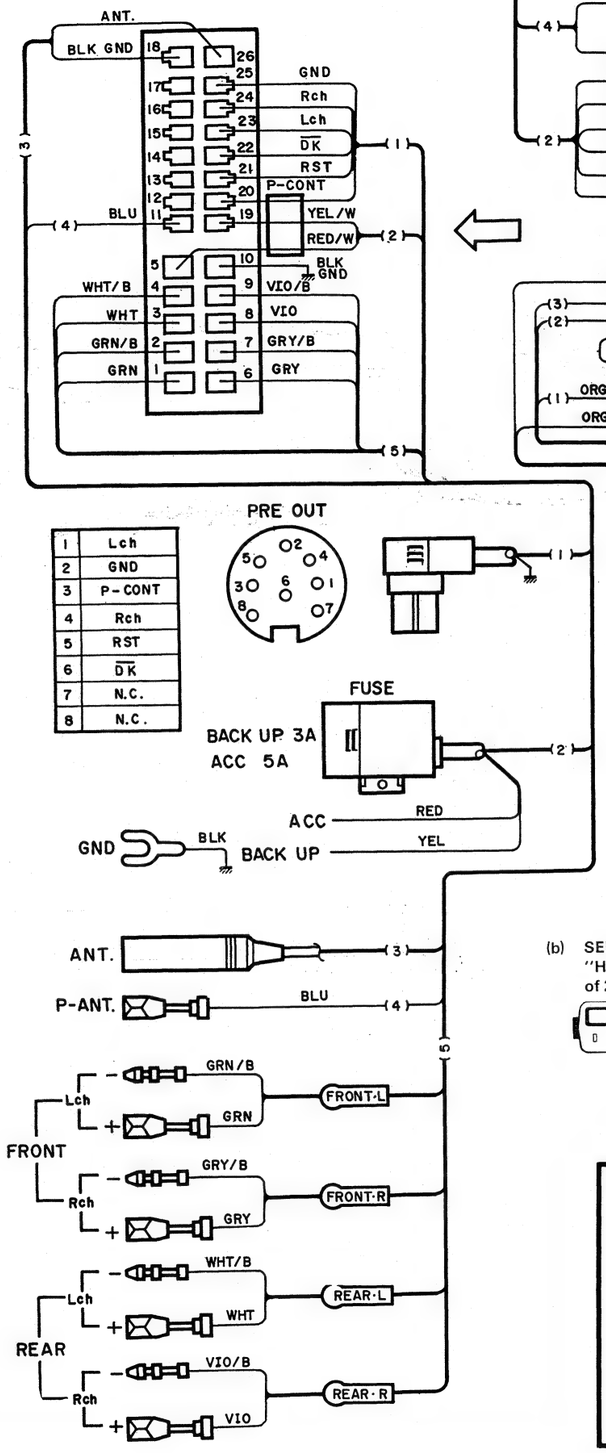
REAR-L WHT

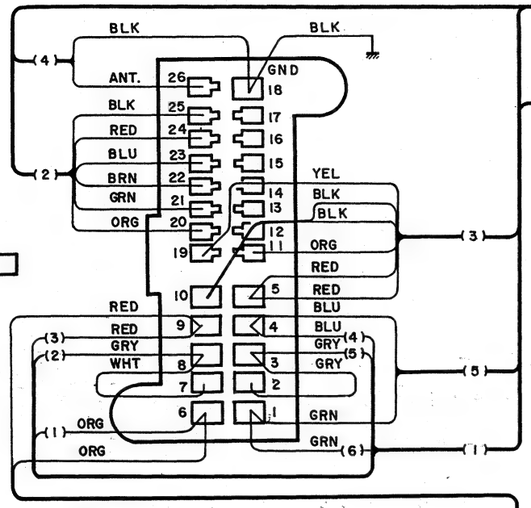
REAR-R VIO

KRC-666D (E)
KRC-666L (E,T,E

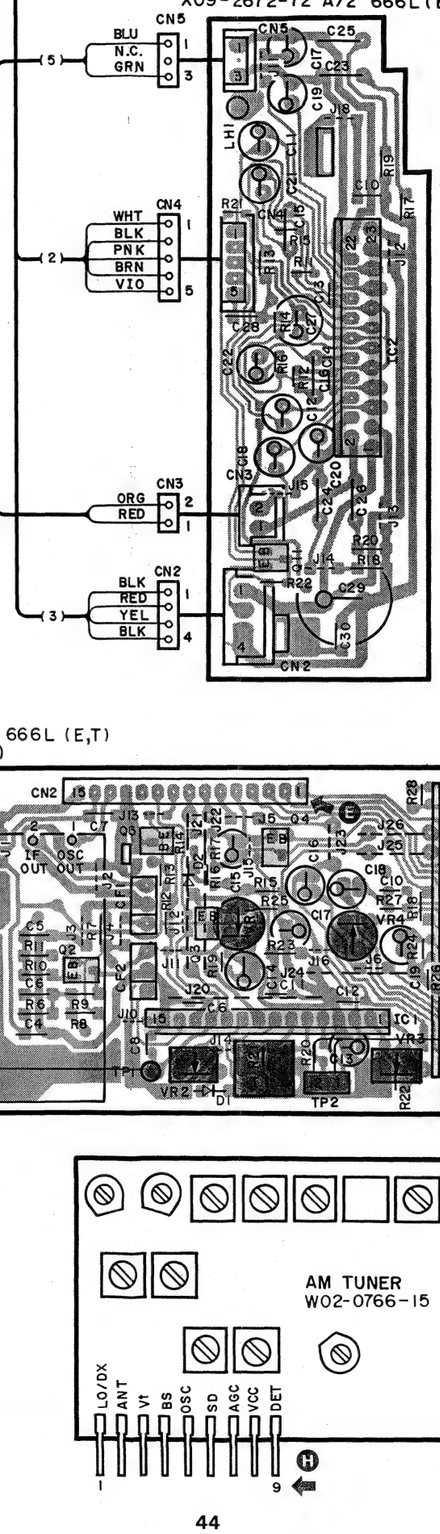
Refer to the schematic diagram for the values of resistors and capacitors.

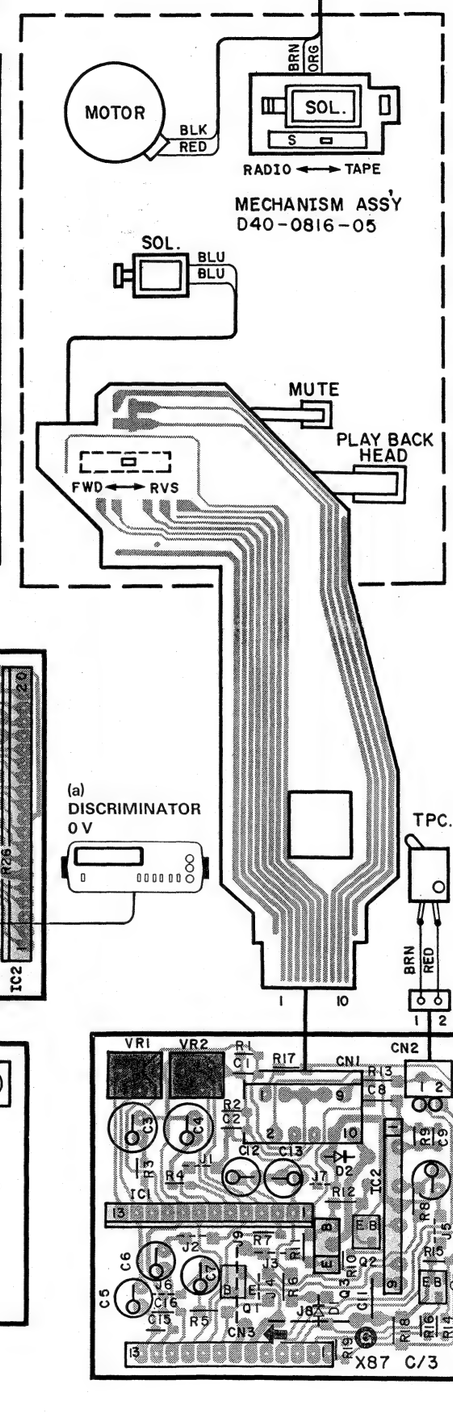
PC BOARD (Foil Side View)





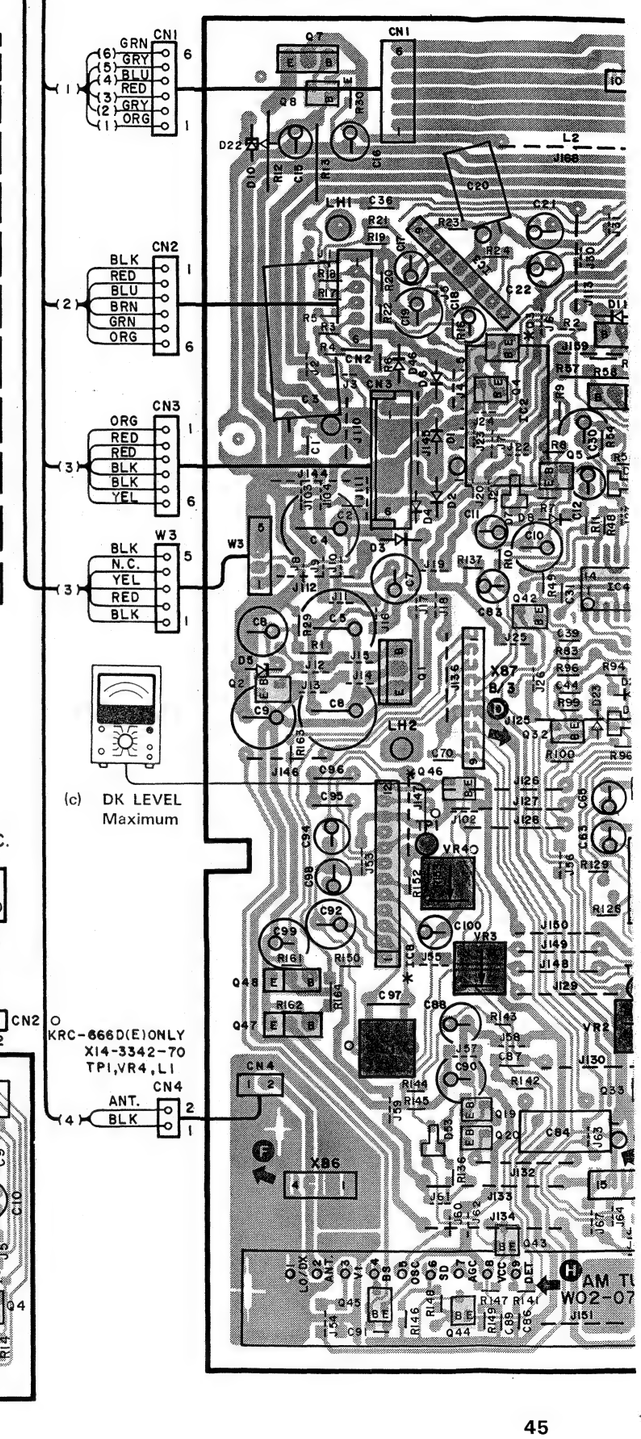
X09-2672-70 A/2 666D(E) 666L(E,T)
X09-2672-72 A/2 666L(EF)





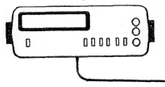
X14-334X-XX

DISTINATION	Ref, NO	IC5	IC8	Q3
KRC-666D	2-70(E)	YES	YES	
KRC-666L	2-72(E,T) 2-74(EF)	NO	NO	

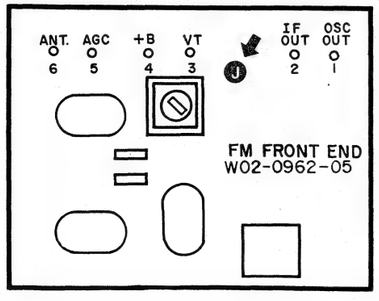


X86-1092-70 666D(E) 666L(E,T)
X86-1092-71 666L(EF)

(b) SEEK STOP LEVEL
"H" level with input of 20 dBμ or more



(a) DISCRIMINATOR 0 V

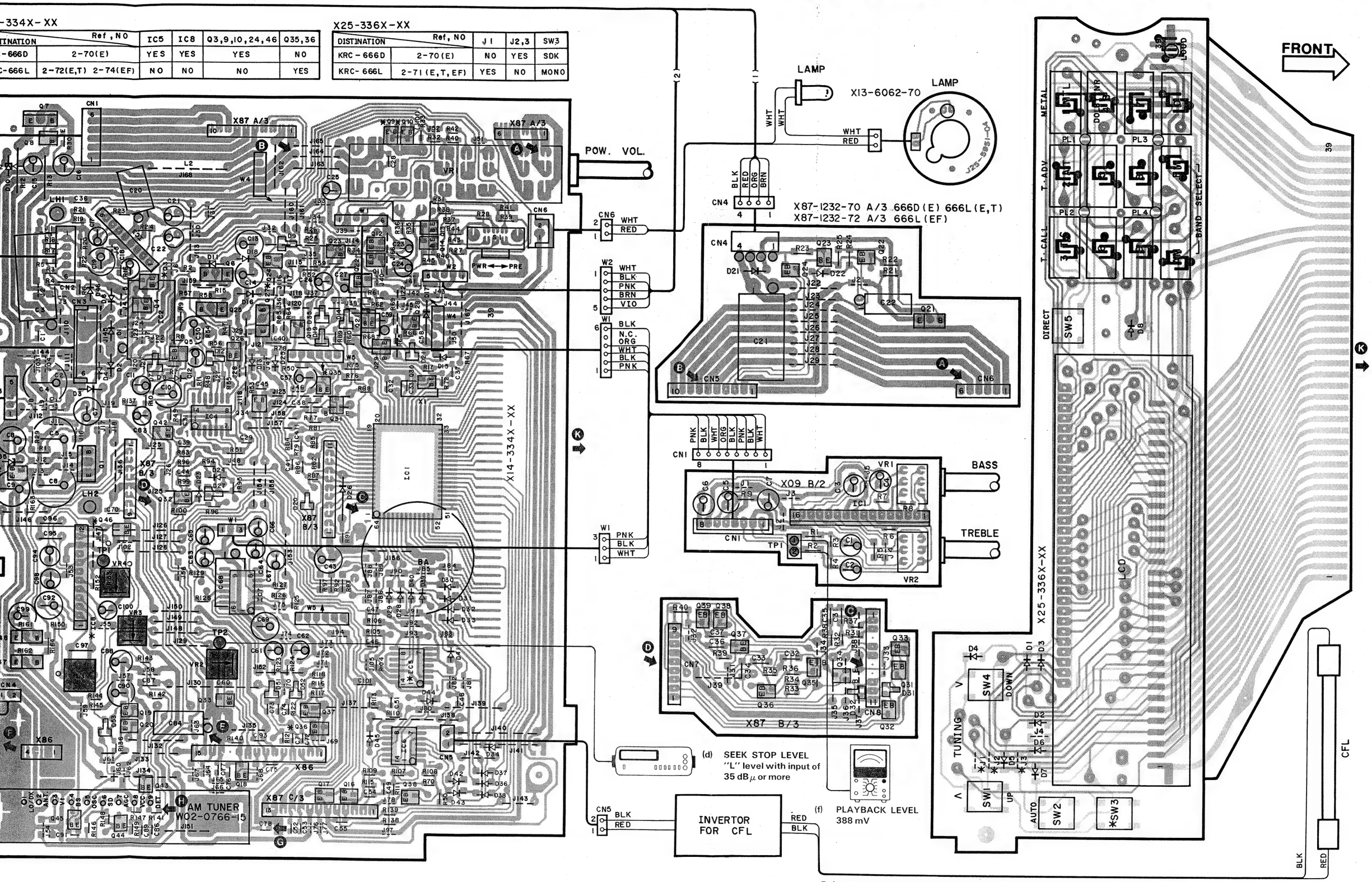


X14-334X-XX

DISTINATION \ Ref, NO	2-70(E)	IC5	IC8	Q3,9,10,24,46	Q35,36
KRC-666D	2-70(E)	YES	YES	YES	NO
KRC-666L	2-72(E,T) 2-74(EF)	NO	NO	NO	YES

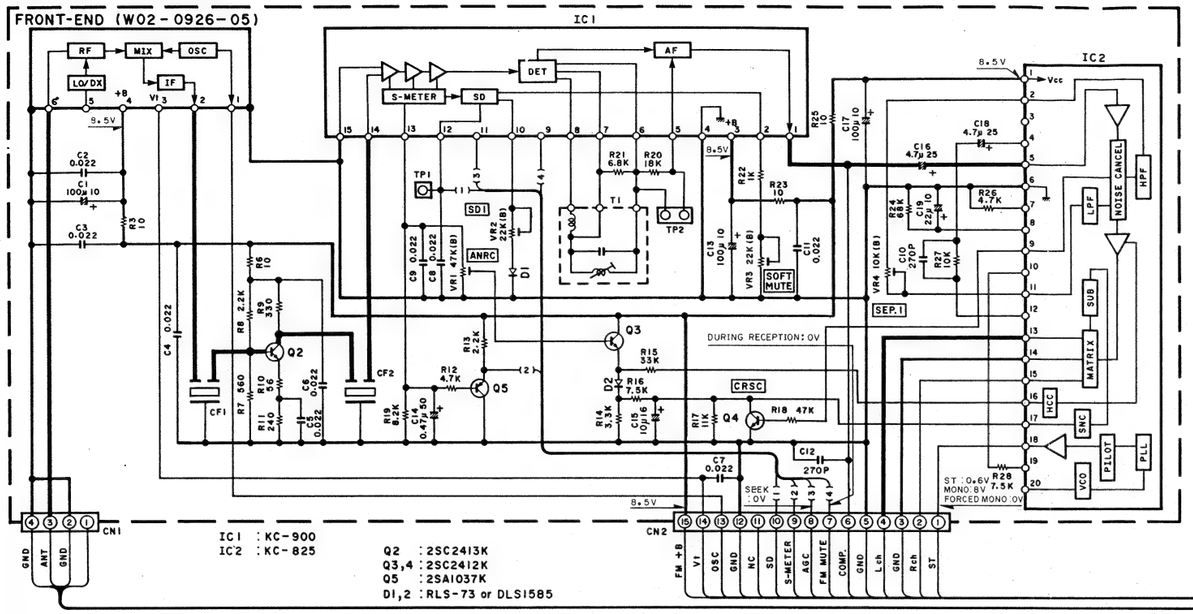
X25-336X-XX

DISTINATION \ Ref, NO	2-70(E)	J1	J2,3	SW3
KRC-666D	2-70(E)	NO	YES	SDK
KRC-666L	2-71(E,T,EF)	YES	NO	MONO



Refer to the schematic diagram for the values of resistors and capacitors.

(X86-1092-70) 666D(E) / 666L(E,T)
(X86-1092-71) 666L(EF)



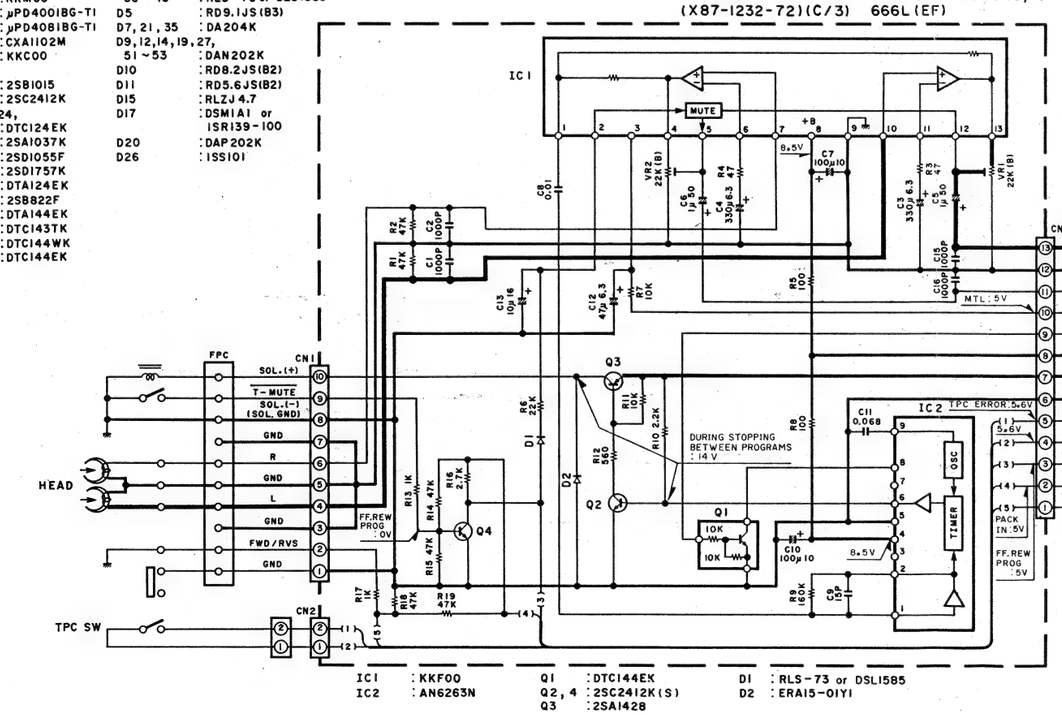
IC1 : KC-900
IC2 : KC-825

Q2 : 2SC2413K
Q3,4 : 2SC2412K
Q5 : 2SA1037K
D1,2 : RLS-73 or DLS1585

(X14-334X-XX)

IC1	: μPD1719G-551-11	D1~3,6,16	: ERA15-01Y1
IC2	: KK9000	D4,8,13,18,22~25,28,30~34, 36~45	: RLS-73 or DLS1585
IC3	: KKM00	D5	: RD9.1JS(B3)
IC4	: μPD4001BG-TI	D7,21,35	: DA204K
IC5,6	: μPD4081BG-TI	D9,12,14,19,27, 51~53	: DAN202K
IC7	: CXA1102M	D10	: RD8.2JS(B2)
IC8	: KKC00	D11	: RD5.6JS(B2)
Q1,7	: 2SB1015	D15	: RLZJ4.7
Q2,8,28,32,38	: 2SC2412K	D17	: DSM1A1 or ISR139-100
Q3,4,17~19,21,24, 26,34,42,46	: DTC124EK	D20	: DAP202K
Q5,22,23,44	: 2SA1037K	D26	: ISS101
Q6	: 2SD1055F		
Q9~14	: 2SD1757K		
Q15,16,20,43,45	: DTA124EK		
Q25,27,47,48	: 2SB822F		
Q29,31	: DTA144EK		
Q30	: DTC143TK		
Q33,37	: DTC144WK		
Q35,36	: DTC144EK		

(X87-1232-70)(C/3) 666D(E) / 666L(E,T)
(X87-1232-72)(C/3) 666L(EF)



IC1 : KKF00
IC2 : AN6263N

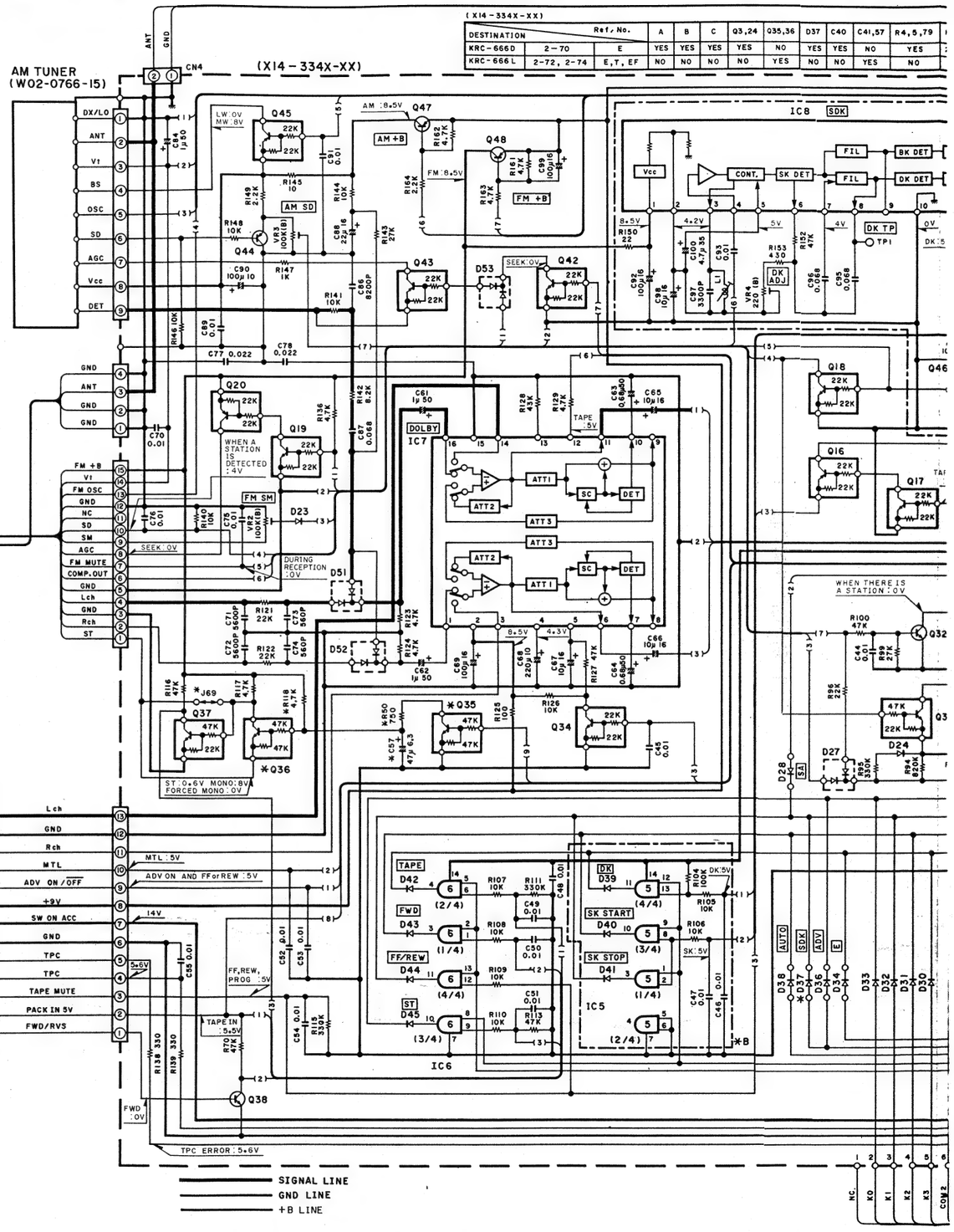
Q1 : DTC144EK
Q2, 4 : 2SC2412K(S)
Q3 : 2SA1428

D1 : RLS-73 or DSL1585
D2 : ERA15-01Y1

AM TUNER (W02-0766-15)

(X14-334X-XX)

DESTINATION	Ref. No.		A	B	C	Q3,24	Q35,36	D37	C40	C41,57	R4,5,79
KRC-666D	2~70	E	YES	YES	YES	YES	NO	YES	YES	NO	YES
KRC-666L	2-72, 2-74	E,T, EF	NO	NO	NO	NO	YES	NO	NO	YES	NO



SIGNAL LINE
GND LINE
+B LINE

CAUTION: For continued safety, replace safety critical components only with manufacturer's recommended parts (refer to parts list). ⚠ Indicates safety critical components. To reduce the risk of electric shock, leakage-current or resistance measurements shall be carried out (exposed parts are acceptably insulated from the supply circuit) before the appliance is returned to the customer.

- DC voltages are measured with a high impedance voltmeter. Values may vary slightly due to variations between individual instruments or/and units.
- Les tensions c.c. doivent être mesurées avec un voltmètre à haute impédance. Les valeurs peuvent différer légèrement du fait des variations inhérentes aux appareils et aux instruments de mesure individuels.
- Die angegebenen Gleichspar einem hochohmigen Voltmeter ken die Meßwerte aufgrund vo einzelnen Instrumenten oder Ger

C40	C41,57	R4,5,79	R33	R50,78,118
YES	NO	YES	2.2K	NO
NO	YES	NO	J	YES

(X87-1232-70/2-72)(B/3)

Q31 ~ 34, 37 : DTC144EK
Q35,36,38, 39 : 2SC2412K(S)

D31, 32 : DAN202K

IC1 CONTROLLER

IC2 P-CON OUTPUT CIRCUIT / PROTECTION CIRCUIT / P-CON INHIBIT

IC3 DIN AMP

IC4

(X13-6062-70)

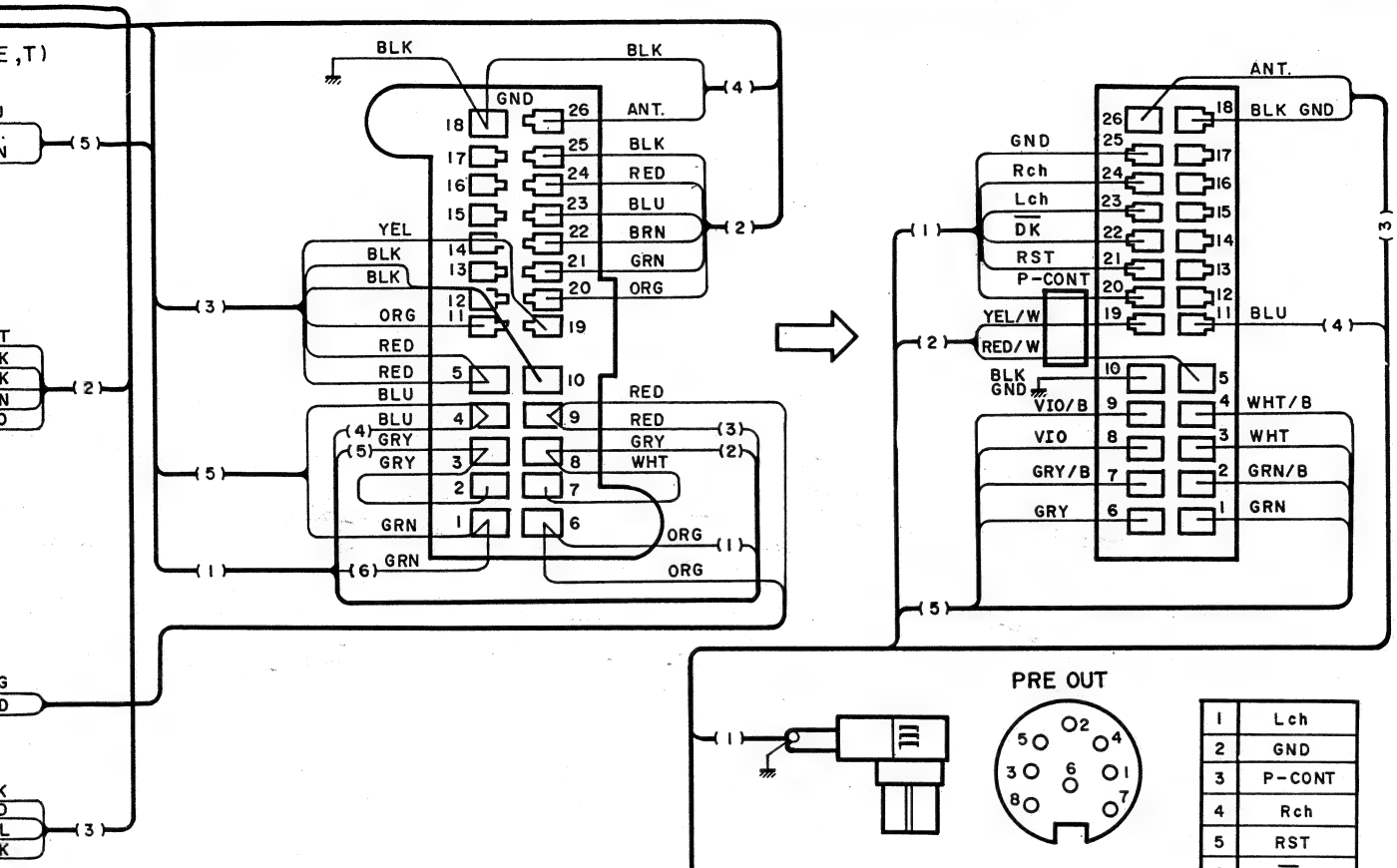
INVERTOR FOR CFL

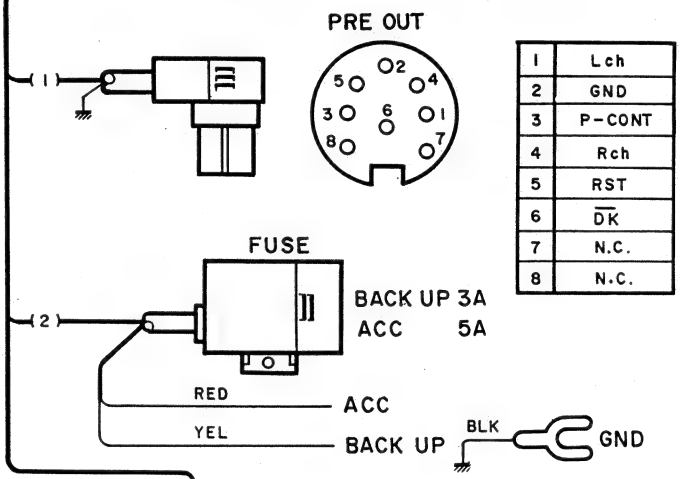
gegebenen Gleichspannungswerte wurden mit
nochohmigen Voltmeter gemessen. Dabei schwan-
Meßwerte aufgrund von Unterschieden zwischen
en Instrumenten oder Geräten u. U. geringfügig.

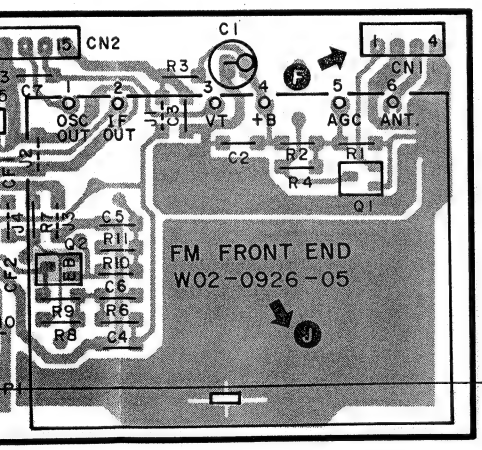
KRC-666D/L(E)(1/2)

KRC-666D/L

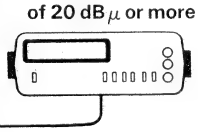
KENWOOD

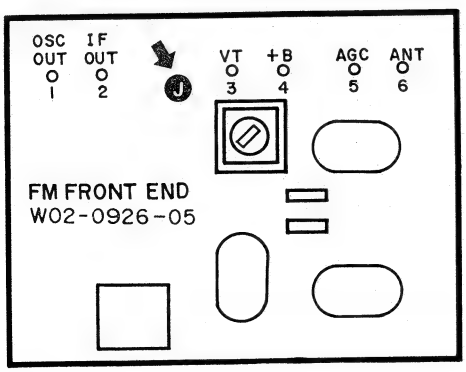


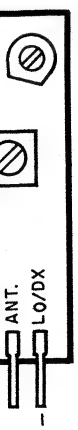


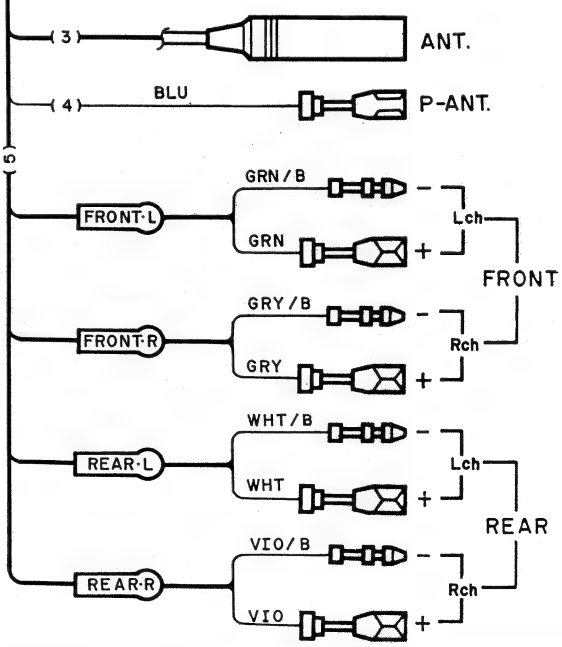


(b) SEEK STOP LEVEL
"H" level with input
of 20 dB μ or more









KRC-666D (E)
KRC-666L (E,T,EF)

Refer to the schematic diagram for the values of resistors and capacitors.

de View)

GND
Rch
Lch
DK
RST
ONT
YEL/W
RED/W
BLK
GND
/B
/B
OUT
FUSE
3A
ACC
RED
UP
YEL
BLU
FRONT·L
FRONT·R
REAR·L
REAR·R

BLK
BLK
ANT.
GND
BLK
RED
BLU
BRN
GRN
ORG
YEL
BLK
BLK
ORG
RED
RED
BLU
RED
RED
GRY
WHT
BLU
GRY
GRY
GRN
ORG
ORG
GRN

(X09-2672-70/2-72)(A/2)

14.4V
IC2
C19 100μ 10
8V
CN5
L-OUT (-)
L-OUT (+)
C17 100μ 10
R15 51
C25 0.1
R19 2.2
R17 2.2
C23 0.1
C21 100μ 10
R13 180K
C15 0.047
R11 220
R22 10K
R21 10K
C27 100μ 16
13.5V
C28 0.1
C16 0.047
R12 220
C14 1500P
R14 180K
R16 51
C22 100μ 10
ON:14.4V
STBY:0V
C12 4.7μ 25
C18 100μ 10
CN3
R-OUT (+)
R-OUT (-)
8V
C20 100μ 10
C26 0.1
C24 0.1
STBY: 5V
47K
47K
Q11
POWER AMP.
R20 2.2
R18 2.2
STBY
BEEP
IC2 : AN7177
Q11 : DTC144EK

(X90-2732-70)

(X90-2722-70)

REAR PRE OUT
1 : Lch
2 : GND
3 : P.CON
4 : Rch
5 : RST
6 : DK (D ONLY)

FM FRONT-END (W02-0926-0
DX/LO
ANT
CI 33P
DI
C3 0.01
LI
C4 0~10P
D2
D3
C2 0.01
R2 100
R11 100K
R12
IC1 : μPC1276G
FET1 : 3SK126 or 3SK195
FET2 : 2SK302 or 2SK360

X86-1092-70 666D(E) 666L(E,T)
X86-1092-71 666L(EF)

C1
CN2
CN1
ANT
AGC
+B
VT
IF OUT
OSC OUT
R1
R2
C2
R4
Q1
R3
C7
C5
R10
Q2
R7
C6
R6
R9
R8
C4
FM FRONT END
W02-0962-05

(b) SEEK STOP LEVEL
"H" level with input
of 20 dBμ or more

ANT. AGC +B VT IF OUT OSC OUT
6 5 4 3 2 1
FM FRONT END
W02-0962-05

(X86-XX)

LCD
LCD25 LCD24 LCD26 LCD0/KS0 LCD1/KS1 LCD15 LCD6/KS6 LCD3/KS3 LCD5/KS5 LCD4/KS4 LCD7 LCD10 LCD9 LCD8 LCD11 LCD14 LCD13 LCD12 LCD17 LCD16 LCD23 LCD22 LCD20 LCD21 LCD19 LCD27 LCD18 LCD2/KS2 COM2 COM1

1-MTL 2-T.A 3-T.C DIRECT S5
4-DOLBY 5 6
7 8 9
0 FM AM
LOUD
S4 DOWN UP S1
AUTO S2
S3 SDK : KRC-66(D
MONO : KRC-66(L
*J3 *J2 *J1
PL2
PL4
TPC ERROR: 5.6V
D1 D2 D3 D4 D5 D6 D7
D8
LO/DX
ANT
+B GND TPC LCD27 LCD26 LCD25 LCD24 LCD23 LCD22 LCD21 LCD20 LCD19 LCD18 LCD17 LCD16 LCD15 LCD14 LCD13 LCD12 LCD11 LCD10 LCD9 LCD8 LCD7 LCD6/KS6 LCD5/KS5 LCD4/KS4 LCD3/KS3 LCD2/KS2 LCD1/KS1 LCD0/KS0 COM1 COM2 K3 K2 K1 K0 NC

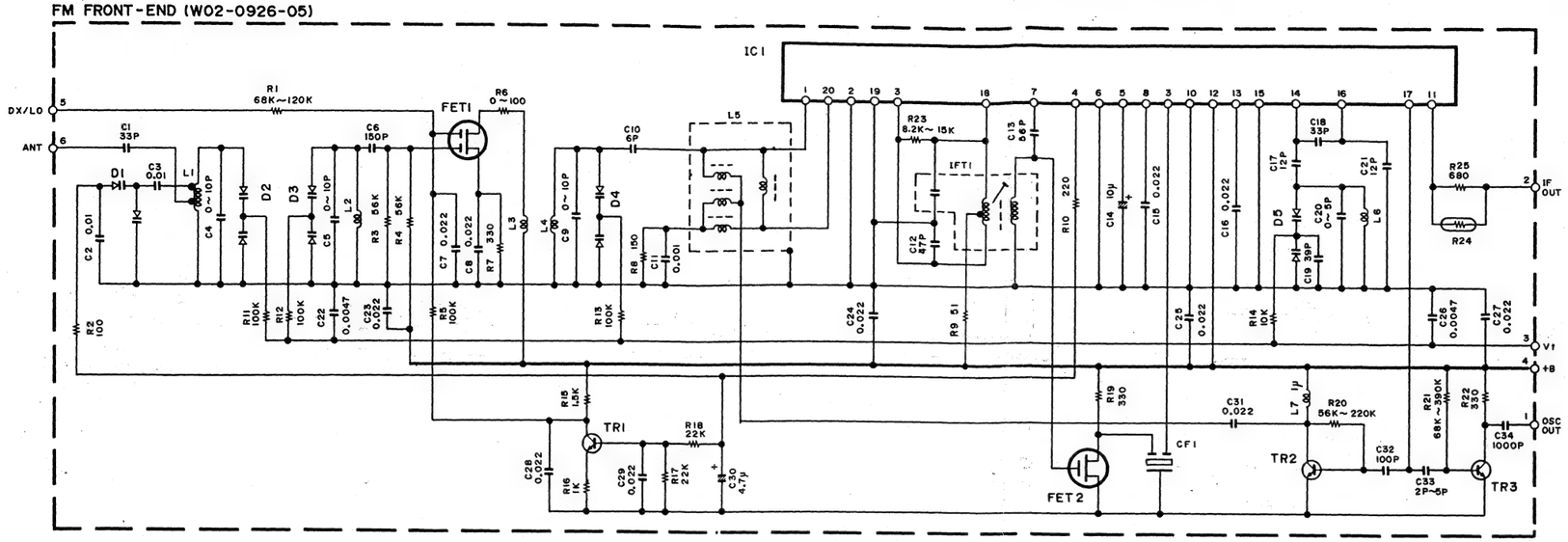
D1~7 : RLS-73 or DLS1585
D8 : B30-1130-05

(10) GND
(11) P-ANT
(4) R-Lch(-)
(3) R-Lch(+)
(2) F-Lch(-)
(1) F-Lch(+)
(9) R-Rch(-)
(8) R-Rch(+)
(7) F-Rch(-)
(6) F-Rch(+)
(18) ANT GND
(26) ANT

DC CODE
F1
ACC 5A
B.U 3A
F2
ACC B.U

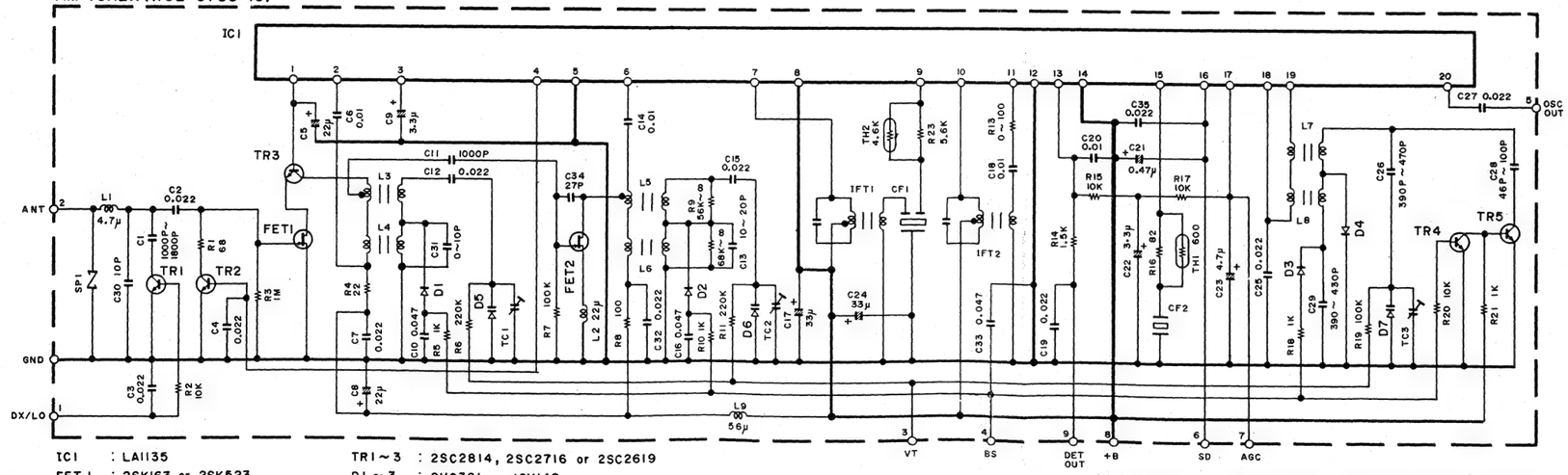
AM TUNER (W02-0766-15)
IC1
TR3
ANT
LI 4.7μ
C2 0.022
C1 1000P/1800P
C30 10P
SP1
TR1
TR2
R1 68
R3 1M
C4 0.022
FET1
GND
C3 0.022
R2 10K
DX/LO
IC1 : LA1135
FET1 : 2SK163 or 2SK523

FM FRONT-END (W02-0926-05)



IC1 : μPC1276G
FET1 : 3SK126 or 3SK195
FET2 : 2SK302 or 2SK360
TR1 : 2SC2712
TR2 : 2SC2714
TR3 : 2SC2715 or 2SC2714
D1 : 1SV172 or HVM14S
D2~5 : SVC212, HVM55 or 1SV225

AM TUNER (W02-0766-15)

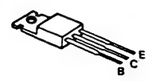


IC1 : LA1135
FET1 : 2SK163 or 2SK523
TR1~3 : 2SC2814, 2SC2716 or 2SC2619
D1~3 : SVC321 or 1SV149

(10) GND
(11) P-ANT
(4) R-Lch(-)
(3) R-Lch(+)
(2) F-Lch(-)
(1) F-Lch(+)
(9) R-Rch(-)
(8) R-Rch(+)
(7) F-Rch(-)
(6) F-Rch(+)
(18) ANT GND
(26) ANT

DC CODE
F1 ACC 5A
B.U 3A F2
ACC B.U

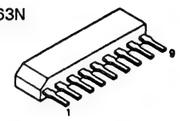
2SB1015



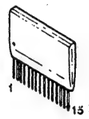
DTC143TK
2SA1428
2SB822F
2SD1055F

DTA124EK
DTA144EK
DTC124EK
DTC144EK
DTC144WK
2SA1037K
2SC2412K
2SC2413K
2SD1757K

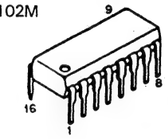
AN6263N



KC-900

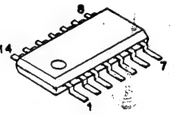


CXA1102M

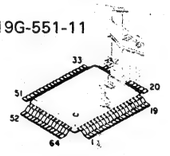


KC-819

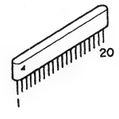
UPD4001BG-T1
UPD4081BG-T1



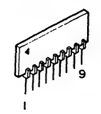
UPD1719G-551-11



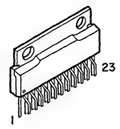
KC-825



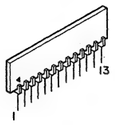
KKM00



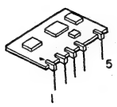
AN7177



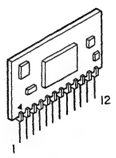
KKF00



KKQ00



KKC00

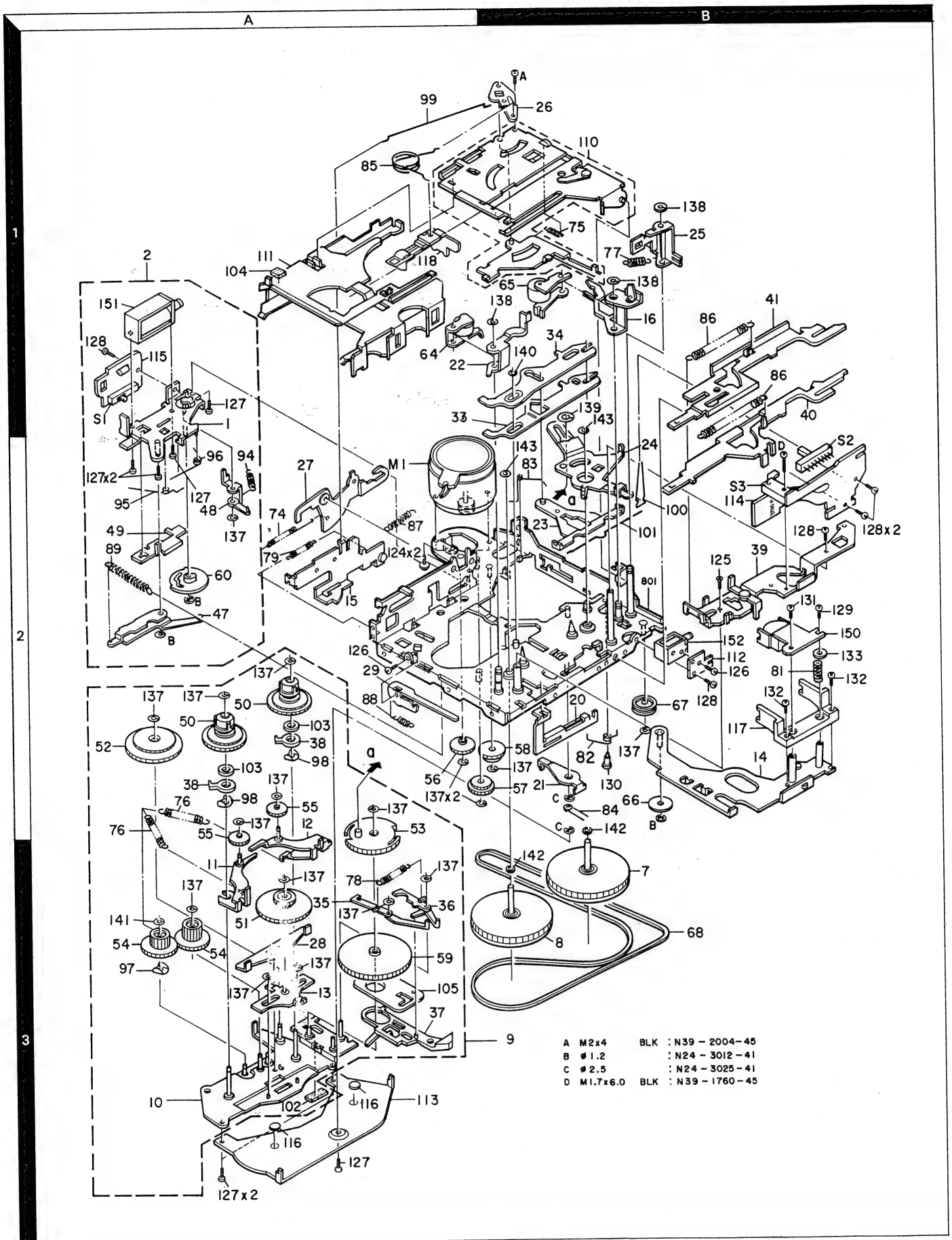


CAUTION: For continued safety, replace safety critical components only with manufacturer's recommended parts (refer to parts list). ⚠ Indicates safety critical components. To reduce the risk of electric shock, leakage-current or resistance measurements shall be carried out (exposed parts are acceptably insulated from the supply circuit) before the appliance is returned to the customer.

- DC voltages are measured with a high impedance voltmeter. Values may vary slightly due to variations between individual instruments or/and units.

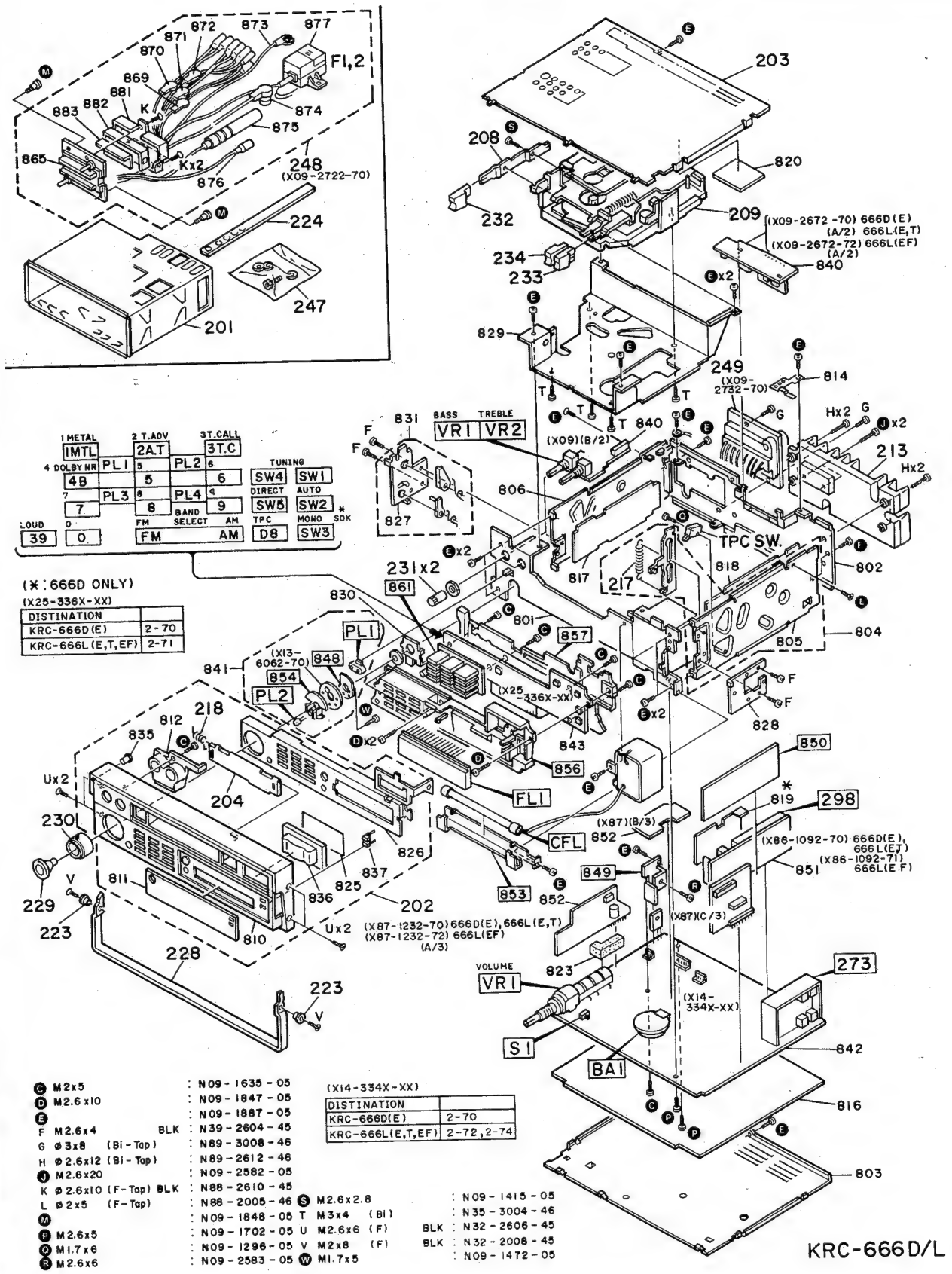
KRC-666D/L
KENWOOD

EXPLODED VIEW (MECHANISM UNIT)



Parts with the exploded numbers larger than 700 are not supplied.

EXPLODED VIEW (MAIN UNIT)



Parts with the exploded numbers larger than 700 are not supplied.

PARTS LIST

* New Parts
Parts without Parts No. are not supplied.
Les articles non mentionnes dans le Parts No. ne sont pas fournis.
Teile ohne Parts No. werden nicht geliefert.

Ref. No. 参照番号	Address 位置	New Parts 新	Parts No. 部品番号	Description 部品名／規格	Destination 仕向	Remarks 備考
KRC-666D/L						
201	1C	*	AD1-1563-21	METALLIC CABINET		
202	3C	*	A20-5551-02	PANEL ASSY	E	
202	3C	*	A20-5552-02	PANEL ASSY	E1T EF	
203	1D	*	A52-0106-23	TOP PLATE		
204	2C	*	A53-1040-03	CASSETTE LID		
-			B46-0100-10	WARRANTY CARD		
-		*	B50-9036-00	INSTRUCTION MANUAL	EE1T	
-		*	B50-9038-00	INSTRUCTION MANUAL	EF	
-			B58-0803-13	CAUTION CARD (FTZ)	EE1T	
-			B58-0854-14	CAUTION CARD (BTL)		
-			B58-0883-04	CAUTION CARD		
-			B58-0887-04	CAUTION CARD		
208	1C,1D		D10-1318-04	LEVER		
209	1D	*	D40-0816-05	CASSETTE MECHANISM ASSY		
213	2D	*	F01-1233-03	HEAT SINK		
F1	1C		F06-5024-05	FUSE (5A) ACC		
F2	1C		F06-3026-05	FUSE (3A) BACK UP		
217	2D		G01-2040-04	EXTENSION SPRING		
218	2C		G01-2044-04	TORSION COIL SPRING		
-		*	H01-7921-04	ITEM CARTON CASE	E	
-		*	H01-7922-04	ITEM CARTON CASE	E1T	
-		*	H01-7932-04	ITEM CARTON CASE	EF	
-			H10-3444-03	POLYSTYRENE FOAMED FIXTURE		
-		*	H10-3445-13	POLYSTYRENE FOAMED FIXTURE		
-			H25-0329-04	PROTECTION BAG		
-			H25-0336-04	PROTECTION BAG (170X250X0.03)		
223	3C	*	J31-0812-24	COLLAR		
224	1C		J54-0059-04	STAY		
-			J61-0067-05	WIRE BAND		
228	3C	*	K01-0084-23	HANDLE		
229	3C	*	K27-1899-03	KNOB (BUTTON) ON/OFF		
230	2C	*	K27-1900-03	KNOB (BUTTON) FADER		
231	2C		K27-1902-04	KNOB (BUTTON) B/T		
232	1C		K27-1903-04	KNOB (BUTTON) EJECT		
233	1D		K27-1904-04	KNOB (BUTTON) FF		
234	1D		K27-1905-04	KNOB (BUTTON) REW		
247	1C		N99-0099-05	SCREW SET		
C	3D		N09-1635-05	TAPTITE SCREW (2X5)		
D	2C		N09-1847-05	EVATITE SCREW (2.6X10)		
E	1D,2D		N09-1887-05	TAPTITE SCREW		
J	2D	*	N09-2582-05	TAPTITE SCREW (2.6X20)		
M	1C		N09-1848-05	STEPPED SCREW		
P	3D		N09-1702-05	TAPTITE SCREW (M2.6X5)		
Q	2D		N09-1296-05	MACHINE SCREW (M1.7X6)		
S	1D		N09-1415-05	MACHINE SCREW (M2.6X2.8)		
S1	3D		S46-1076-05	LEAF SWITCH		
BA1	3D		W09-0064-05	BATTERY		
248	1C	*	X90-2722-70	COMPOUND ASSY UNIT		

E: Scandinavia & Europe K: USA P: Canada
U: PX(Far East, Hawaii) T: England M: Other Areas
UE: AAFES(Europe) X: Australia

E: KRC-666D
E1, T, EF: KRC-666L

⚠ indicates safety critical components.

PARTS LIST

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Ref. No. 参照番号	Address 位置	New Parts 新	Parts No. 部品番号	Description 部品名／規格	Destination 仕向	Remarks 備考
249	1D	*	X90-2732-70	COMPOUND ASSY UNIT	EE1T	
249	1D	*	X90-2732-71	COMPOUND ASSY UNIT	EF	
AUDIO UNIT (X09-2672-70: E, T, 2-72: EF)						
C1 -6			CE04DW1E4R7M	ELECTRO 4.7UF 25WV		
C7			CE04DW1A101M	ELECTRO 100UF 10WV		
C11 ,12			CE04DW1E4R7M	ELECTRO 4.7UF 25WV		
C13 ,14			CK73FB1H152K	CHIP C 1500PF K		
C15 ,16			CF92FV1H473J	MF 0.047UF J		
C17 -22			CE04DW1A101M	ELECTRO 100UF 10WV		
C23 -26			CF92FV1H104J	MF 0.10UF J		
C27			CE04DW1C101M	ELECTRO 100UF 16WV		
C28			CK73EB1E104K	CHIP C 0.10UF K		
C29			C90-1404-05	ELECTRO 2200UF 16WV		
C30			CK41DY1C103M	CYLND CHIP C 0.010UF M		
LH1			J19-2826-05	HOLDER		
J1 -4			R92-0338-05	CLYND CHIP R 0 OHM		
J5			R92-0670-05	CHIP R 0 OHM		
J11 -14			R92-0338-05	CLYND CHIP R 0 OHM		
J15 ,16			R92-0670-05	CHIP R 0 OHM		
R1 ,2			RD41DB2B102J	CYLND CHIP R 1.0K J 1/8W		
R3 ,4			RK73FB2A182J	CHIP R 1.8K J 1/10W		
R5			RD41DB2B102J	CYLND CHIP R 1.0K J 1/8W		
R6			RK73FB2A102J	CHIP R 1.0K J 1/10W		
R7 ,8			RK73FB2A472J	CHIP R 4.7K J 1/10W		
R9			RD41DB2B101J	CYLND CHIP R 100 J 1/8W		
R11 ,12			RK73FB2A221J	CHIP R 220 J 1/10W		
R13 ,14			RK73FB2A184J	CHIP R 180K J 1/10W		
R15 ,16			RK73FB2A510J	CHIP R 51 J 1/10W		
R17 -20			RD41DB2B2R2J	CYLND CHIP R 2.2 J 1/8W		
R21 ,22			RK73FB2A103J	CHIP R 10K J 1/10W		
VR1 ,2	2C,2D		R10-4031-05	POTENTIOMETER (BASS/TREBLE)		
IC1			KC-819	IC(TONE AMP X2)		
IC2			AN7177	IC(POWER AMP X4)		
Q11			DTC144EK	DIGITAL TRANSISTOR		
SUB-CIRCUIT UNIT (X13-6062-70)						
PL1	2C		B30-1233-05	LAMP		
PL2	2C	*	B30-1234-05	LAMP (WITH CONNECTOR)		
W	2C		N09-1472-05	TAPTITE SCREW (1.7X5)		
SYNTHESIZER UNIT (X14-3342-70: E, 2-72: E1, T, 2-74: EF)						
C1 ,2			CK73FB1H103K	CHIP C 0.010UF K		
C3 -6			CE04DW1C102M	ELECTRO 1000UF 16WV		
C7			C90-1263-05	ELECTRO 100UF 16WV		
C8			CE04DW1A221M	ELECTRO 220UF 10WV		
C9			CE04DW1A471M	ELECTRO 470UF 10WV		
C10			C90-1263-05	ELECTRO 100UF 16WV		
C11 ,12			C90-0824-05	ELECTRO 1UF 50WV		
C13 ,14			C90-1263-05	ELECTRO 100UF 16WV		
C15			C90-0478-05	ELECTRO 10UF 16WV		
C16			CE04DW1A221M	ELECTRO 220UF 10WV		
C17 ,18		*	CE04MW1A220M	ELECTRO 22UF 10WV		
C19			C90-1263-05	ELECTRO 100UF 16WV		

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E: KRC-666D
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C20			CE04DW0J331M	ELECTRO	330UF	6.3WV		
C21 ,22			CE04MW1C100M	ELECTRO	10UF	16WV		
C23 ,24			C90-0506-05	ELECTRO	0.22UF	50WV		
C25			C90-0494-05	ELECTRO	22UF	6.3WV	D	
C26			CE04CW0J220M	ELECTRO	22UF	6.3WV		
C27			C90-0508-05	ELECTRO	2.2UF	50WV		
C28			CK73FB1H223K	CHIP C	0.022UF	K		
C29			CK73FB1H103K	CHIP C	0.010UF	K		
C30			C90-1263-05	ELECTRO	100UF	16WV		
C31			CK73FB1H103K	CHIP C	0.010UF	K		
C32			CC73FCH1H200J	CHIP C	20PF	J		
C33			CC73FCH1H180J	CHIP C	18PF	J		
C36			CK73EB1H473K	CHIP C	0.047UF	K		
C37 -39			CK73FB1H103K	CHIP C	0.010UF	K		
C40			CK73FB1H103K	CHIP C	0.010UF	K	D	
C41			CK73FB1H103K	CHIP C	0.010UF	K	L	
C42			CK73FB1H103K	CHIP C	0.010UF	K	D	
C43			C90-1263-05	ELECTRO	100UF	16WV		
C44 ,45			CK73FB1H103K	CHIP C	0.010UF	K		
C46 ,47			CK73FB1H103K	CHIP C	0.010UF	K	D	
C48 -55			CK73FB1H103K	CHIP C	0.010UF	K		
C57			C90-0495-05	ELECTRO	47UF	6.3WV	L	
C58			C92-0005-05	CHIP-TAN	2.2UF	6.3WV		
C59			CE04DW1E4R7M	ELECTRO	4.7UF	25WV		
C60			CK73FB1H103K	CHIP C	0.010UF	K		
C61 ,62			CE04MW1H010M	ELECTRO	1.0UF	50WV		
C63 ,64			C90-1245-05	ELECTRO	0.68UF	50WV		
C65 ,66			CE04MW1C100M	ELECTRO	10UF	16WV		
C67			C90-0478-05	ELECTRO	10UF	16WV		
C68			CE04DW1A221M	ELECTRO	220UF	10WV		
C69			C90-1263-05	ELECTRO	100UF	16WV		
C70			CK73FB1H103K	CHIP C	0.010UF	K		
C71 ,72			CK73FB1H562K	CHIP C	5600PF	K		
C73 ,74			CK73FB1H561K	CHIP C	560PF	K		
C75 ,76			CK73FB1H103K	CHIP C	0.010UF	K		
C77 ,78			CK73FB1H223K	CHIP C	0.022UF	K		
C83			C90-0831-05	ELECTRO	33UF	10WV		
C84			CE04DW1H010M	ELECTRO	1.0UF	50WV		
C86			CK73FB1H822K	CHIP C	8200PF	K		
C87			CK73EB1E683K	CHIP C	0.068UF	K		
C88			CE04CW1C220M	ELECTRO	22UF	16WV		
C89			CK73FB1H103K	CHIP C	0.010UF	K		
C90			CE04CW1A101M	ELECTRO	100UF	10WV		
C91			CK73FB1H103K	CHIP C	0.010UF	K		
C92			C90-1263-05	ELECTRO	100UF	16WV	D	
C93			CK73FB1H103K	CHIP C	0.010UF	K	D	
C94			C90-0478-05	ELECTRO	10UF	16WV	D	
C95 ,96			CF92FV1H683J	MF	0.068UF	J	D	
C97			CQ93HP2A332J	MYLAR	3300PF	J	D	
C98			C90-0478-05	ELECTRO	10UF	16WV	D	
C99			C90-1263-05	ELECTRO	100UF	16WV		
C100		*	CE04CW1V4R7M	ELECTRO	4.7UF	35WV		
C101			CK73EB1H223K	CHIP C	0.022UF	K		
LH1 ,2			J19-2826-05	HOLDER				

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D: KRC-666D
L: KRC-666L

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L1			L39-0156-05	TRAP COIL (DK LEVEL)	D	
L2			L39-0136-05	TRAP COIL		
X1			L77-0585-05	CRYSTAL RESONATOR(4.5MHZ)		
C	3D		N09-1635-05	TAPTITE SCREW (2X5)		
R	3D	*	N09-2583-05	TAPTITE SCREW (2.6X6)		
J1 -3			R92-0338-05	CLYND CHIP R 0 OHM		
J5			R92-0350-05	JUMPER WIRE (RESISTOR TYPE)		
J6 ,7			R92-0338-05	CLYND CHIP R 0 OHM		
J8 -10			R92-0350-05	JUMPER WIRE (RESISTOR TYPE)		
J11 -19			R92-0338-05	CLYND CHIP R 0 OHM		
J20 -24			R92-0350-05	JUMPER WIRE (RESISTOR TYPE)		
J25 ,26			R92-0338-05	CLYND CHIP R 0 OHM		
J27 -32			R92-0350-05	JUMPER WIRE (RESISTOR TYPE)		
J33 -35			R92-0338-05	CLYND CHIP R 0 OHM		
J36 -38			R92-0350-05	JUMPER WIRE (RESISTOR TYPE)		
J39			R92-0338-05	CLYND CHIP R 0 OHM		
J40 -46			R92-0350-05	JUMPER WIRE (RESISTOR TYPE)		
J47 ,48			R92-0338-05	CLYND CHIP R 0 OHM		
J50			R92-0338-05	CLYND CHIP R 0 OHM		
J51 ,52			R92-0350-05	JUMPER WIRE (RESISTOR TYPE)		
J53			R92-0338-05	CLYND CHIP R 0 OHM	D	
J54			R92-0338-05	CLYND CHIP R 0 OHM		
J55			R92-0338-05	CLYND CHIP R 0 OHM	D	
J56 -59			R92-0338-05	CLYND CHIP R 0 OHM		
J60 ,61			R92-0350-05	JUMPER WIRE (RESISTOR TYPE)		
J62			R92-0338-05	CLYND CHIP R 0 OHM		
J63 -66			R92-0350-05	JUMPER WIRE (RESISTOR TYPE)		
J67 ,68			R92-0338-05	CLYND CHIP R 0 OHM		
J69			R92-0350-05	JUMPER WIRE (RESISTOR TYPE)	D	
J70 -79			R92-0350-05	JUMPER WIRE (RESISTOR TYPE)		
J80			R92-0338-05	CLYND CHIP R 0 OHM		
J81			R92-0350-05	JUMPER WIRE (RESISTOR TYPE)		
J82			R92-0338-05	CLYND CHIP R 0 OHM		
J83			R92-0350-05	JUMPER WIRE (RESISTOR TYPE)		
J84 -94			R92-0338-05	CLYND CHIP R 0 OHM		
J95			R92-0338-05	CLYND CHIP R 0 OHM	D	
J97			R92-0338-05	CLYND CHIP R 0 OHM	L	
J101			R92-0350-05	JUMPER WIRE (RESISTOR TYPE)	L	
J102			R92-0338-05	CLYND CHIP R 0 OHM	D	
J103,104			R92-0338-05	CLYND CHIP R 0 OHM		
R1			RD41DB2B272J	CYLND CHIP R 2.7K J 1/8W		
R2			RD41FB2B472J	CYLND CHIP R 4.7K J 1/8W		
R3			RD41FB2B473J	CYLND CHIP R 47K J 1/8W		
R4			RD41FB2B473J	CYLND CHIP R 47K J 1/8W	D	
R5			RD41FB2B102J	CYLND CHIP R 1.0K J 1/8W	D	
R6			RD41FB2B103J	CYLND CHIP R 10K J 1/8W		
R7			RD41FB2B471J	CYLND CHIP R 470 J 1/8W		
R8			RD41FB2B223J	CYLND CHIP R 22K J 1/8W		
R9 -11			RD41FB2B104J	CYLND CHIP R 100K J 1/8W		
R12			RD14DB2H100J	SMALL-RD 10 J 1/2W		
R15			RD41DB2B392J	CYLND CHIP R 3.9K J 1/8W		
R16			RD41FB2B101J	CYLND CHIP R 100 J 1/8W		
R17 ,18		*	RD41FB2B754J	CYLND CHIP R 750K J 1/8W		
R19 ,20			RD41FB2B222J	CYLND CHIP R 2.2K J 1/8W		

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R21 ,22			RD41FB2B221J	CYLND CHIP R 220 J 1/8W		
R23			RD41FB2B821J	CYLND CHIP R 820 J 1/8W		
R24			RD41DB2B821J	CYLND CHIP R 820 J 1/8W		
R25 ,26			RD41FB2B472J	CYLND CHIP R 4.7K J 1/8W	D	
R27 ,28			RD41FB2B471J	CYLND CHIP R 470 J 1/8W		
R29 ,30			RD41FB2B473J	CYLND CHIP R 47K J 1/8W		
R31 ,32			RD41FB2B222J	CYLND CHIP R 2.2K J 1/8W		
R33			RD41FB2B222J	CYLND CHIP R 2.2K J 1/8W	D	
R34			RD41FB2B103J	CYLND CHIP R 10K J 1/8W		
R35 ,36			RD41FB2B132J	CYLND CHIP R 1.3K J 1/8W		
R37 ,38			RD41FB2B332J	CYLND CHIP R 3.3K J 1/8W		
R39			RD41DB2B332J	CYLND CHIP R 3.3K J 1/8W		
R40			RD41FB2B332J	CYLND CHIP R 3.3K J 1/8W		
R41			RD41DB2B332J	CYLND CHIP R 3.3K J 1/8W		
R42			RD41FB2B332J	CYLND CHIP R 3.3K J 1/8W		
R43 ,44			RD41FB2B103J	CYLND CHIP R 10K J 1/8W		
R45 ,46			RD41FB2B472J	CYLND CHIP R 4.7K J 1/8W		
R47			RD41FB2B222J	CYLND CHIP R 2.2K J 1/8W		
R48 ,49			RD41FB2B104J	CYLND CHIP R 100K J 1/8W		
R50		*	RD41FB2B751J	CYLND CHIP R 750 J 1/8W	L	
R51 ,52			RD41DB2B103J	CYLND CHIP R 10K J 1/8W		
R53			RD41FB2B104J	CYLND CHIP R 100K J 1/8W		
R54			RD41FB2B471J	CYLND CHIP R 470 J 1/8W		
R55			RD41FB2B104J	CYLND CHIP R 100K J 1/8W		
R56			RD41FB2B474J	CYLND CHIP R 470K J 1/8W		
R57			RD14DB2H100J	SMALL-RD 10 J 1/2W		
R58			RD41DB2B472J	CYLND CHIP R 4.7K J 1/8W		
R59 -62			RD41FB2B223J	CYLND CHIP R 22K J 1/8W		
R63 ,64			RD41FB2B472J	CYLND CHIP R 4.7K J 1/8W		
R65			RD41FB2B513J	CYLND CHIP R 51K J 1/8W		
R66			RD41FB2B223J	CYLND CHIP R 22K J 1/8W		
R67			RD41FB2B222J	CYLND CHIP R 2.2K J 1/8W		
R68			RD41FB2B473J	CYLND CHIP R 47K J 1/8W		
R69			RD41FB2B102J	CYLND CHIP R 1.0K J 1/8W		
R70			RD41FB2B473J	CYLND CHIP R 47K J 1/8W		
R71			RD41FB2B331J	CYLND CHIP R 330 J 1/8W		
R72			RD41FB2B472J	CYLND CHIP R 4.7K J 1/8W		
R75 ,76			RD41DB2B472J	CYLND CHIP R 4.7K J 1/8W		
R77			RD41FB2B103J	CYLND CHIP R 10K J 1/8W		
R78			RD41FB2B473J	CYLND CHIP R 47K J 1/8W	L	
R79			RD41FB2B473J	CYLND CHIP R 47K J 1/8W	D	
R81 ,82			RD41FB2B473J	CYLND CHIP R 47K J 1/8W		
R83			RD41FB2B103J	CYLND CHIP R 10K J 1/8W		
R84			RD41FB2B472J	CYLND CHIP R 4.7K J 1/8W		
R85 -87			RD41FB2B103J	CYLND CHIP R 10K J 1/8W		
R88			RD41FB2B472J	CYLND CHIP R 4.7K J 1/8W		
R89			RD41FB2B103J	CYLND CHIP R 10K J 1/8W		
R91			RD41FB2B102J	CYLND CHIP R 1.0K J 1/8W		
R92 ,93			RD41DB2B103J	CYLND CHIP R 10K J 1/8W		
R94			RD41FB2B824J	CYLND CHIP R 820K J 1/8W		
R95			RD41FB2B334J	CYLND CHIP R 330K J 1/8W		
R96			RD41FB2B223J	CYLND CHIP R 22K J 1/8W		
R97			RD41DB2B473J	CYLND CHIP R 47K J 1/8W		
R98			RD41FB2B102J	CYLND CHIP R 1.0K J 1/8W		
R99			RD41FB2B273J	CYLND CHIP R 27K J 1/8W		

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R100			RD41FB2B473J	CYLND CHIP R 47K J 1/8W		
R104			RD41FB2B104J	CYLND CHIP R 100K J 1/8W	D	
R105,106			RD41DB2B103J	CYLND CHIP R 10K J 1/8W	D	
R107-110			RD41FB2B103J	CYLND CHIP R 10K J 1/8W		
R111			RD41FB2B334J	CYLND CHIP R 330K J 1/8W		
R113			RD41FB2B473J	CYLND CHIP R 47K J 1/8W		
R115			RD41FB2B334J	CYLND CHIP R 330K J 1/8W		
R116			RD41FB2B473J	CYLND CHIP R 47K J 1/8W		
R117			RD41FB2B472J	CYLND CHIP R 4.7K J 1/8W		
R118			RD41FB2B473J	CYLND CHIP R 47K J 1/8W	L	
R121,122		*	RD41FB2B223J	CYLND CHIP R 22K J 1/8W		
R123,124			RD41FB2B472J	CYLND CHIP R 4.7K J 1/8W		
R125			RD41FB2B101J	CYLND CHIP R 100 J 1/8W		
R126			RD41FB2B103J	CYLND CHIP R 10K J 1/8W		
R127			RD41FB2B473J	CYLND CHIP R 47K J 1/8W		
R128			RD41FB2B433J	CYLND CHIP R 43K J 1/8W		
R129			RD41FB2B472J	CYLND CHIP R 4.7K J 1/8W		
R136			RD41FB2B472J	CYLND CHIP R 4.7K J 1/8W		
R137			RD41FB2B101J	CYLND CHIP R 100 J 1/8W		
R138,139			RD41FB2B331J	CYLND CHIP R 330 J 1/8W		
R140,141			RD41FB2B103J	CYLND CHIP R 10K J 1/8W		
R142			RD41FB2B822J	CYLND CHIP R 8.2K J 1/8W		
R143			RD41FB2B273J	CYLND CHIP R 27K J 1/8W		
R144			RD41FB2B103J	CYLND CHIP R 10K J 1/8W		
R145			RD41FB2B100J	CYLND CHIP R 10 J 1/8W		
R146			RD41FB2B103J	CYLND CHIP R 10K J 1/8W		
R147			RD41FB2B102J	CYLND CHIP R 1.0K J 1/8W		
R148			RD41FB2B103J	CYLND CHIP R 10K J 1/8W		
R149			RD41FB2B222J	CYLND CHIP R 2.2K J 1/8W		
R150			RD41FB2B220J	CYLND CHIP R 22 J 1/8W	D	
R152			RD41FB2B473J	CYLND CHIP R 47K J 1/8W	D	
R153		*	RD41FB2B431J	CYLND CHIP R 430 J 1/8W	D	
R161-163			RD41FB2B472J	CYLND CHIP R 4.7K J 1/8W		
R164			RD41DB2B222J	CYLND CHIP R 2.2K J 1/8W		
VR1	3D		R24-3013-05	POTENTIOMETER (VOLUME)		
VR2 ,3			R12-5058-05	TRIMMING POT. (LEVEL)		
VR4			R12-0104-05	TRIMMING POT. (DK LEVEL)	D	
S1	3D		S31-2100-05	SLIDE SWITCH		
D1 -3			ERA15-01Y1	DIODE		
D4			DLS1585	DIODE		
D4			RLS-73	DIODE		
D5			RD9.1JS(B3)	ZENER DIODE		
D6			ERA15-01Y1	DIODE		
D7			DA204K	DIODE		
D8			DLS1585	DIODE		
D8			RLS-73	DIODE		
D9			DAN202K	DIODE		
D10			RD8.2JS(B2)	ZENER DIODE		
D11			RD5.6JS(B2)	ZENER DIODE		
D12			DAN202K	DIODE		
D13			DLS1585	DIODE		
D13			RLS-73	DIODE		
D14			DAN202K	DIODE		

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D15			RLZJ4.7	DIODE		
D16			ERA15-01Y1	DIODE		
D17			DSM1A1	DIODE		
D17			1SR139-100	DIODE		
D18			DLS1585	DIODE		
D18			RLS-73	DIODE		
D19			DAN202K	DIODE		
D20			DAP202K	DIODE		
D21			DA204K	DIODE		
D22 -25			DLS1585	DIODE		
D22 -25			RLS-73	DIODE		
D26			1SS101	DIODE		
D27			DAN202K	DIODE		
D28			DLS1585	DIODE		
D28			RLS-73	DIODE		
D30 -34			DLS1585	DIODE		
D30 -34			RLS-73	DIODE		
D35			DA204K	DIODE		
D36			RLS-73	DIODE		
D36 -45			DLS1585	DIODE		
D37			RLS-73	DIODE	D	
D38			RLS-73	DIODE		
D39 -41			RLS-73	DIODE	D	
D42 -45			RLS-73	DIODE		
D51 -53			DAN202K	DIODE		
IC1		*	UPD1719G-551-11	IC(FREQ SYNTHESIZER PLL,CONT)		
IC2			KKQ00	IC(P-CON)		
IC3			KKM00	IC(DIN AMP)		
IC4		*	UPD4001BG-T1	IC(NOR X4)		
IC5		*	UPD4081BG-T1	IC(AND X4)	D	
IC6		*	UPD4081BG-T1	IC(AND X4)		
IC7		*	CXA1102M	IC(DOLBY B)		
IC8			KKC00	IC(SDK)	D	
Q1			2SB1015	TRANSISTOR		
Q2			2SC2412K	TRANSISTOR		
Q3			DTC124EK	DIGITAL TRANSISTOR	D	
Q4			DTC124EK	DIGITAL TRANSISTOR		
Q5			2SA1037K	TRANSISTOR		
Q6			2SD1055F	TRANSISTOR		
Q7			2SB1015	TRANSISTOR		
Q8			2SC2412K	TRANSISTOR		
Q9 ,10			2SD1757K	TRANSISTOR	D	
Q11 -14			2SD1757K	TRANSISTOR		
Q15 ,16			DTA124EK	DIGITAL TRANSISTOR		
Q17 -19			DTC124EK	DIGITAL TRANSISTOR		
Q20			DTA124EK	DIGITAL TRANSISTOR		
Q21			DTC124EK	DIGITAL TRANSISTOR		
Q22 ,23			2SA1037K	TRANSISTOR		
Q24			DTC124EK	DIGITAL TRANSISTOR	D	
Q25			2SB822F	TRANSISTOR		
Q26			DTC124EK	DIGITAL TRANSISTOR		
Q27			2SB822F	TRANSISTOR		
Q28			2SC2412K	TRANSISTOR		
Q29			DTA144EK	DIGITAL TRANSISTOR		
Q30			DTC143TK	DIGITAL TRANSISTOR		

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Q31			DTA144EK	DIGITAL TRANSISTOR		
Q32			2SC2412K	TRANSISTOR		
Q33			DTC144WK	DIGITAL TRANSISTOR		
Q34			DTC124EK	DIGITAL TRANSISTOR		
Q35 ,36			DTC144EK	DIGITAL TRANSISTOR	L	
Q37			DTC144WK	DIGITAL TRANSISTOR		
Q38			2SC2412K	TRANSISTOR		
Q42			DTC124EK	DIGITAL TRANSISTOR		
Q43			DTA124EK	DIGITAL TRANSISTOR		
Q44			2SA1037K	TRANSISTOR		
Q45			DTA124EK	DIGITAL TRANSISTOR		
Q46			DTC124EK	DIGITAL TRANSISTOR	D	
Q47 ,48			2SB822F	TRANSISTOR		
273	3D	*	W02-0766-15	TUNER ASSY		
SWITCH UNIT (X25-3362-70: E, 2-71 E1, T, EF)						
CFL	2D	*	B30-1238-05	FLUORESCENT TUBE ASSY		
D8			B30-1130-05	LED(SLH-38VC3)		
FL1	2D	*	B38-0110-15	LIQUID CRYSTAL		
PL1 -4	2C	*	B30-1232-05	LAMP		
C	3D		N09-1635-05	TAPTITE SCREW (2X5)		
J1			R92-0338-05	CLYND CHIP R 0 OHM	L	
J2 ,3			R92-0338-05	CLYND CHIP R 0 OHM	D	
J4			R92-0338-05	CLYND CHIP R 0 OHM		
S1 -5	2C		S40-1117-05	PUSH SWITCH		
D1 -4			DLS1585	DIODE		
D1 -4			RLS-73	DIODE		
D5			DLS1585	DIODE	L	
D5			RLS-73	DIODE	L	
D6 ,7			DLS1585	DIODE		
D6 ,7			RLS-73	DIODE		
FRONT-END UNIT (X86-1092-70: E, T, 2-71: EF)						
C1			CE04DW1A101M	ELECTRO 100UF 10WV		
C2 -7			CK41DF1E223Z	CYLND CHIP C 0.022UF Z		
C8 ,9			CK73FB1H223K	CHIP C 0.022UF K		
C10			CC73FSL1H271J	CHIP C 270PF J		
C11			CK73FB1H223K	CHIP C 0.022UF K		
C12			CC73FSL1H271J	CHIP C 270PF J		
C13			CE04DW1A101M	ELECTRO 100UF 10WV		
C14			CE04DW1HR47M	ELECTRO 0.47UF 50WV		
C15			CE04DW1C100M	ELECTRO 10UF 16WV		
C16			CE04DW1E4R7M	ELECTRO 4.7UF 25WV		
C17			CE04DW1A101M	ELECTRO 100UF 10WV		
C18			CE04DW1E4R7M	ELECTRO 4.7UF 25WV		
C19			CE04DW1A220M	ELECTRO 22UF 10WV		
CF1 ,2			L72-0524-05	CERAMIC FILTER		
T1			L30-0472-05	FM IFT (DISCRIMINATOR)		
J1 -6			R92-0338-05	CLYND CHIP R 0 OHM		
J10 -16			R92-0670-05	CHIP R 0 OHM		
R3			RD41DB2B100J	CYLND CHIP R 10 J 1/8W		
R6			RD41DB2B100J	CYLND CHIP R 10 J 1/8W		
R7			RD41DB2B561J	CYLND CHIP R 560 J 1/8W		

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R8			RD41DB2B222J	CYLND CHIP R 2.2K J 1/8W		
R9			RD41DB2B331J	CYLND CHIP R 330 J 1/8W		
R10			RD41DB2B560J	CYLND CHIP R 56 J 1/8W		
R11			RD41DB2B241J	CYLND CHIP R 240 J 1/8W		
R12			RK73FB2A472J	CHIP R 4.7K J 1/10W		
R13			RK73FB2A222J	CHIP R 2.2K J 1/10W		
R14			RK73FB2A332J	CHIP R 3.3K J 1/10W		
R15			RK73FB2A333J	CHIP R 33K J 1/10W		
R16			RK73FB2A752J	CHIP R 7.5K J 1/10W		
R17			RK73FB2A113J	CHIP R 11K J 1/10W		
R18			RD41DB2B473J	CYLND CHIP R 47K J 1/8W		
R19			RK73FB2A822J	CHIP R 8.2K J 1/10W		
R20			RK73FB2A183J	CHIP R 18K J 1/10W		
R21			RK73FB2A682J	CHIP R 6.8K J 1/10W		
R22			*RK73FB2A102J	CHIP R 1.0K J 1/10W		
R23			RD41DB2B100J	CYLND CHIP R 10 J 1/8W		
R24			RK73FB2A683J	CHIP R 68K J 1/10W		
R25			RK73FB2A100J	CHIP R 10 J 1/10W		
R26			RK73FB2A472J	CHIP R 4.7K J 1/10W		
R27			RK73FB2A103J	CHIP R 10K J 1/10W		
R28			RK73FB2A752J	CHIP R 7.5K J 1/10W		
VR1			R12-3083-05	TRIMMING POT. (47K)ANRC		
VR2 ,3			R12-3101-05	TRIMMING POT. (22K)LEVEL		
VR4			R12-3071-05	TRIMMING POT. (10K)SEPARATION		
D1 ,2			DLS1585	DIODE		
D1 ,2			RLS-73	DIODE		
IC1			KC-900	IC(FM IF/DETECTION)		
IC2			KC-825	IC(NOISE CANCELLER/ MPX)		
Q2			2SC2413K	TRANSISTOR		
Q3 ,4			2SC2412K	TRANSISTOR		
Q5			2SA1037K	TRANSISTOR		
298	2D		W02-0926-05	FM FRONT-END ASSY		
PLAYBACK AMPLIFIER UNIT (X87-1230-11)						
C1 ,2			CK73FB1H102K	CHIP C 1000PF K		
C3 ,4		*	CE04DW0J331M	ELECTRO 330UF 6.3WV		
C5 ,6			CE04DW1H010M	ELECTRO 1.0UF 50WV		
C7			CE04DW1A101M	ELECTRO 100UF 10WV		
C8			CK73EB1H103K	CHIP C 0.010UF K		
C9			CC73FSL1H150J	CHIP C 15PF J		
C10			CE04DW1A101M	ELECTRO 100UF 10WV		
C11			CF92FV1H683J	MF 0.068UF J		
C12			CE04DW0J470M	ELECTRO 47UF 6.3WV		
C13			CE04DW1C100M	ELECTRO 10UF 16WV		
C15 ,16			CK73FB1H102K	CHIP C 1000PF K		
C21			CE04DW1C102M	ELECTRO 1000UF 16WV		
C22			CE04DW1E4R7M	ELECTRO 4.7UF 25WV		
C31			C92-0001-05	CHIP TAN 0.1UF 35WV		
C32			C92-0007-05	CHIP TAN 2.2UF 20WV		
C33			CK73EB1H223K	CHIP C 0.022UF K		
C34			CK73EB1H103K	CHIP C 0.010UF K		
C35			C92-0001-05	CHIP TAN 0.1UF 35WV		
C36			C92-0007-05	CHIP TAN 2.2UF 20WV		
C37			CK73EB1H223K	CHIP C 0.022UF K		

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J1 -5			R92-0338-05	CLYND CHIP R 0 OHM		
J6 ,7			R92-0670-05	CHIP R 0 OHM		
J10			R92-0338-05	CLYND CHIP R 0 OHM	EE1T	
J14			R92-0338-05	CLYND CHIP R 0 OHM	E2	
J21			R92-0338-05	CLYND CHIP R 0 OHM		
J31 ,32			R92-0338-05	CLYND CHIP R 0 OHM		
J33			R92-0670-05	CHIP R 0 OHM		
R1 ,2			RK73FB2A473J	CHIP R 47K J 1/10W		
R3 ,4			RK73FB2A470J	CHIP R 47 J 1/10W		
R5			RD41DB2B101J	CYLND CHIP R 100 J 1/8W		
R6			RD41DB2B223J	CYLND CHIP R 22K J 1/8W		
R7			RK73FB2A103J	CHIP R 10K J 1/10W		
R8			RD41DB2B101J	CYLND CHIP R 100 J 1/8W		
R9			RK73FB2A164J	CHIP R 160K J 1/10W		
R10			RK73FB2A222J	CHIP R 2.2K J 1/10W		
R11			RD41DB2B103J	CYLND CHIP R 10K J 1/8W		
R12			RD41DB2B561J	CYLND CHIP R 560 J 1/8W		
R13			RD41DB2B102J	CYLND CHIP R 1.0K J 1/8W		
R14 ,15			RK73FB2A473J	CHIP R 47K J 1/10W		
R16			RK73FB2A272J	CHIP R 2.7K J 1/10W		
R17			RD41DB2B102J	CYLND CHIP R 1.0K J 1/8W		
R18			RD41DB2B473J	CYLND CHIP R 47K J 1/8W		
R19			RK73FB2A473J	CHIP R 47K J 1/10W		
R21			RD41DB2B472J	CYLND CHIP R 4.7K J 1/8W		
R22			RD41DB2B222J	CYLND CHIP R 2.2K J 1/8W		
R23 -25			RD41DB2B103J	CYLND CHIP R 10K J 1/8W		
R31			RK73FB2A682J	CHIP R 6.8K J 1/10W		
R32 ,33			RK73FB2A152J	CHIP R 1.5K J 1/10W		
R34			RK73FB2A102J	CHIP R 1.0K J 1/10W		
R35			RK73FB2A152J	CHIP R 1.5K J 1/10W		
R36			RK73FB2A103J	CHIP R 10K J 1/10W		
R37			RK73FB2A682J	CHIP R 6.8K J 1/10W		
R38			RK73FB2A123J	CHIP R 12K J 1/10W		
R39			RK73FB2A103J	CHIP R 10K J 1/10W		
R40			RD41DB2B471J	CYLND CHIP R 470 J 1/8W		
VR1 ,2			R12-3101-05	TRIMMING POT. (22K)PLAY. B/LEVEL		
D1			DLS1585			
D1			RLS-73			
D2			ERA15-01Y1			
D22			DLS1585			
D22			RLS-73	DIODE		
D31 ,32			DAN202K	DIODE		
IC1			KKF00	IC(TAPE EQ)		
IC2			AN6263N	IC(DPSS BLANK DETECT)		
Q1			DTC144EK	DIGITAL TRANSISTOR		
Q2			2SC2412K(S)	TRANSISTOR		
Q3			2SA1428	TRANSISTOR		
Q4			2SC2412K(S)	TRANSISTOR		
Q21			2SA1428	TRANSISTOR		
Q22			2SC2412K(S)	TRANSISTOR		
Q23 ,24			DTC144EK	DIGITAL TRANSISTOR		
Q31 -34			DTC144EK	DIGITAL TRANSISTOR		
Q35 ,36			2SC2412K(S)	TRANSISTOR		
Q37			DTC144EK	DIGITAL TRANSISTOR		

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Ref. No. 参照番号	Address 位置	New Parts 新	Parts No. 部品番号	Description 部品名／規格	Destination 仕向	Remarks 備考
Q38 ,39			2SC2412K(S)	TRANSISTOR		
MECHANISM ASS'Y (D40-0816-05)						
1	1A	*	A11-0268-08	SUB CHASSIS ASSY		
2	1A	*	A11-0269-08	SUB CHASSIS ASSY		
7	3B	*	D01-0099-08	FLYWHEEL ASSY (F)		
8	3B	*	D01-0100-08	FLYWHEEL ASSY (R)		
9	3B	*	D03-0267-08	REEL DISK ASSY		
10	3A	*	D03-0268-08	REEL DISK		
11	3A	*	D10-2117-08	SLIDER ASSY (A)		
12	2A	*	D10-2118-08	SLIDER ASSY (B)		
13	3A	*	D10-2119-08	LEVER ASSY (FR)		
14	2B	*	D10-2120-08	LEVER ASSY (HEAD PLATE)		
15	2A	*	D10-2219-08	LEVER ASSY (EJECT)		
16	1B	*	D10-2122-08	LEVER ASSY (INV)		
20	2B	*	D10-2123-08	LEVER (FR CAM)		
21	2B	*	D10-2124-08	LEVER (FR CAM)		
22	1A	*	D10-2125-08	LEVER (FR CAM)		
23	2B	*	D10-2126-08	ARM		
24	2B	*	D10-2127-08	ARM		
25	1B	*	D10-2128-08	ARM (FR RELEASE)		
26	1B	*	D10-2130-08	LEVER (INV)		
27	2A	*	D10-2131-08	ARM (ACTION)		
28	3A	*	D10-2132-08	LEVER (SENSOR)		
29	2A	*	D10-2133-08	LEVER (LOCK PLATE)		
33	1A	*	D10-2134-08	LEVER		
34	1B	*	D10-2135-08	LEVER		
35	3A	*	D10-2136-08	ARM		
36	3A	*	D10-2137-08	ARM		
37	3A	*	D10-2138-08	LEVER (SENSOR)		
38	2A	*	D10-2139-08	LEVER (SENSOR)		
39	2B	*	D10-2140-08	LEVER (SINE PLATE)		
40	1B	*	D10-2141-08	LEVER (FR)		
41	1B	*	D10-2142-08	LEVER (FR)		
47	2A	*	D10-2145-08	ARM		
48	2A	*	D10-2146-08	LEVER ASSY		
49	2A	*	D10-2147-08	LEVER		
50	2A	*	D13-0685-08	GEAR ASSY (REEL DISK)		
51	3A	*	D13-0686-08	GEAR ASSY (FR GEAR)		
52	2A	*	D13-0687-08	GEAR ASSY (TAKE UP)		
53	2A	*	D13-0688-08	GEAR ASSY (SWITCHING)		
54	3A	*	D13-0689-08	GEAR (TAKE UP)		
55	2A	*	D13-0690-08	GEAR (TAKE UP)		
56	2A	*	D13-0691-08	GEAR (IDLE)		
57	2A	*	D13-0692-08	GEAR (IDLE)		
58	2B	*	D13-0693-08	GEAR (IDLE)		
59	3A	*	D13-0694-08	GEAR (SWITCHING)		
60	2A	*	D13-0695-08	GEAR (SETTING)		
64	1A	*	D14-0272-08	PINCH ROLLER ASSY (R)		
65	1B	*	D14-0273-08	PINCH ROLLER ASSY (F)		
66	2B	*	D14-0274-08	ROLLER (HEAD PLATE)		
67	2B	*	D15-0275-08	PULLEY (IDLE)		
68	3B	*	D16-0183-08	BELT		
74	2A	*	G01-2217-08	TENSION SPRING		

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75	1B	*	G01-2212-08	TENSION SPRING		
76	2A	*	G01-2213-08	TENSION SPRING		
77	1B	*	G01-2214-08	TENSION SPRING		
78	3A	*	G01-2215-08	TENSION SPRING		
79	2A	*	G01-2216-08	TENSION SPRING		
81	2B	*	G01-2221-08	COMPRESSION SPRING		
82	2B	*	G01-2222-08	TORSION COIL SPRING		
83	2B	*	G01-2223-08	TORSION COIL SPRING		
84	2B	*	G01-2224-08	TORSION COIL SPRING		
85	1A	*	G01-2225-08	TORSION COIL SPRING		
86	1B	*	G01-2226-08	TENSION SPRING		
87	2A	*	G01-2227-08	TENSION SPRING		
88	2A	*	G01-2228-08	TENSION SPRING		
89	2A	*	G01-2229-08	TENSION SPRING		
94	2A	*	G01-2230-08	TENSION SPRING		
95	2A	*	G01-2231-08	TORSION COIL SPRING		
96	2A	*	G01-2232-08	TORSION COIL SPRING		
97	3A	*	G02-0472-08	FLAT SPRING		
98	2A	*	G02-0473-08	FLAT SPRING		
99	1A	*	G09-0093-08	SPRING		
100	2B	*	G09-0094-08	SPRING		
101	2B	*	G09-0095-08	SPRING (PR)		
102	3A	*	G10-0129-08	FELT		
103	2A	*	G10-0130-08	FELT (FRICTION)		
104	2A	*	G11-1308-08	CUSHION		
105	3A	*	G16-0187-08	SHEET		
110	1B	*	J19-2989-18	HOLDER (ACTION PLATE)		
111	1A	*	J19-2990-08	HOLDER (CASSETTE CASE)		
112	2B	*	J19-2991-08	BRACKET		
113	3A	*	J21-5252-08	MOUNTING HARDWARE(FLYWHEEL)		
114	2B	*	J25-5896-08	PRINTED WIRING BOARD(FPC)		
115	1A	*	J25-5897-08	PRINTED WIRING BOARD		
116	3A	*	J30-0246-08	SPACER		
117	2B	*	J90-0609-08	TAPE GUIDE		
118	1A	*	J90-0610-18	CASSETTE GUIDE		
		*	N24-3012-41	E TYPE RETAINING RING		
		*	N24-3025-41	E TYPE RETAINING RING		
124	2A	*	N09-1999-08	SCREW (M2.6X3) MOTOR		
125	2B	*	N09-2000-08	SCREW (M2.6X4.5)		
126	2B	*	N09-2501-08	SCREW (M2X2)		
127	1A,3A	*	N09-2502-08	SCREW (M2X3)		
128	1A,2B	*	N09-2503-08	SCREW (M2X3)		
129	2B	*	N09-2505-08	SCREW		
130	2B	*	N09-2506-08	SCREW		
131	2B	*	N09-2507-08	SCREW		
132	2B	*	N09-2508-08	SCREW (M2X5)		
133	2B	*	N19-1133-08	FLAT WASHER (Ø2.1)		
137	2A,3A	*	N19-1134-08	FLAT WASHER (Ø1.25)		
138	1B	*	N19-1135-08	FLAT WASHER (Ø2.1)		
139	1B	*	N19-1136-08	FLAT WASHER (Ø3.1)		
140	1B	*	N19-1137-08	FLAT WASHER (Ø1.7)		
141	3A	*	N19-1138-08	FLAT WASHER		
142	2B,3B	*	N19-1144-08	FLAT WASHER (Ø2.1)		
143	1B,2B	*	N19-1145-08	FLAT WASHER (Ø1.9)		

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S1	1A	*	S31-3009-08	SLIDE SWITCH (NON SHORT TYPE)		
S2	2B	*	S31-3007-08	SLIDE SWITCH		
S3	2B	*	S46-1112-08	LEAF SWITCH		
150	2B	*	T31-0051-08	PLAYBACK HEAD		
151	1A	*	T91-0208-08	SOLENOID COIL		
152	2B	*	T94-0207-08	SOLENOID COIL		
M1	2A	*	T42-0491-08	MOTOR ASSY		
TUNER ASS'Y (W02-0766-15)						
D1 -4			1SS110	DIODE		
D1 -4			1SS563	DIODE		
D1 -4			1S1555	DIODE		
D5 -7			SVC321	DIODE		
D5 -7			1SV149	DIODE		
FET1			2SK163	FET		
FET2			2SK184	FET		
TR1 -5			2SC2620	TRANSISTOR		
TR1 -5			2SC2714	TRANSISTOR		
TR1 -5			2SC2814	TRANSISTOR		
FM FRONT-END (W02-0926-05)						
D1			HVM14S	DIODE		
D1			1SV172	DIODE		
D2 -5			HVM55	DIODE		
D2 -5			SVC212	DIODE		
D2 -5			1SV225	DIODE		
FET1			3SK126	FET		
FET1			3SK195	FET		
FET2			2SK302	FET		
FET2			2SK360	FET		
IC1			UPC1276G	IC(FM)		
TR1			2SC2712	TRANSISTOR		
TR2			2SC2714	TRANSISTOR		
TR3			2SC2714	TRANSISTOR		
TR3			2SC2715	TRANSISTOR		
SCREW SET (N99-0099-05)						
-			N09-0335-05	SCREW (Ø5X16)		
-			N09-0366-05	HEX NUT (M5X20)		
-			N10-1050-46	HEX NUT (M5)		
-			N14-0117-05	FLUNGE NUT (M5)		

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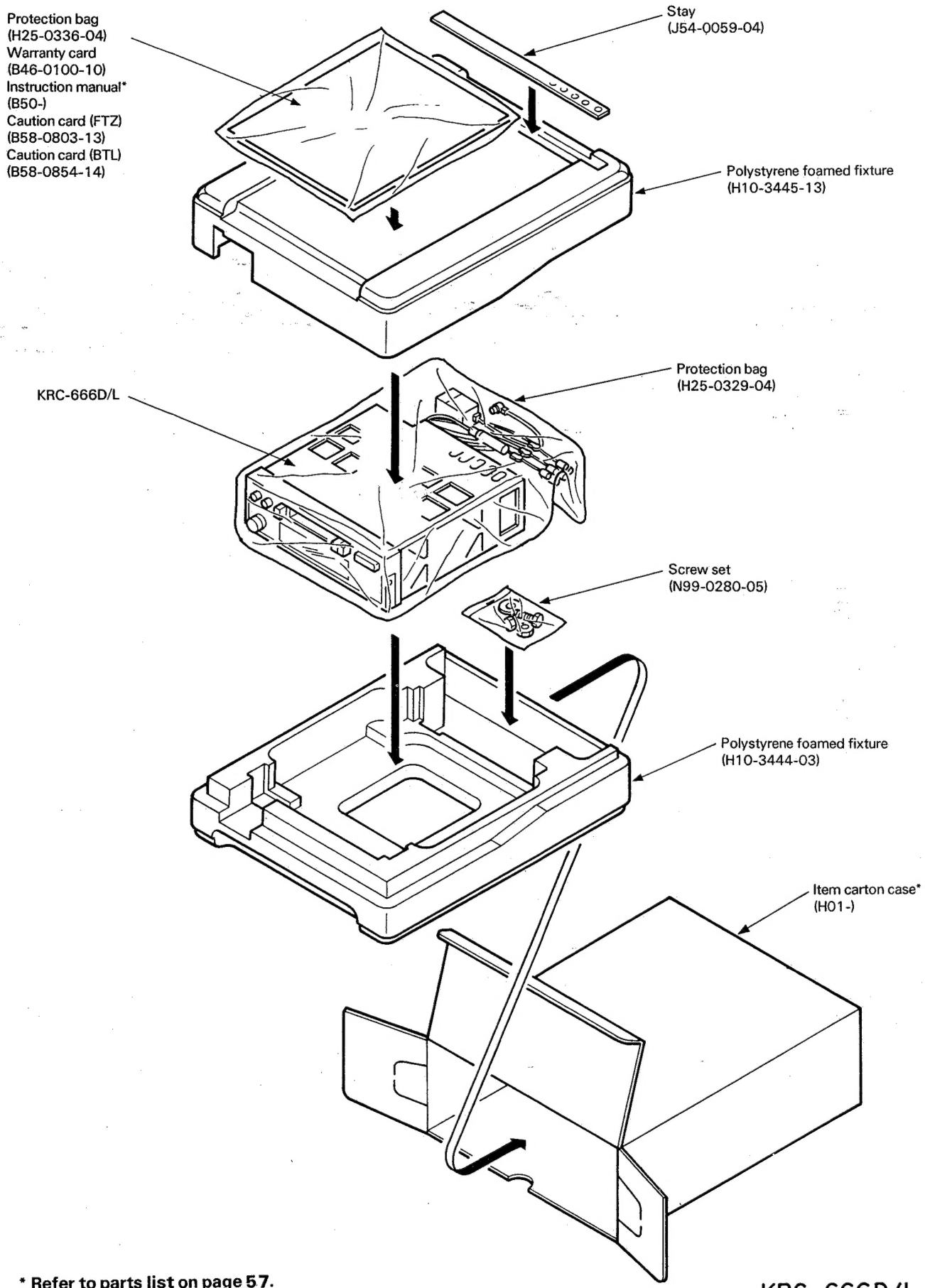
PACKING



* Refer to parts list on page 57.

KRC-666D/L

KRC-666D/L

SPECIFICATIONS

Specification subject to change without notice.

FM Tuner Section

Frequency Range	87.5 ~ 108.0 MHz
Usable Sensitivity (DIN)	1.1 μV/75 ohms
Stereo Sensitivity (S/N = 46 dB)	1.6 μV/75 ohms
Frequency Response (±4.5 dB)	30 ~ 15,000 Hz
Signal to Noise Ratio (IEC-A)	65 dB
Selectivity (DIN)	70 dB
Stereo Separation (1 kHz)	35 dB
19 kHz Carrier Leakage	65 dB

MW Tuner Section

MW Frequency Range	531 ~ 1,611 kHz
MW Usable Sensitivity	30 μV

LW Tuner Section

LW Frequency Range	153 ~ 281 kHz
LW Usable Sensitivity	60 μV

Cassette Deck Section

Tape Speed	4.76 cm/s
Wow and Flutter (WRMS)	0.08% (WRMS)
(DIN)	0.2% (W-PEAK)
Fast Winding Time (C-60)	100 sec
Frequency Response (120 μs)	30 Hz ~ 16 kHz (+4 dB, −6 dB)
(70 μs)	30 Hz ~ 18 kHz (+4 dB, −6 dB)
Stereo Separation (1 kHz)	43 dB
Signal to Noise Ratio (IEC-A)	
NR OFF	53 dB
Dolby-B	62 dB

Audio Section

Maximum Output Power (1 kHz, 4 ohms)	25 W + 25 W
Rated Output Power (10% THD, 1 kHz, 4 ohms)	20 W + 20 W
(1% THD, 1 kHz, 4 ohms)	15 W + 15 W
Tone Action	Bass: 100 Hz ±10 dB
	Treble: 10 kHz ±10 dB
Preout Level/Impedance	1,000 mV/180 ohms

General

Operating Voltage (GND)	14.4 V (11 ~ 16 V)
Current Consumption	4.5 A at Rated Power
Dimensions (W × H × D)	188 × 58 × 177 mm
Body Size (W × H × D)	182 × 52 × 159 mm
Weight	2.1 kg

Note:

Component and circuitry are subject to modification to insure best operation under differing local conditions. This manual is based on the Europe (E) standard, and provides information on regional circuit modification through use of alternate schematic diagrams, and information on regional component variations through use of parts list.

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